

BOXER-6404U

Fanless Embedded Box PC

User's Manual 2nd Ed

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Packing List

Before setting up your product, please make sure the following items have been shipped:

Item	Quantity
● BOXER-6404U	1
● Phoenix power connector	1
● Wallmount bracket	2

If any of these items are missing or damaged, please contact your distributor or sales representative immediately.

About this Document

This User's Manual contains all the essential information, such as detailed descriptions and explanations on the product's hardware and software features (if any), its specifications, dimensions, jumper/connector settings/definitions, and driver installation instructions (if any), to facilitate users in setting up their product.

Users may refer to the product page at AAEON.com for the latest version of this document.

Safety Precautions

Please read the following safety instructions carefully. It is advised that you keep this manual for future references

1. All cautions and warnings on the device should be noted.
2. All cables and adapters supplied by AAEON are certified and in accordance with the material safety laws and regulations of the country of sale. Do not use any cables or adapters not supplied by AAEON to prevent system malfunction or fires.
3. Make sure the power source matches the power rating of the device.
4. Position the power cord so that people cannot step on it. Do not place anything over the power cord.
5. Always completely disconnect the power before working on the system's hardware.
6. No connections should be made when the system is powered as a sudden rush of power may damage sensitive electronic components.
7. If the device is not to be used for a long time, disconnect it from the power supply to avoid damage by transient over-voltage.
8. Always disconnect this device from any AC supply before cleaning.
9. While cleaning, use a damp cloth instead of liquid or spray detergents.
10. Make sure the device is installed near a power outlet and is easily accessible.
11. Keep this device away from humidity.
12. Place the device on a solid surface during installation to prevent falls
13. Do not cover the openings on the device to ensure optimal heat dissipation.
14. Watch out for high temperatures when the system is running.
15. Do not touch the heat sink or heat spreader when the system is running
16. Never pour any liquid into the openings. This could cause fire or electric shock.

17. As most electronic components are sensitive to static electrical charge, be sure to ground yourself to prevent static charge when installing the internal components. Use a grounding wrist strap and contain all electronic components in any static-shielded containers.
18. If any of the following situations arises, please the contact our service personnel:
 - i. Damaged power cord or plug
 - ii. Liquid intrusion to the device
 - iii. Exposure to moisture
 - iv. Device is not working as expected or in a manner as described in this manual
 - v. The device is dropped or damaged
 - vi. Any obvious signs of damage displayed on the device
19. **DO NOT LEAVE THIS DEVICE IN AN UNCONTROLLED ENVIRONMENT WITH TEMPERATURES BEYOND THE DEVICE'S PERMITTED STORAGE TEMPERATURES (SEE CHAPTER 1) TO PREVENT DAMAGE.**

FCC Statement

Warning!



This device complies with Part 15 FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received including interference that may cause undesired operation.

Caution:

There is a danger of explosion if the battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions and your local government's recycling or disposal directives.

Attention:

Il y a un risque d'explosion si la batterie est remplacée de façon incorrecte. Ne la remplacer qu'avec le même modèle ou équivalent recommandé par le constructeur. Recycler les batteries usées en accord avec les instructions du fabricant et les directives gouvernementales de recyclage.

China RoHS Requirements (CN)

产品中有毒有害物质或元素名称及含量

AAEON System

QO4-381 Rev.A2

部件名称	有毒有害物质或元素					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (Cr(VI))	多溴联苯 (PBB)	多溴二苯醚 (PBDE)
印刷电路板 及其电子组件	×	○	○	○	○	○
外部信号 连接器及线材	×	○	○	○	○	○
外壳	○	○	○	○	○	○
中央处理器 与内存	×	○	○	○	○	○
硬盘	×	○	○	○	○	○
液晶模块	×	○	○	○	○	○
光驱	×	○	○	○	○	○
触控模块	×	○	○	○	○	○
电源	×	○	○	○	○	○
电池	×	○	○	○	○	○
本表格依据 SJ/T 11364 的规定编制。 ○：表示该有毒有害物质在该部件所有均质材料中的含量均在 GB/T 26572 标准规定的限量要求以下。 ×：表示该有害物质的某一均质材料超出了 GB/T 26572 的限量要求，然而该部件仍符合欧盟指令 2011/65/EU 的规范。 环保使用期限(EFUP (Environmental Friendly Use Period))：10 年 备注： 一、此产品所标示之环保使用期限，系指在一般正常使用状况下。 二、上述部件物质中央处理器、内存、硬盘、光驱、电源为选购品。 三、上述部件物质液晶模块、触控模块仅一体机产品适用。						

China RoHS Requirement (EN)

Name and content of hazardous substances in product

AAEON System

QO4-381 Rev.A2

Part Name	Hazardous Substances					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (Cr(VI))	多溴联苯 (PBB)	多溴二苯醚 (PBDE)
PCB Assemblies	×	○	○	○	○	○
Connector and Cable	×	○	○	○	○	○
Chassis	○	○	○	○	○	○
CPU and Memory	×	○	○	○	○	○
Hard Disk	×	○	○	○	○	○
LCD Modules	×	○	○	○	○	○
CD-ROM/DVD-ROM	×	○	○	○	○	○
Touch Modules	×	○	○	○	○	○
Power	×	○	○	○	○	○
Battery	×	○	○	○	○	○
The table is prepared in accordance with the provisions of SJ/T 11364. ○ : Indicates that said hazardous substance contained in all of the homogenous materials for this product is below the limit requirement of GB/T 26572. × : Indicates that said hazardous substance contained in at least one of the homogenous materials used for this part is above the limit requirement of GB/T 26572. But this product still be compliance with 2011/65/EU Directive (allowed with 2011/65/EU Annex III of RoHS exemption with number 6(c),7(a),7(c)-1). EFUP (Environment Friendly Use Period) value: 10 years. Notes: 1. This product defined period of use is under normal condition. 2. In above part, CPU/Memory/ Hard Disk/CD-ROM/DVD-ROM/ Power are optional. 3. In above part, LCD Modules/ Touch Modules are for all-in-one product model.						

Table of Contents

Chapter 1 - Product Specifications	1
1.1 Specifications	2
Chapter 2 – Hardware Information	5
2.1 Dimensions	6
2.2 Jumpers and connectors.....	7
2.3 List of Jumpers	8
2.3.1 Clear CMOS Jumper (JP2).....	9
2.3.2 Front Panel Connector (JP3)	9
2.3.3 Auto Power Button Enable/Disable Selection (JP4)	9
2.4 List of Connectors.....	10
2.4.1 LAN 1 (RJ-45) (CN4)	11
2.4.2 LAN 2 (RJ-45) (CN5)	11
2.4.3 Mini-Card Slot (Full-Mini Card) (CN6)	12
2.4.4 USB 2.0 Port 1 (CN9)	14
2.4.5 LPC Port (CN11)	15
2.4.6 Digital IO Port (CN15)	16
2.4.7 +5 V Output for SATA HDD (CN19)	16
2.4.8 SATA Port (CN20).....	17
2.4.9 External Power Input (CN39).....	17
2.4.10 Mini-Card Slot (Half-Mini Card) (CN40).....	18
2.4.11 UIM Card Socket (Push-Push type) (CN41)	20
2.4.12 USB3.0 x 1 (CN42).....	20
2.4.13 COM1 (CN43)	21
2.5 Installing DRAM	22
Chapter 3 - AMI BIOS Setup	24
3.1 System Test and Initialization	25

3.2	AMI BIOS Setup	26
3.3	Setup Submenu: Main	27
3.4	Setup Submenu: Advanced	28
3.4.1	Advanced: CPU Configuration	29
3.4.2	Advanced: IDE Configuration (IDE)	30
3.4.3	Advanced: USB Configuration	31
3.4.4	Advanced: Hardware Monitor	32
3.4.5	Advanced: Dynamic Digital IO Configuration	33
3.4.6	Power Management	34
3.4.7	Advanced: SIO Configuration	35
3.4.7.1	SIO Configuration: Serial Port 1 Configuration	36
3.5	Setup submenu: Chipset	37
3.5.1	Chipset: North Bridge	38
3.5.1.1	North Bridge: Display Control Configuration	39
3.5.2	Chipset: South Bridge	40
3.6	Setup submenu: Security	41
3.7	Setup submenu: Boot	42
3.8	Setup submenu: Save & Exit	43
Chapter 4	Drivers Installation	44
4.1	Driver Download and Installation	45
Appendix A	- Watchdog Timer Programming	49
A.1	Watchdog Timer Initial Program	50
A.2	Watchdog Sample Program	51
Appendix B	- I/O Information	55
B.1	I/O Address Map	56
B.2	Memory Address Map	57
B.3	IRQ Mapping Chart	58
Appendix C	- Digital I/O Ports	66

C.1 Electrical Specifications for Digital I/O Ports..... 67

C.2 DIO Programming..... 68

C.3 Digital I/O Register..... 69

C.4 Digital I/O Sample Program..... 70

Chapter 1

Product Specifications

1.1 Specifications

System

● Processor	Intel® Celeron® Processor J1900 (2M Cache, up to 2.42 GHz) Intel® Celeron® Processor N2807 (1M Cache, up to 2.16 GHz)
● System Memory	204-pin DDR3L 1333/1600 MHz SODIMM x 1, up to 8 GB (J1900) 204-pin DDR3L 1333/1600 MHz SODIMM x 1, up to 4 GB (N2807)
● Chipset	-
● Storage	Half size mSATA bay x 1 2.5" SATA HDD/SSD drive bay x 1
● Rear I/O Panel	USB type A x 1 for USB3.0 USB type A x 1 for USB2.0 DB-9 x 1 for RS-232 SIM card slot
● Front I/O Panel	RJ-45 x 2 for GbE LAN VGA x 1 Programmable 8-bit Digital I/O Power switch x 1 3 pin DC Power input x 1
● Front Indicators	LED for power status x 1 LED for HDD status x 1
● Expansion Slot	Full size mini PCIe slot x 1 Half size mini PCIe slot x 1 (for mSATA)

- OS Support
 - LPC wafer x 1
 - Windows® 10
 - Windows® 8.1
 - Windows® 7
 - Windows® Embedded Standard 8
 - Windows® Embedded Standard 7
 - Linux by Ubuntu

Mechanical

- Construction
 - Aluminum heatsink & Steel chassis
- Mounting
 - Wallmount
 - VSEA mounting kit (Optional)
 - Din-Rail mounting kit (Optional)
- Dimension (W x H x D)
 - 166 x 41.5 x 106.6 mm (6.54 x 1.63 x 4.20")
- Gross Weight
 -

Environmental

- Operating Temperature
 - 25 ~ 60°C (-13 ~ 140°F) - with 0.5 m/s airflow
- Storage Temperature
 - 30 ~ 80°C (-22 ~ 176°F)
- Operating Humidity
 - 5~95% @ 40°C, non-condensing
- Anti-Vibration
 - 5 Grms/ 5 ~ 500Hz Operation (mSATA)
- Anti-Shock
 -
- EMC
 - CE/FCC Class A

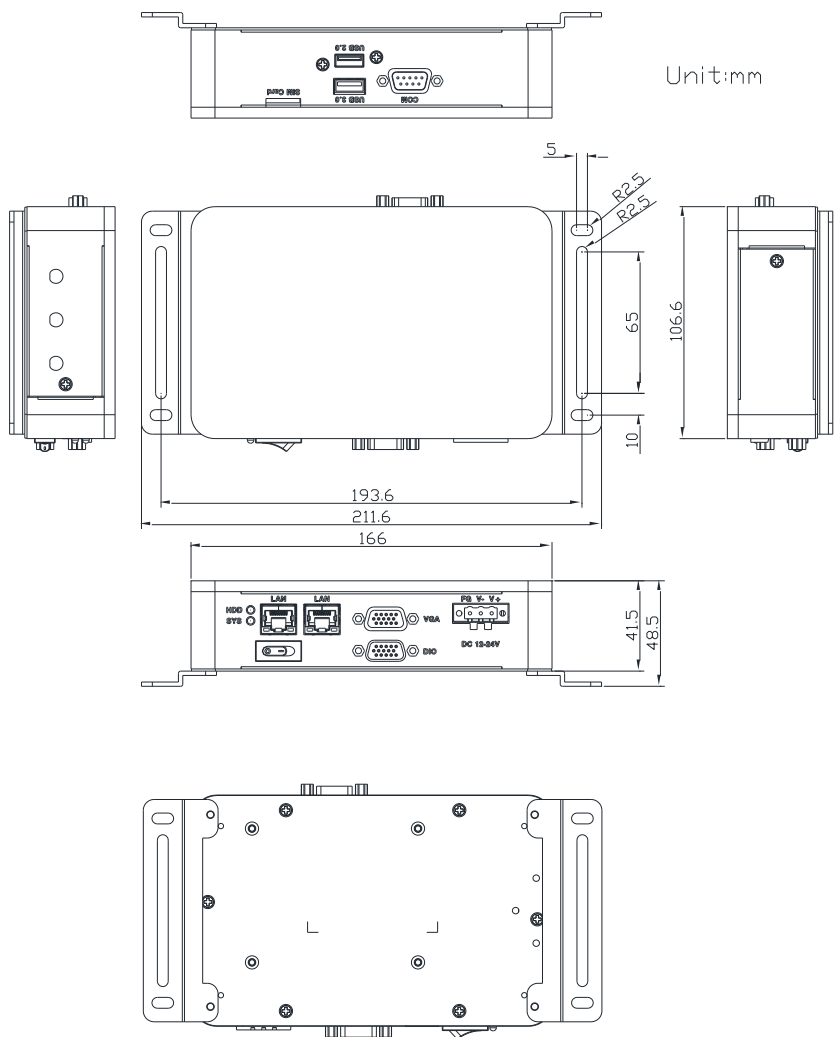
Power Supply

- **DC Input** 3-pin Phoenix DC Input 12~24V, optional
110/230V AC adapter.
ATX mode

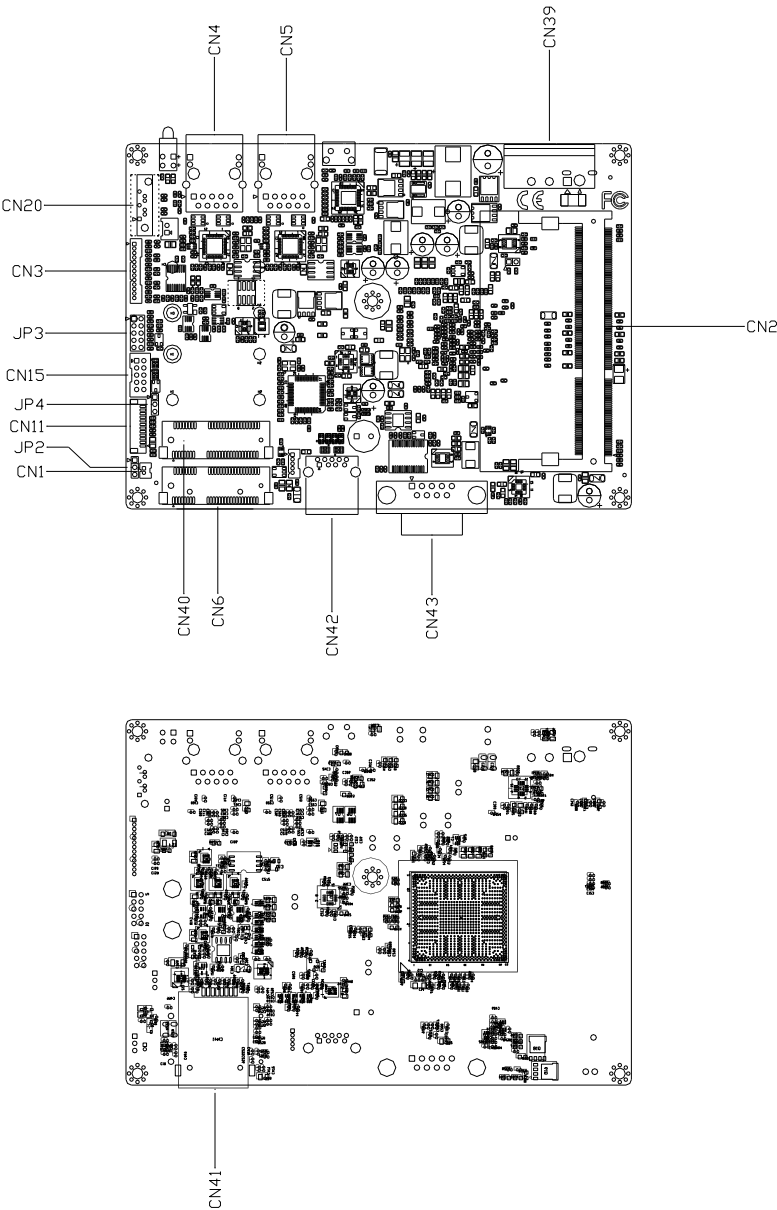
Chapter 2

Hardware Information

2.1 Dimensions



2.2 Jumpers and connectors

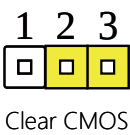
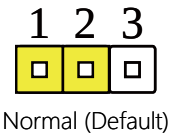


2.3 List of Jumpers

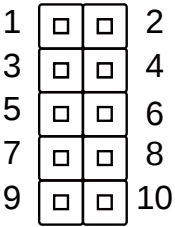
Please refer to the table below for all of the system's jumpers that you can configure for your application

Label	Function
JP2	Clear CMOS Jumper
JP3	Front Panel Connector
JP4	Auto Power Button Enable/Disable Selection

2.3.1 Clear CMOS Jumper (JP2)

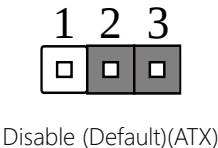
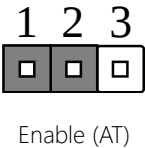


2.3.2 Front Panel Connector (JP3)



Pin	Pin Name	Pin	Pin Name
Pin 1	PWR_BTN-	Pin 2	PWR_BTN+
Pin 3	HDD_LED-	Pin 4	HDD_LED+
Pin 5	SPEAKER-	Pin 6	SPEAKER+
Pin 7	PWR_LED-	Pin 8	PWR_LED+
Pin 9	H/W RESET-	Pin 10	H/W RESET+

2.3.3 Auto Power Button Enable/Disable Selection (JP4)



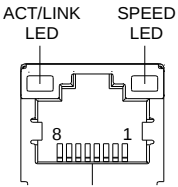
※ Enable Auto Power Button JP4 (1-2); Needs to use the power button JP4 (2-3) to power on the system.

2.4 List of Connectors

Please refer to the table below for all of the system’s connectors that you can configure for your application

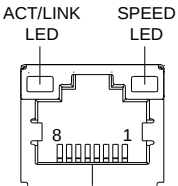
Label	Function
CN1	Battery connector
CN2	DDR3L SO-DIMM Slot
CN3	VGA
CN4	LAN1
CN5	LAN2
CN6	Mini-Card Slot (Full-Mini Card)
CN9	USB 2.0 X1
CN11	LPC Port
CN15	Digital IO Port
CN19	+5V Output for SATA HDD
CN20	SATA Port
CN39	External Power Input (Optional) (3P terminal)
CN40	Mini-Card Slot (Half-Mini Card)
CN41	UIM Card (push-push type)
CN42	USB 3.0 Ports 0
CN43	COM Port 1

2.4.1 LAN 1 (RJ-45) (CN4)



Pin	Pin Name	Signal Type	Signal Level
1	MDI0+	DIFF	
2	MDI0-	DIFF	
3	MDI1+	DIFF	
4	MDI2+	DIFF	
5	MDI2-	DIFF	
6	MDI1-	DIFF	
7	MDI3+	DIFF	
8	MDI3-	DIFF	

2.4.2 LAN 2 (RJ-45) (CN5)



Pin	Pin Name	Signal Type	Signal Level
1	MDI0+	DIFF	
2	MDI0-	DIFF	
3	MDI1+	DIFF	
4	MDI2+	DIFF	
5	MDI2-	DIFF	

Pin	Pin Name	Signal Type	Signal Level
6	MDI1-	DIFF	
7	MDI3+	DIFF	
8	MDI3-	DIFF	

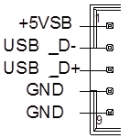
2.4.3 Mini-Card Slot (Full-Mini Card) (CN6)

Pin	Pin Name	Signal Type	Signal Level
1	PCIE_WAKE#	IN	
2	+3.3VSB	PWR	+3.3V
3	NC		
4	GND	GND	
5	NC		
6	+1.5V	PWR	+1.5V
7	PCIE_CLK_REQ#	IN	
8	UIM_PWR	PWR	
9	GND	GND	
10	UIM_DATA	I/O	
11	PCIE_REF_CLK-	DIFF	
12	UIM_CLK	IN	
13	PCIE_REF_CLK+	DIFF	
14	UIM_RST	IN	
15	GND	GND	
16	UIM_VPP	PWR	
17	NC		
18	GND	GND	
19	NC		

Pin	Pin Name	Signal Type	Signal Level
20	W_DISABLE#	OUT	+3.3V
21	GND	GND	
22	PCIE_RST#	OUT	+3.3V
23	PCIE_RX-	DIFF	
24	+3.3VSB	PWR	+3.3V
25	PCIE_RX+	DIFF	
26	GND	GND	
27	GND	GND	
28	+1.5V	PWR	+1.5V
29	GND	GND	
30	SMB_CLK	I/O	+3.3V
31	PCIE_TX-	DIFF	
32	SMB_DATA	I/O	+3.3V
33	PCIE_TX+	DIFF	
34	GND	GND	
35	GND	GND	
36	USB_D-	DIFF	
37	GND	GND	
38	USB_D+	DIFF	
39	+3.3VSB	PWR	+3.3V
40	GND	GND	
41	+3.3VSB	PWR	+3.3V
42	NC		
43	GND	GND	
44	NC		
45	NC		

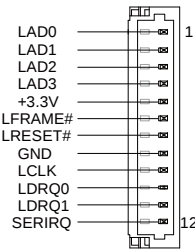
Pin	Pin Name	Signal Type	Signal Level
46	NC		
47	NC		
48	+1.5V	PWR	+1.5V
49	NC		
50	GND	GND	
51	NC		
52	+3.3VSB	PWR	+3.3V

2.4.4 USB 2.0 Port 1 (CN9)



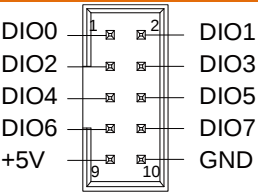
Pin	Pin Name	Signal Type	Signal Level
1	+5VSB	PWR	+5V
2	USB_D-	DIFF	
3	USB_D+	DIFF	
4	GND	GND	
5	GND	GND	

2.4.5 LPC Port (CN11)



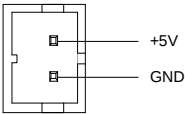
Pin	Pin Name	Signal Type	Signal Level
1	LAD0	I/O	+3.3V
2	LAD1	I/O	+3.3V
3	LAD2	I/O	+3.3V
4	LAD3	I/O	+3.3V
5	+3.3V	PWR	+3.3V
6	LFRAME#	IN	
7	LRESET#	OUT	+3.3V
8	GND	GND	
9	LCLK	OUT	
10	LDRQ0	IN	
11	LDRQ1	IN	
12	SERIRQ	I/O	+3.3V

2.4.6 Digital IO Port (CN15)



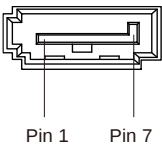
Pin	Pin Name	Signal Type	Signal Level
1	DIO0	I/O	+5V
2	DIO1	I/O	+5V
3	DIO2	I/O	+5V
4	DIO3	I/O	+5V
5	DIO4	I/O	+5V
6	DIO5	I/O	+5V
7	DIO6	I/O	+5V
8	DIO7	I/O	+5V
9	+5V	PWR	+5V
10	GND	GND	

2.4.7 +5 V Output for SATA HDD (CN19)



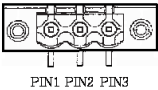
Pin	Pin Name	Signal Type	Signal Level
1	+V5S	PWR	+5V
2	GND	GND	

2.4.8 SATA Port (CN20)



Pin	Pin Name	Signal Type	Signal Level
1	GND	GND	
2	SATA_TXP1	DIFF	
3	SATA_TXN1	DIFF	
4	GND	GND	
5	SATA_RXN1	DIFF	
6	SATA_RXP1	DIFF	
7	GND	GND	

2.4.9 External Power Input (CN39)



Pin	Pin Name	Signal Type	Signal Level
1	VIN	PWR	+12V~+24V
2	GND	GND	
3	GND_EARTH	GND	

2.4.10 Mini-Card Slot (Half-Mini Card) (CN40)

Pin	Pin Name	Signal Type	Signal Level
1	PCIE_WAKE#	IN	
2	+3.3VSB	PWR	+3.3V
3	NC		
4	GND	GND	
5	NC		
6	+1.5V	PWR	+1.5V
7	NC		
8	NC		
9	GND	GND	
10	NC	I/O	
11	PCIE_REF_CLK-	DIFF	
12	NC	IN	
13	PCIE_REF_CLK+	DIFF	
14	NC		
15	GND	GND	
16	NC	PWR	
17	NC		
18	GND	GND	
19	NC		
20	W_DISABLE#	OUT	+3.3V
21	GND	GND	
22	PCIE_RST#	OUT	+3.3V
23	PCIE_RX-	DIFF	
24	+3.3VSB	PWR	+3.3V

Pin	Pin Name	Signal Type	Signal Level
25	PCIE_RX+	DIFF	
26	GND	GND	
27	GND	GND	
28	+1.5V	PWR	+1.5V
29	GND	GND	
30	SMB_CLK	I/O	+3.3V
31	PCIE_TX-	DIFF	
32	SMB_DATA	I/O	+3.3V
33	PCIE_TX+	DIFF	
34	GND	GND	
35	GND	GND	
36	USB_D-	DIFF	
37	GND	GND	
38	USB_D+	DIFF	
39	+3.3VSB	PWR	+3.3V
40	GND	GND	
41	+3.3VSB	PWR	+3.3V
42	NC		
43	GND	GND	
44	NC		
45	NC		
46	NC		
47	NC		
48	+1.5V	PWR	+1.5V
49	NC		
50	GND	GND	

Pin	Pin Name	Signal Type	Signal Level
51	NC		
52	+3.3VSB	PWR	+3.3V

2.4.11 UIM Card Socket (Push-Push type) (CN41)

Pin	Pin Name	Signal Type	Signal Level
1	UIM_PWR	PWR	
2	UIM_RST	IN	
3	UIM_CLK	IN	
4	GND	GND	
5	UIM_VPP	PWR	
6	UIM_DATA	I/O	

2.4.12 USB3.0 x 1 (CN42)

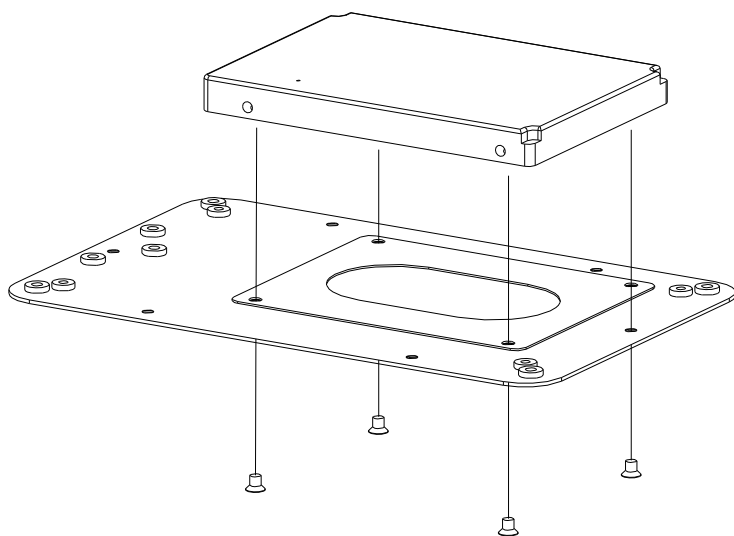
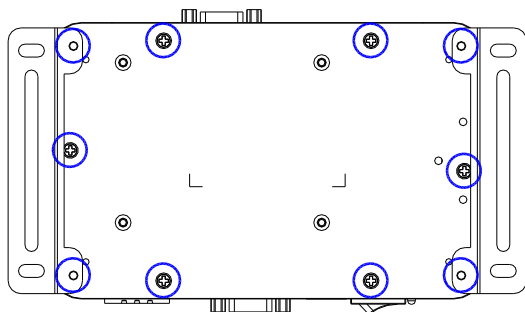
Pin	Pin Name	Signal Type	Signal Level
1	+5V	PWR	
2	USB_D-	DIFF	
3	USB_D+	DIFF	
4	GND	GND	
5	USB3.0 RX-	DIFF	
6	USB3.0 RX+	DIFF	
7	GND	GND	
8	USB3.0 TX-	DIFF	
9	USB3.0 TX+	DIFF	

2.4.13 COM1 (CN43)

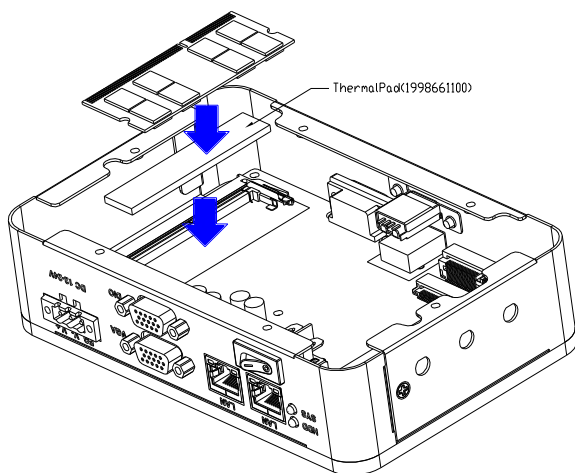
Pin	Pin Name	Signal Type	Signal Level
1	DCD		
2	RXD		
3	TXD		
4	DTR		
5	GND		
6	DSR		
7	RTS		
8	CTS		
9	RI		

2.5 Installing DRAM

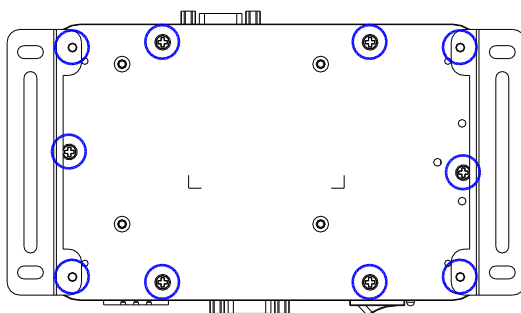
1. Remove the screws as shown below; then remove the cover.



- Put the thermal pad on between the chassis and the RAM, slot in the RAM diagonally into the slot and push down to secure.



- Re-tighten the screws



Chapter 3

AMI BIOS Setup

3.1 System Test and Initialization

The system uses certain routines to perform testing and initialization. If an error, fatal or non-fatal, is encountered, a few short beeps or an error message will be outputted. The board can usually continue the boot up sequence with non-fatal errors.

The system configuration verification routines check the current system configuration against the values stored in the CMOS memory. If they do not match, an error message will be outputted, in which case you will need to run the BIOS setup program to set the configuration information in memory.

There are three situations in which you will need to change the CMOS settings:

- You are starting your system for the first time
- You have changed your system's hardware
- The CMOS memory has lost power and the configuration information is erased

The system's CMOS memory uses a backup battery for data retention, which is to be replaced once emptied.

3.2 AMI BIOS Setup

The AMI BIOS ROM has a pre-installed Setup program that allows users to modify basic system configurations, which is stored in the battery-backed CMOS RAM and BIOS NVRAM so that the information is retained when the power is turned off.

To enter BIOS Setup, press or <F2> immediately while your computer is powering up.

The function for each interface can be found below.

Main – Date and time can be set here. Press <Tab> to switch between date elements

Advanced – Enable/ Disable boot option for legacy network devices

Chipset – For hosting bridge parameters

Boot – Enable/ Disable quiet Boot Option

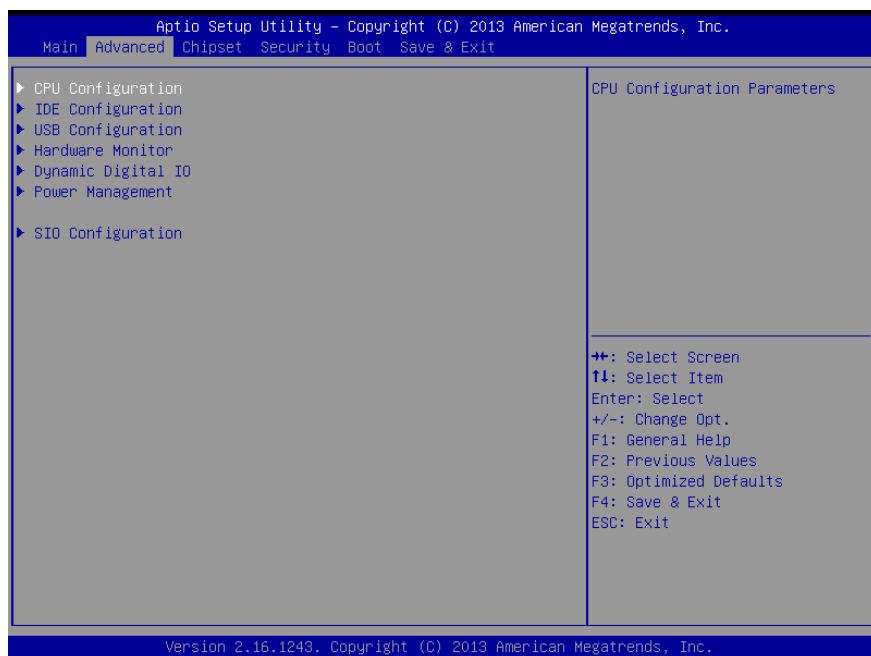
Security – The setup administrator password can be set here

Save & Exit – Save your changes and exit the program

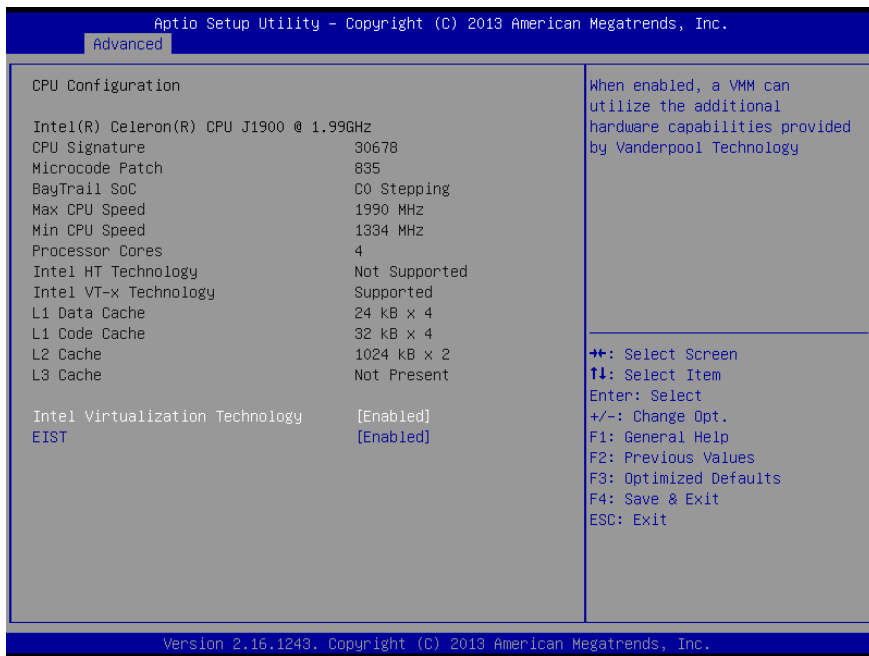
3.3 Setup Submenu: Main



3.4 Setup Submenu: Advanced



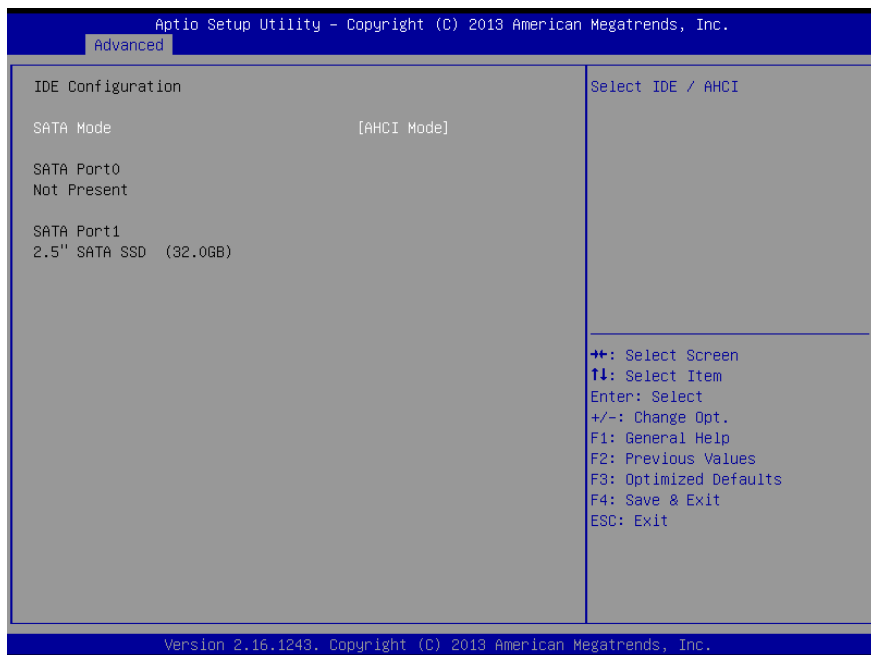
3.4.1 Advanced: CPU Configuration



Options summary:

Intel Virtualization Technology	Disabled	
	Enabled	Optimal Default, Failsafe Default
When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology.		
EIST	Disabled	
	Enabled	Optimal Default, Failsafe Default
Enable/Disable Intel SpeedStep		

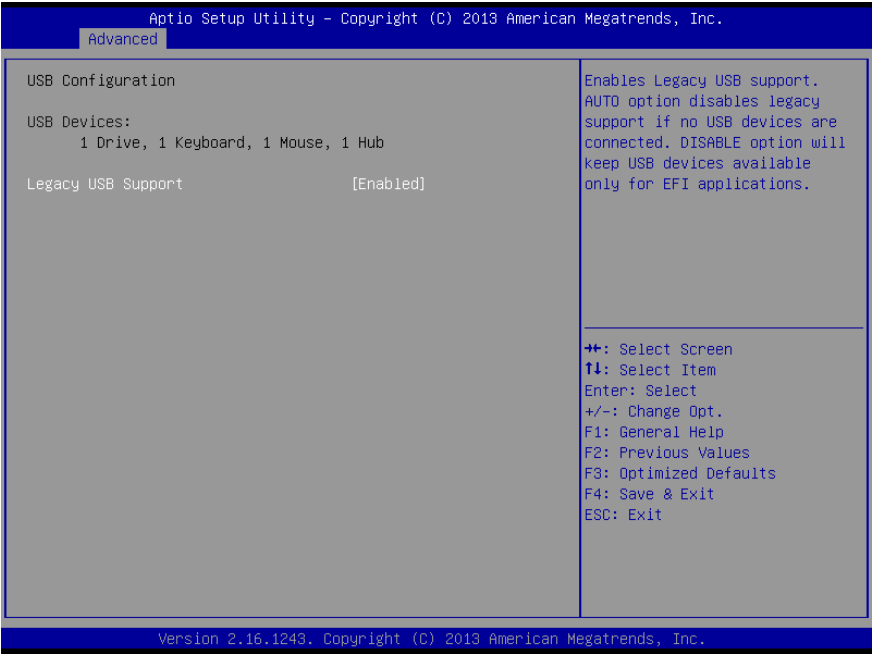
3.4.2 Advanced: IDE Configuration (IDE)



Options summary:

SATA Mode	IDE Mode	
	AHCI Mode	Optimal Default, Failsafe Default
Select IDE / AHCI		

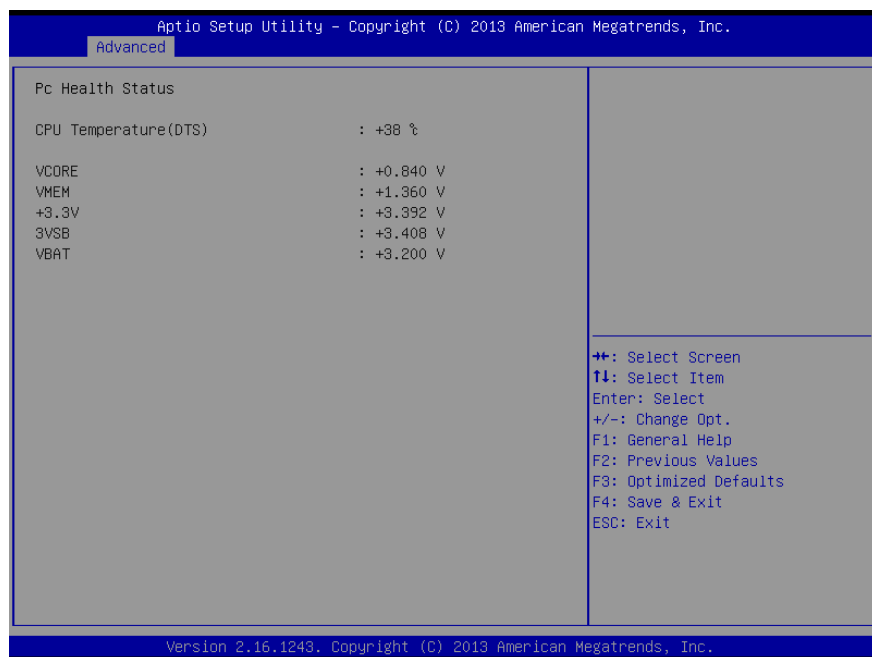
3.4.3 Advanced: USB Configuration



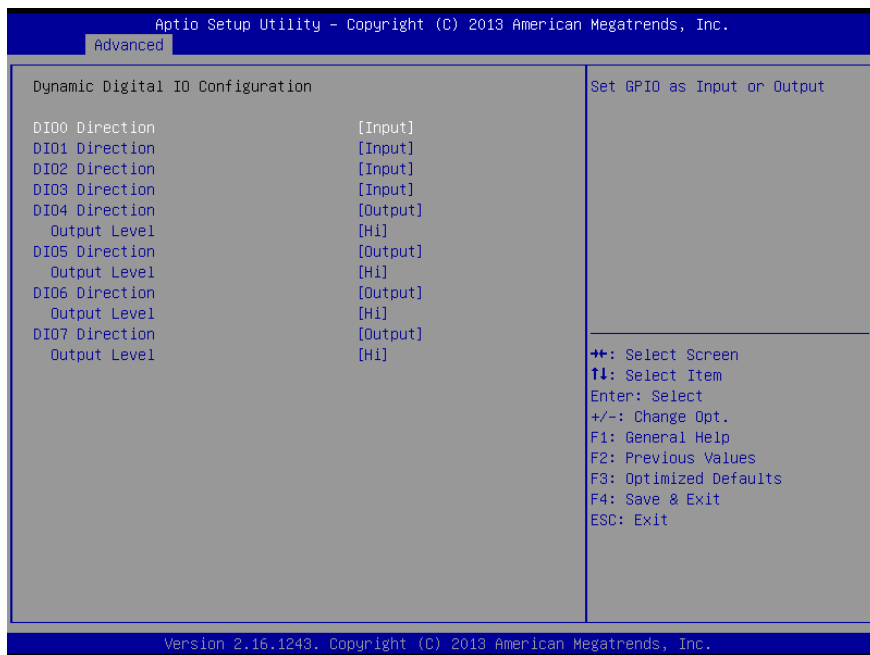
Options summary:

Legacy USB Support	Enabled	Optimal Default, Failsafe Default
	Disabled	
	Auto	
Enables BIOS Support for Legacy USB Support. When enabled, USB can be functional in legacy environment like DOS. AUTO option disables legacy support if no USB devices are connected		

3.4.4 Advanced: Hardware Monitor



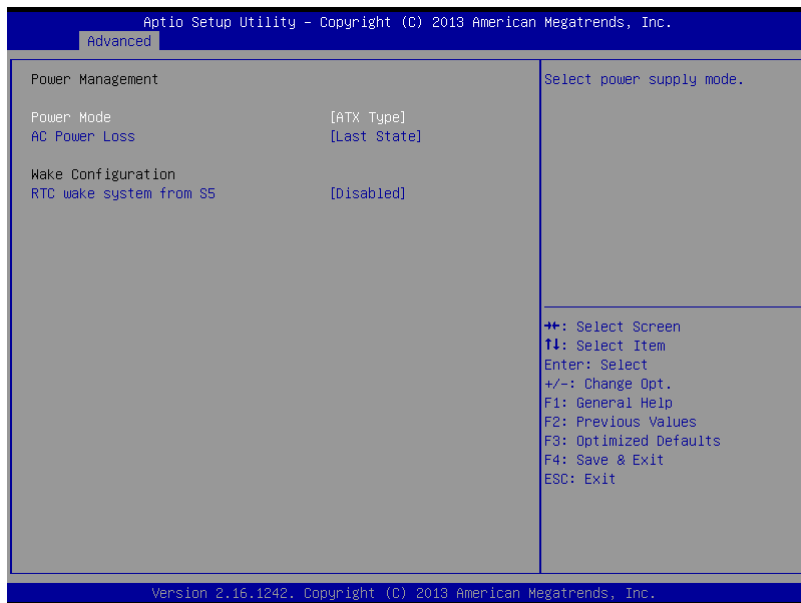
3.4.5 Advanced: Dynamic Digital IO Configuration



Options summary:

GPI[3:0] Direction	Input	Optimal Default, Failsafe Default
	Output	
Set GPIO as Input or Output		
GPI[3:0] Output Level	Hi	Optimal Default, Failsafe Default
	Low	
Set GPI[3:0] Output as Hi or Low		
GPO[3:0] Direction	Input	Optimal Default, Failsafe Default
	Output	
Set GPIO as Input or Output		
GPO[3:0] Output Level	Hi	Optimal Default, Failsafe Default
	Low	
Set GPO[3:0] Output as Hi or Low		

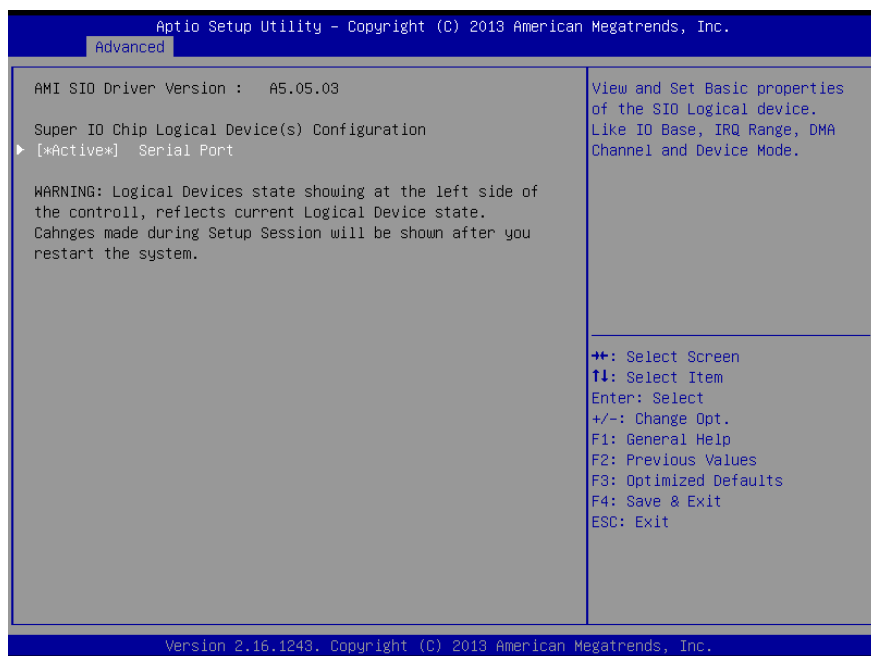
3.4.6 Power Management



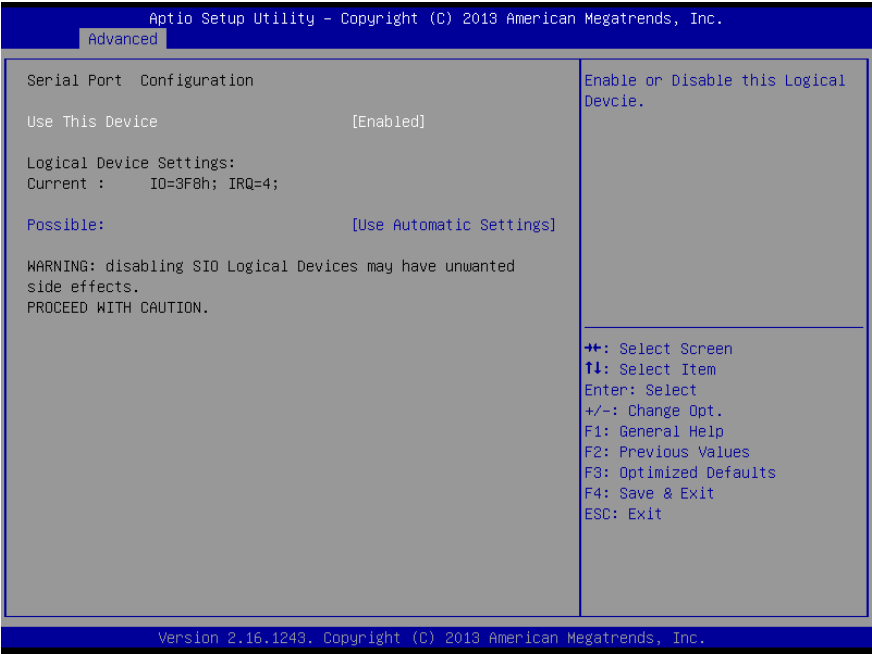
Options summary:

Power Mode	ATX Type	Optimal Default, Failsafe Default
	AT Type	
Select power supply mode.		
Restore AC Power Loss	Last State	Optimal Default, Failsafe Default
	Power On	
	Power Loss	
Select power state when power is re-applied after a power failure.		
Wake on Ring	Disabled	Optimal Default, Failsafe Default
	Enabled	
Enabled/Disabled wake from Ring		
RTC wake system from S5	Disabled	Optimal Default, Failsafe Default
	Fixed Time	
	Dynamic Time	
Enable or disable System wake on alarm event. When enabled, System will wake on the hr::min::sec specified		

3.4.7 Advanced: SIO Configuration



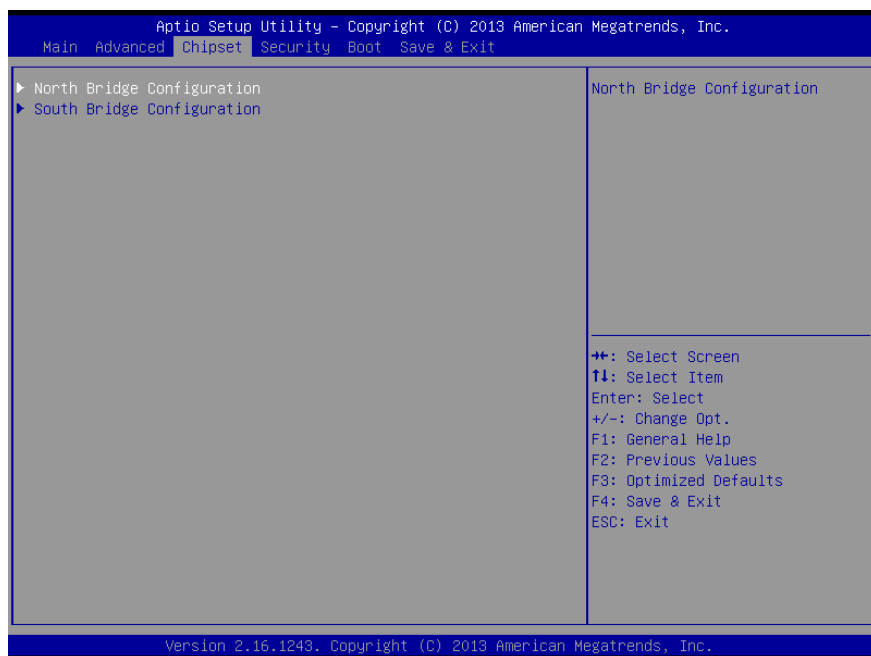
3.4.7.1 SIO Configuration: Serial Port 1 Configuration



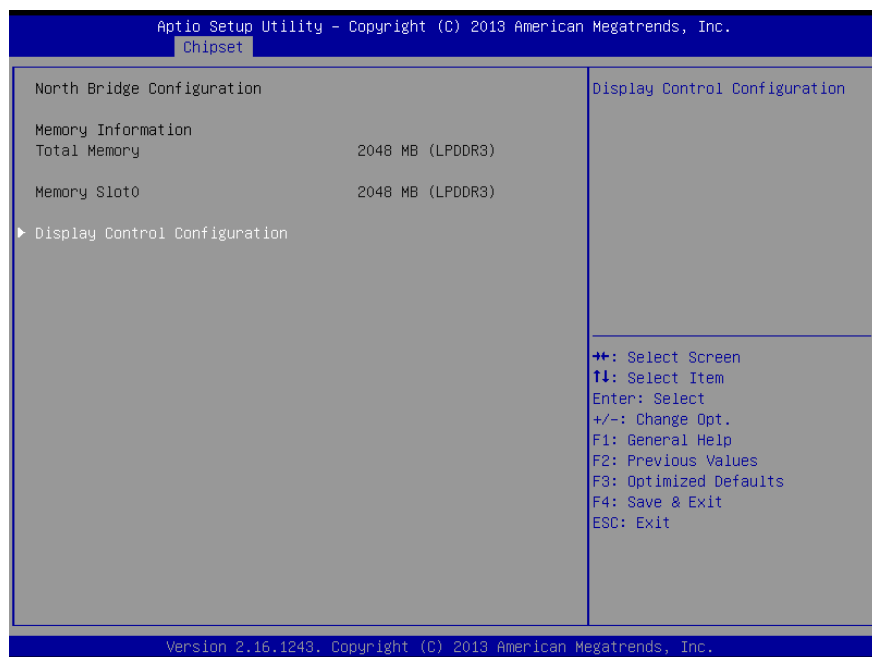
Options summary:

Use This Device	Disabled	Optimal Default, Failsafe Default
	Enabled	
En/Disable Serial Port (COM)		
Possible:	Use Automatic Settings	Optimal Default, Failsafe Default
	IO=3F8; IRQ=4;	
	IO=2F8; IRQ=3;	
Select an optimal setting for IO device		

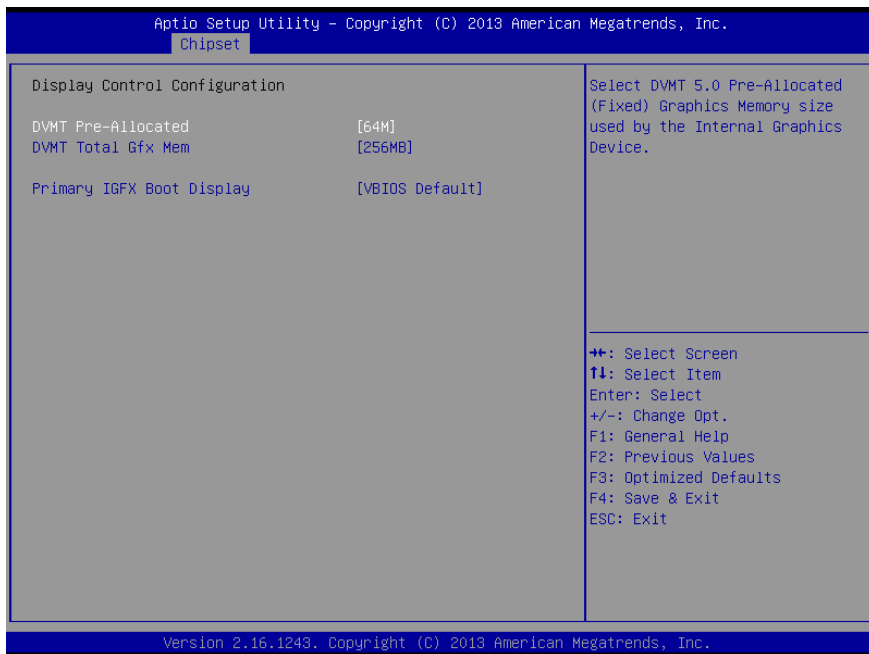
3.5 Setup submenu: Chipset



3.5.1 Chipset: North Bridge



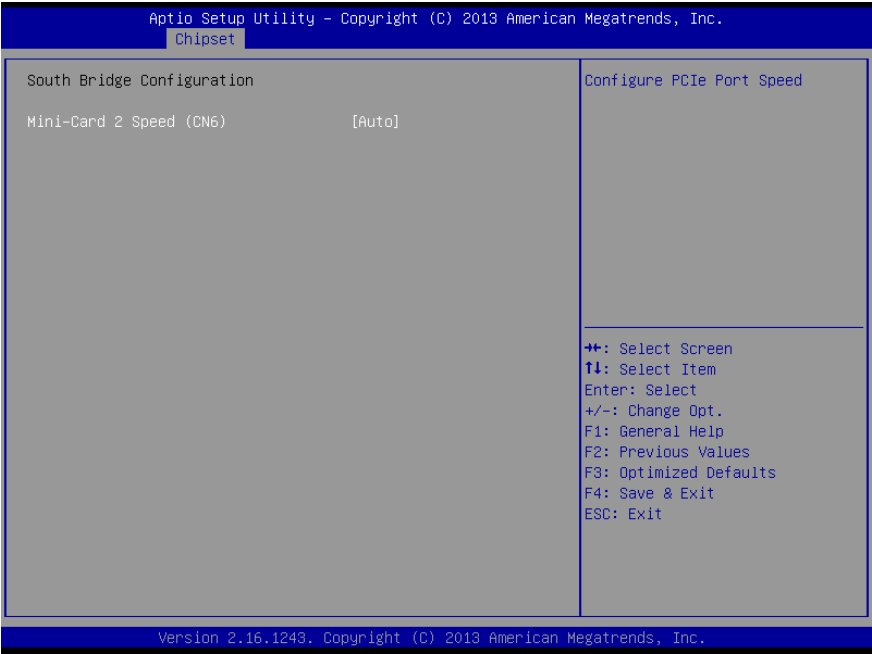
3.5.1.1 North Bridge: Display Control Configuration



Options summary:

DVMT Pre-Allocated	64M	Optimal Default, Failsafe Default
	96M	
	128M	
	160M ...	
	512M	
Select DVMT 5.0 Pre-Allocated (Fixed) Graphics Memory size used by the Internal Graphics Device.		
DVMT Total Gfx Mem	128MB	Optimal Default, Failsafe Default
	256MB	
	Max	
Select DVMT 5.0 Total Graphics Memory size used by the Internal Graphics Device.		
Primary IGFX Boot Display	VBIOS Default	Optimal Default, Failsafe Default
	CRT	
Select the Video Device which will be activated during POST. This has no effect if external graphics present. Secondary boot display selection will appear based on your selection. VGA modes will be supported only on primary display		

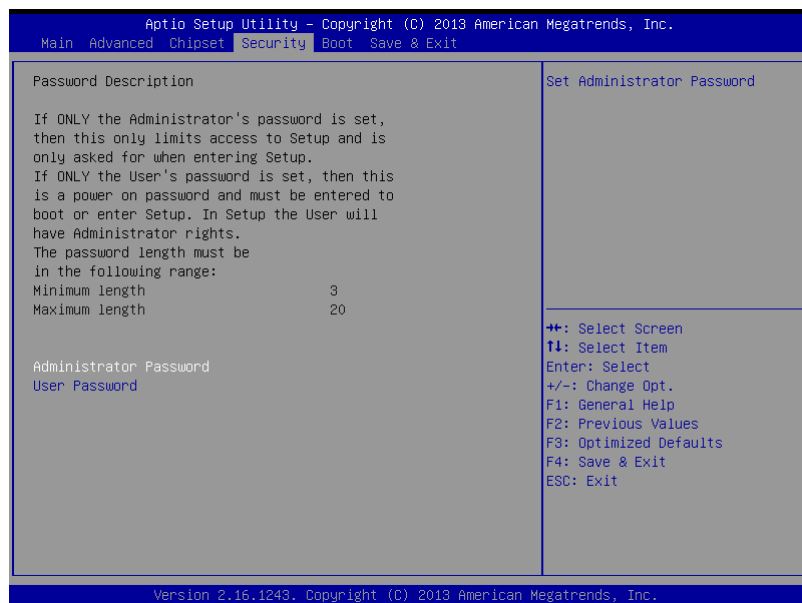
3.5.2 Chipset: South Bridge



Options summary:

Mini-Card 2 Speed (CN6)	Auto	Optimal Default, Failsafe Default
	Gen 2	
	Gen 1	
Configure PCIe Port Speed		

3.6 Setup submenu: Security



Change User/Administrator Password

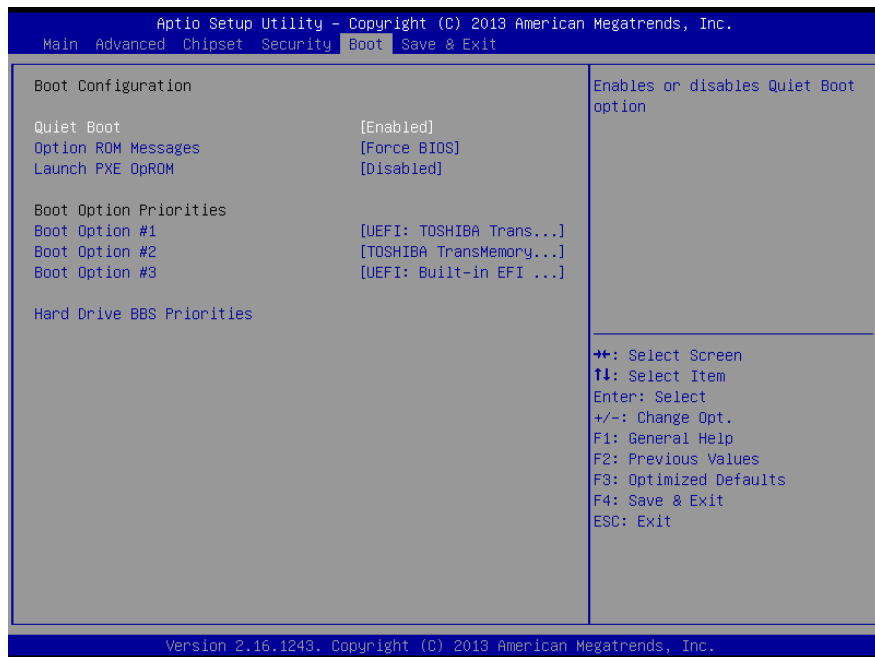
You can set a User Password once an Administrator Password is set. The password will be required during boot up, or when the user enters the Setup utility. Please Note that a User Password does not provide access to many of the features in the Setup utility.

Select the password you wish to set, press Enter to open a dialog box to enter your password (you can enter no more than six letters or numbers). Press Enter to confirm your entry, after which you will be prompted to retype your password for a final confirmation. Press Enter again after you have retyped it correctly.

Removing the Password

Highlight this item and type in the current password. At the next dialog box press Enter to disable password protection.

3.7 Setup submenu: Boot



Options summary:

Quiet Boot	Disabled	Default
	Enabled	
En/Disable showing boot logo.		
Option ROM Messages	Force BIOS	Default
	Keep Current	
Set display mode for Option ROM		
Launch PXE OpROM	Disabled	Default
	Enabled	
Controls the execution of LAN PXE OpROM		

3.8 Setup submenu: Save & Exit



Chapter 4

Drivers Installation

4.1 Driver Download and Installation

Drivers for the BOXER-6404U can be downloaded from the product page on the AAEON website by following this link:

<https://www.aaeon.com/en/p/ultra-slim-box-pc-boxer-6404u>

Download the driver(s) you need and follow the steps below to install them.

Step 1 – Install Chipset Drivers

1. Open the **Step 1 - Chipset** folder and select your OS
2. Open the **SetupChipset.exe** file in the folder
3. Follow the instructions
4. Drivers will be installed automatically

Step 2 – Install Graphics Driver

1. Open the **Step 2 - Graphics** folder and select your OS
2. Open the **Setup.exe** file in the folder
3. Follow the instructions
4. Drivers will be installed automatically

Step 3 – Install LAN Driver

1. Open the **Step 3 - LAN** folder and select your OS
2. Open the **Setup.exe** file in the folder
3. Follow the instructions
4. Drivers will be installed automatically

Step 4 – Install USB 3.0 Drivers (Windows 7 only)

1. Open the **Step 4 – USB 3.0** followed by the **Setup.exe** file
2. Follow the instructions
3. Drivers will be installed automatically

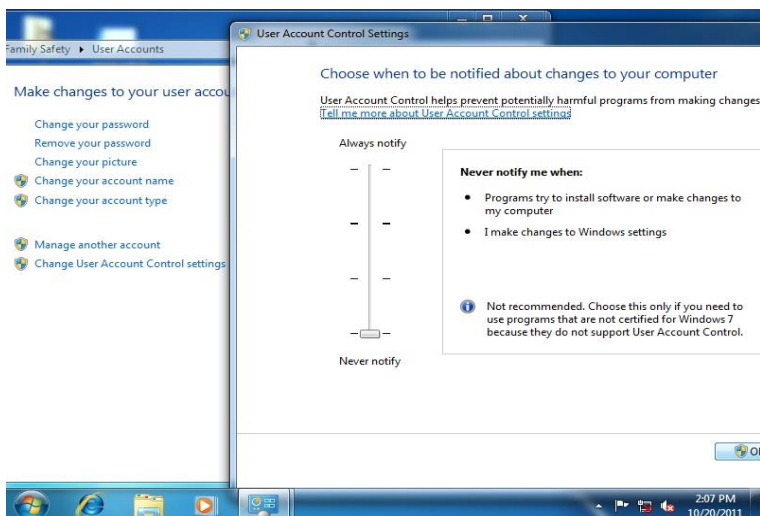
Step 5 – Install MBI Drivers

1. Open the **Step 5 – MBI(Optional)** folder and select your OS
2. Open the **Setup.exe** file
3. Follow the instructions
4. Drivers will be installed automatically

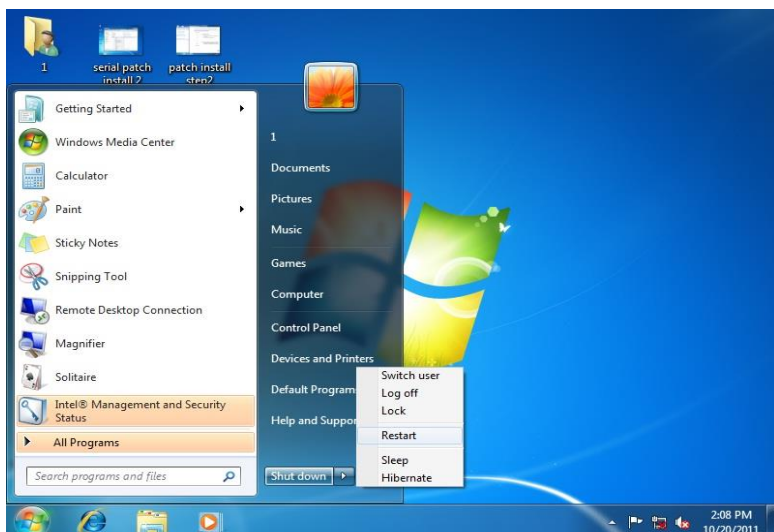
Step 6 – Install Serial Port Driver (Optional)

For Windows 7:

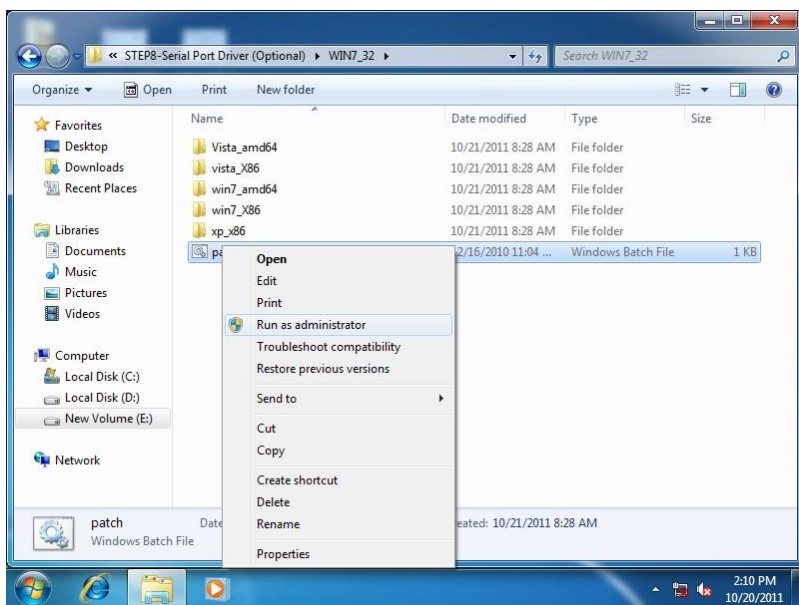
1. Change User Account Control settings to **Never notify**



2. Reboot and log in as administrator



3. Open the **Step 6 - Serial Port Driver (Optional)** folder and run **patch.bat** as administrator



For Windows 8 and Windows 10:

1. Open the **Step 6 - Serial Port Driver (Optional)** folder and select your OS
2. Open the **batch.bat** file in the folder
3. Follow the instructions
4. Drivers will be installed automatically

Appendix A

Watchdog Timer Programming

A.1 Watchdog Timer Initial Program

Table 1 : SuperIO relative register table		
	Default Value	Note
Index	0x2E(Note1)	SIO MB PnP Mode Index Register 0x2E or 0x4E
Data	0x2F(Note2)	SIO MB PnP Mode Data Register 0x2F or 0x4F

Table 2 : Watchdog relative register table					
	LDN	Register	BitNum	Value	Note
Timer Counter	0x07(Note3)	0xF6(Note4)		(Note24)	Time of watchdog timer (0~255) This register is byte access
Counting Unit	0x07(Note5)	0xF5(Note6)	3(Note7)	0(Note8)	Select time unit. 0: second 1: minute
Watchdog Enable	0x07(Note9)	0xF5(Note10)	5(Note11)	1(Note12)	0: Disable 1: Enable
Timeout Status	0x07(Note13)	0xF5(Note14)	6(Note15)	1	1: Clear timeout status
Output Mode	0x07(Note16)	0xF5(Note17)	4(Note18)	1(Note19)	Select WDTRST# output mode 0: level 1: pulse
WDTRST output	0x07(Note20)	0xFA(Note21)	0(Note22)	1(Note23)	Enable/Disable time out output via WDTRST# 0: Disable 1: Enable

A.2 Watchdog Sample Program

```
*****
// SuperIO relative definition (Please reference to Table 1)
#define byte  SIOIndex  //This parameter is represented from Note1
#define byte  SIOData   //This parameter is represented from Note2
#define void   IOWriteByte(byte IOPort, byte Value);
#define byte   IOReadByte(byte IOPort);
// Watch Dog relative definition (Please reference to Table 2)
#define byte   TimerLDN  //This parameter is represented from Note3
#define byte   TimerReg  //This parameter is represented from Note4
#define byte   TimerVal  // This parameter is represented from Note24
#define byte   UnitLDN   //This parameter is represented from Note5
#define byte   UnitReg   //This parameter is represented from Note6
#define byte   UnitBit   //This parameter is represented from Note7
#define byte   UnitVal   //This parameter is represented from Note8
#define byte   EnableLDN //This parameter is represented from Note9
#define byte   EnableReg //This parameter is represented from Note10
#define byte   EnableBit //This parameter is represented from Note11
#define byte   EnableVal //This parameter is represented from Note12
#define byte   StatusLDN // This parameter is represented from Note13
#define byte   StatusReg // This parameter is represented from Note14
#define byte   StatusBit // This parameter is represented from Note15
#define byte   ModeLDN   // This parameter is represented from Note16
#define byte   ModeReg   // This parameter is represented from Note17
#define byte   ModeBit   // This parameter is represented from Note18
#define byte   ModeVal   // This parameter is represented from Note19
#define byte   WDTRstLDN // This parameter is represented from Note20
#define byte   WDTRstReg // This parameter is represented from Note21
#define byte   WDTRstBit // This parameter is represented from Note22
#define byte   WDTRstVal // This parameter is represented from Note23
*****
```

```
*****
VOID  Main(){
    // Procedure : AaeonWDTConfig
    // (byte)Timer : Time of WDT timer.(0x00~0xFF)
    // (boolean)Unit : Select time unit(0: second, 1: minute).
    AaeonWDTConfig();

    // Procedure : AaeonWDTEnable
    // This procedure will enable the WDT counting.
    AaeonWDTEnable();
}
*****
```

```

*****
// Procedure : AaeonWDTEnable
VOID  AaeonWDTEnable (){
    WDTEnableDisable(EnableLDN, EnableReg, EnableBit, 1);
}

// Procedure : AaeonWDTConfig
VOID  AaeonWDTConfig (){
    // Disable WDT counting
    WDTEnableDisable(EnableLDN, EnableReg, EnableBit, 0);
    // Clear Watchdog Timeout Status
    WDTClearTimeoutStatus();
    // WDT relative parameter setting
    WDTParameterSetting();
}

VOID  WDTEnableDisable(byte LDN, byte Register, byte BitNum, byte Value){
    SIOBitSet(LDN, Register, BitNum, Value);
}

VOID  WDTParameterSetting(){
    // Watchdog Timer counter setting
    SIOByteSet(TimerLDN, TimerReg, TimerVal);
    // WDT counting unit setting
    SIOBitSet(UnitLDN, UnitReg, UnitBit, UnitVal);
    // WDT output mode setting, level / pulse
    SIOBitSet(ModelLDN, ModeReg, ModeBit, ModeVal);
    // Watchdog timeout output via WDTRST#
    SIOBitSet(WDTRstLDN, WDTRstReg, WDTRstBit, WDTRstVal);
}

VOID  WDTClearTimeoutStatus(){
    SIOBitSet(StatusLDN, StatusReg, StatusBit, 1);
}
*****

```

```

*****
VOID  SIOEnterMBPnPMode(){
    IOWriteByte(SIOIndex, 0x87);
    IOWriteByte(SIOIndex, 0x87);
}

VOID  SIOExitMBPnPMode(){
    IOWriteByte(SIOIndex, 0xAA);
}

VOID  SIOSelectLDN(byte LDN){
    IOWriteByte(SIOIndex, 0x07); // SIO LDN Register Offset = 0x07
    IOWriteByte(SIOData, LDN);
}

VOID  SIOBitSet(byte LDN, byte Register, byte BitNum, byte Value){
    Byte TmpValue;

    SIOEnterMBPnPMode();
    SIOSelectLDN(byte LDN);
    IOWriteByte(SIOIndex, Register);
    TmpValue = IOReadByte(SIOData);
    TmpValue &= ~(1 << BitNum);
    TmpValue |= (Value << BitNum);
    IOWriteByte(SIOData, TmpValue);
    SIOExitMBPnPMode();
}

VOID  SIOByteSet(byte LDN, byte Register, byte Value){
    SIOEnterMBPnPMode();
    SIOSelectLDN(LDN);
    IOWriteByte(SIOIndex, Register);
    IOWriteByte(SIOData, Value);
    SIOExitMBPnPMode();
}
*****

```

Appendix B

I/O Information

B.1 I/O Address Map

404u	
Input/output (IO)	
	[0000000000000000 - 000000000000006F] PCI Express Root Complex
	[0000000000000020 - 0000000000000021] Programmable interrupt controller
	[0000000000000024 - 0000000000000025] Programmable interrupt controller
	[0000000000000028 - 0000000000000029] Programmable interrupt controller
	[000000000000002C - 000000000000002D] Programmable interrupt controller
	[000000000000002E - 000000000000002F] Motherboard resources
	[0000000000000030 - 0000000000000031] Programmable interrupt controller
	[0000000000000034 - 0000000000000035] Programmable interrupt controller
	[0000000000000038 - 0000000000000039] Programmable interrupt controller
	[000000000000003C - 000000000000003D] Programmable interrupt controller
	[0000000000000040 - 0000000000000043] System timer
	[000000000000004E - 000000000000004F] Motherboard resources
	[0000000000000050 - 0000000000000053] System timer
	[0000000000000061 - 0000000000000061] Motherboard resources
	[0000000000000063 - 0000000000000063] Motherboard resources
	[0000000000000065 - 0000000000000065] Motherboard resources
	[0000000000000067 - 0000000000000067] Motherboard resources
	[0000000000000070 - 0000000000000070] Motherboard resources
	[0000000000000070 - 0000000000000077] System CMOS/real time clock
	[0000000000000078 - 00000000000000CF] PCI Express Root Complex
	[0000000000000080 - 000000000000008F] Motherboard resources
	[0000000000000092 - 0000000000000092] Motherboard resources
	[00000000000000A0 - 00000000000000A1] Programmable interrupt controller
	[00000000000000A4 - 00000000000000A5] Programmable interrupt controller
	[00000000000000A8 - 00000000000000A9] Programmable interrupt controller
	[00000000000000AC - 00000000000000AD] Programmable interrupt controller
	[00000000000000B0 - 00000000000000B1] Programmable interrupt controller
	[00000000000000B2 - 00000000000000B3] Motherboard resources
	[00000000000000B4 - 00000000000000B5] Programmable interrupt controller
	[00000000000000B8 - 00000000000000B9] Programmable interrupt controller
	[00000000000000BC - 00000000000000BD] Programmable interrupt controller
	[00000000000003B0 - 00000000000003BB] Intel(R) HD Graphics
	[00000000000003C0 - 00000000000003DF] Intel(R) HD Graphics
	[00000000000003F8 - 00000000000003FF] Communications Port (COM1)
	[0000000000000400 - 000000000000047F] Motherboard resources
	[00000000000004D0 - 00000000000004D1] Programmable interrupt controller
	[0000000000000500 - 00000000000005FE] Motherboard resources
	[0000000000000600 - 000000000000061F] Motherboard resources
	[0000000000000680 - 000000000000069F] Motherboard resources
	[0000000000000A00 - 0000000000000A0F] Motherboard resources
	[0000000000000A10 - 0000000000000A1F] Motherboard resources
	[0000000000000D00 - 0000000000000FFF] PCI Express Root Complex
	[0000000000000C00 - 0000000000000CFF] Realtek PCIe GBE Family Controller #2
	[0000000000000C00 - 0000000000000CFF] Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor PCI Express - Root Port 2 - 0F4A
	[0000000000000D00 - 0000000000000DFF] Realtek PCIe GBE Family Controller #3
	[0000000000000D00 - 0000000000000DFF] Realtek PCIe GBE Family Controller
	[0000000000000D00 - 0000000000000DFF] Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor PCI Express - Root Port 1 - 0F48
	[0000000000000E00 - 0000000000000E1F] Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor Platform Control Unit - SMBus Port - 0F12
	[0000000000000E20 - 0000000000000E3F] Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor AHCI - 0F23
	[0000000000000E40 - 0000000000000E43] Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor AHCI - 0F23
	[0000000000000E50 - 0000000000000E57] Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor AHCI - 0F23
	[0000000000000E60 - 0000000000000E63] Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor AHCI - 0F23
	[0000000000000E70 - 0000000000000E77] Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor AHCI - 0F23
	[0000000000000E80 - 0000000000000E87] Intel(R) HD Graphics

B.2 Memory Address Map

Memory	
[0000000000A0000 - 0000000000BFFFF]	Intel(R) HD Graphics
[0000000000A0000 - 0000000000BFFFF]	PCI Express Root Complex
[0000000000C0000 - 0000000000DFFFF]	PCI Express Root Complex
[0000000000E0000 - 0000000000FFFFFF]	PCI Express Root Complex
[0000000080000000 - 00000000D0612FFE]	PCI Express Root Complex
[00000000C0000000 - 00000000CFFFFFFF]	Intel(R) HD Graphics
[00000000D0000000 - 00000000D03FFFFF]	Intel(R) HD Graphics
[00000000D0400000 - 00000000D0403FFF]	Realtek PCIe GBE Family Controller #2
[00000000D0400000 - 00000000D040FFFF]	Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor PCI Express - Root Port 2 - 0F4A
[00000000D0404000 - 00000000D0404FFF]	Realtek PCIe GBE Family Controller #2
[00000000D0500000 - 00000000D0503FFF]	Realtek PCIe GBE Family Controller #3
[00000000D0500000 - 00000000D0503FFF]	Realtek PCIe GBE Family Controller
[00000000D0500000 - 00000000D050FFFF]	Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor PCI Express - Root Port 1 - 0F48
[00000000D0504000 - 00000000D0504FFF]	Realtek PCIe GBE Family Controller #3
[00000000D0504000 - 00000000D0504FFF]	Realtek PCIe GBE Family Controller
[00000000D0600000 - 00000000D060FFFF]	Intel(R) USB 3.0 eXtensible Host Controller - 0100 (Microsoft)
[00000000D0610000 - 00000000D061001F]	Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor Platform Control Unit - SMBus Port - 0F12
[00000000D0612000 - 00000000D06127FF]	Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor AHCI - 0F23
[00000000E0000000 - 00000000FFFFFFF]	Motherboard resources
[00000000E00000D0 - 00000000E00000DB]	Intel(R) Sideband Fabric Device
[00000000FED00000 - 00000000FED003FF]	High precision event timer
[00000000FED01000 - 00000000FED01FFF]	Motherboard resources
[00000000FED03000 - 00000000FED03FFF]	Motherboard resources
[00000000FED04000 - 00000000FED04FFF]	Motherboard resources
[00000000FED08000 - 00000000FED08FFF]	Motherboard resources
[00000000FED1C000 - 00000000FED1CFFF]	Motherboard resources
[00000000FEE00000 - 00000000FFFFFFF]	Motherboard resources
[00000000FEF00000 - 00000000FFFFFFF]	Motherboard resources

B.3 IRQ Mapping Chart

Interrupt request (IRQ)		
	(ISA) 0x00000000 (00)	System timer
	(ISA) 0x00000004 (04)	Communications Port (COM1)
	(ISA) 0x00000008 (08)	High precision event timer
	(ISA) 0x0000000B (11)	Realtek PCIe GBE Family Controller
	(ISA) 0x00000051 (81)	Microsoft ACPI-Compliant System
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	(ISA) 0x00000053 (83)	Microsoft ACPI-Compliant System
	(ISA) 0x00000054 (84)	Microsoft ACPI-Compliant System
	(ISA) 0x00000055 (85)	Microsoft ACPI-Compliant System
	(ISA) 0x00000056 (86)	Microsoft ACPI-Compliant System
	(ISA) 0x00000057 (87)	Microsoft ACPI-Compliant System
	(ISA) 0x00000058 (88)	Microsoft ACPI-Compliant System
	(ISA) 0x00000059 (89)	Microsoft ACPI-Compliant System
	(ISA) 0x0000005A (90)	Microsoft ACPI-Compliant System
	(ISA) 0x0000005B (91)	Microsoft ACPI-Compliant System
	(ISA) 0x0000005C (92)	Microsoft ACPI-Compliant System
	(ISA) 0x0000005D (93)	Microsoft ACPI-Compliant System
	(ISA) 0x0000005E (94)	Microsoft ACPI-Compliant System
	(ISA) 0x0000005F (95)	Microsoft ACPI-Compliant System
	(ISA) 0x00000060 (96)	Microsoft ACPI-Compliant System
	(ISA) 0x00000061 (97)	Microsoft ACPI-Compliant System
	(ISA) 0x00000062 (98)	Microsoft ACPI-Compliant System
	(ISA) 0x00000063 (99)	Microsoft ACPI-Compliant System
	(ISA) 0x00000064 (100)	Microsoft ACPI-Compliant System
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	(ISA) 0x0000006C (108)	Microsoft ACPI-Compliant System
	(ISA) 0x0000006D (109)	Microsoft ACPI-Compliant System
	(ISA) 0x0000006E (110)	Microsoft ACPI-Compliant System
	(ISA) 0x0000006F (111)	Microsoft ACPI-Compliant System
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	(ISA) 0x00000071 (113)	Microsoft ACPI-Compliant System
	(ISA) 0x00000072 (114)	Microsoft ACPI-Compliant System
	(ISA) 0x00000073 (115)	Microsoft ACPI-Compliant System
	(ISA) 0x00000074 (116)	Microsoft ACPI-Compliant System
	(ISA) 0x00000075 (117)	Microsoft ACPI-Compliant System
	(ISA) 0x00000076 (118)	Microsoft ACPI-Compliant System
	(ISA) 0x00000077 (119)	Microsoft ACPI-Compliant System
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	(ISA) 0x00000079 (121)	Microsoft ACPI-Compliant System
	(ISA) 0x0000007A (122)	Microsoft ACPI-Compliant System
	(ISA) 0x0000007B (123)	Microsoft ACPI-Compliant System
	(ISA) 0x0000007C (124)	Microsoft ACPI-Compliant System
	(ISA) 0x0000007D (125)	Microsoft ACPI-Compliant System
	(ISA) 0x0000007E (126)	Microsoft ACPI-Compliant System
	(ISA) 0x0000007F (127)	Microsoft ACPI-Compliant System

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 (ISA) 0x00000081 (129)	Microsoft ACPI-Compliant System
 (ISA) 0x00000082 (130)	Microsoft ACPI-Compliant System
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 (ISA) 0x0000008D (141)	Microsoft ACPI-Compliant System
 (ISA) 0x0000008E (142)	Microsoft ACPI-Compliant System
 (ISA) 0x0000008F (143)	Microsoft ACPI-Compliant System
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 (ISA) 0x00000093 (147)	Microsoft ACPI-Compliant System
 (ISA) 0x00000094 (148)	Microsoft ACPI-Compliant System
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 (ISA) 0x0000009C (156)	Microsoft ACPI-Compliant System
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 (ISA) 0x0000009E (158)	Microsoft ACPI-Compliant System
 (ISA) 0x0000009F (159)	Microsoft ACPI-Compliant System
 (ISA) 0x000000A0 (160)	Microsoft ACPI-Compliant System
 (ISA) 0x000000A1 (161)	Microsoft ACPI-Compliant System
 (ISA) 0x000000A2 (162)	Microsoft ACPI-Compliant System
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 (ISA) 0x000000A6 (166)	Microsoft ACPI-Compliant System
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 (ISA) 0x000000AC (172)	Microsoft ACPI-Compliant System
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 (ISA) 0x000000B1 (177)	Microsoft ACPI-Compliant System
 (ISA) 0x000000B2 (178)	Microsoft ACPI-Compliant System
 (ISA) 0x000000B3 (179)	Microsoft ACPI-Compliant System

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	(ISA) 0x000000B6 (182)	Microsoft ACPI-Compliant System
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	(ISA) 0x0000011F (287)	Microsoft ACPI-Compliant System
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 (ISA) 0x0000012F (303)	Microsoft ACPI-Compliant System
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 (ISA) 0x0000013E (318)	Microsoft ACPI-Compliant System
 (ISA) 0x0000013F (319)	Microsoft ACPI-Compliant System
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 (ISA) 0x0000014E (334)	Microsoft ACPI-Compliant System
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 (ISA) 0x0000016B (363)	Microsoft ACPI-Compliant System
 (ISA) 0x0000016C (364)	Microsoft ACPI-Compliant System
 (ISA) 0x0000016D (365)	Microsoft ACPI-Compliant System
 (ISA) 0x0000016E (366)	Microsoft ACPI-Compliant System
 (ISA) 0x0000016F (367)	Microsoft ACPI-Compliant System
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 (ISA) 0x00000171 (369)	Microsoft ACPI-Compliant System
 (ISA) 0x00000172 (370)	Microsoft ACPI-Compliant System
 (ISA) 0x00000173 (371)	Microsoft ACPI-Compliant System
 (ISA) 0x00000174 (372)	Microsoft ACPI-Compliant System
 (ISA) 0x00000175 (373)	Microsoft ACPI-Compliant System
 (ISA) 0x00000176 (374)	Microsoft ACPI-Compliant System
 (ISA) 0x00000177 (375)	Microsoft ACPI-Compliant System
 (ISA) 0x00000178 (376)	Microsoft ACPI-Compliant System
 (ISA) 0x00000179 (377)	Microsoft ACPI-Compliant System
 (ISA) 0x0000017A (378)	Microsoft ACPI-Compliant System
 (ISA) 0x0000017B (379)	Microsoft ACPI-Compliant System
 (ISA) 0x0000017C (380)	Microsoft ACPI-Compliant System
 (ISA) 0x0000017D (381)	Microsoft ACPI-Compliant System
 (ISA) 0x0000017E (382)	Microsoft ACPI-Compliant System
 (ISA) 0x0000017F (383)	Microsoft ACPI-Compliant System
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 (ISA) 0x00000181 (385)	Microsoft ACPI-Compliant System
 (ISA) 0x00000182 (386)	Microsoft ACPI-Compliant System
 (ISA) 0x00000183 (387)	Microsoft ACPI-Compliant System
 (ISA) 0x00000184 (388)	Microsoft ACPI-Compliant System
 (ISA) 0x00000185 (389)	Microsoft ACPI-Compliant System
 (ISA) 0x00000186 (390)	Microsoft ACPI-Compliant System
 (ISA) 0x00000187 (391)	Microsoft ACPI-Compliant System
 (ISA) 0x00000188 (392)	Microsoft ACPI-Compliant System
 (ISA) 0x00000189 (393)	Microsoft ACPI-Compliant System
 (ISA) 0x0000018A (394)	Microsoft ACPI-Compliant System
 (ISA) 0x0000018B (395)	Microsoft ACPI-Compliant System
 (ISA) 0x0000018C (396)	Microsoft ACPI-Compliant System
 (ISA) 0x0000018D (397)	Microsoft ACPI-Compliant System
 (ISA) 0x0000018E (398)	Microsoft ACPI-Compliant System
 (ISA) 0x0000018F (399)	Microsoft ACPI-Compliant System

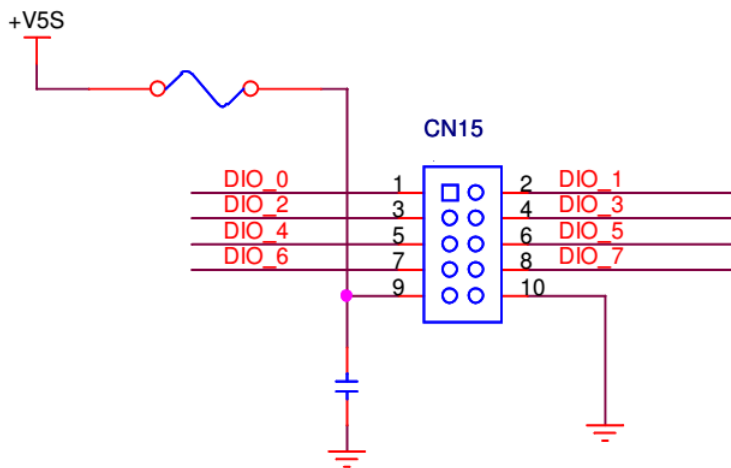
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	(ISA) 0x0000019C (412)	Microsoft ACPI-Compliant System
	(ISA) 0x0000019D (413)	Microsoft ACPI-Compliant System
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	(ISA) 0x000001A3 (419)	Microsoft ACPI-Compliant System
	(ISA) 0x000001A4 (420)	Microsoft ACPI-Compliant System
	(ISA) 0x000001A5 (421)	Microsoft ACPI-Compliant System
	(ISA) 0x000001A6 (422)	Microsoft ACPI-Compliant System
	(ISA) 0x000001A7 (423)	Microsoft ACPI-Compliant System
	(ISA) 0x000001A8 (424)	Microsoft ACPI-Compliant System
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	(ISA) 0x000001AA (426)	Microsoft ACPI-Compliant System
	(ISA) 0x000001AB (427)	Microsoft ACPI-Compliant System
	(ISA) 0x000001AC (428)	Microsoft ACPI-Compliant System
	(ISA) 0x000001AD (429)	Microsoft ACPI-Compliant System
	(ISA) 0x000001AE (430)	Microsoft ACPI-Compliant System
	(ISA) 0x000001AF (431)	Microsoft ACPI-Compliant System
	(ISA) 0x000001B0 (432)	Microsoft ACPI-Compliant System
	(ISA) 0x000001B1 (433)	Microsoft ACPI-Compliant System
	(ISA) 0x000001B2 (434)	Microsoft ACPI-Compliant System
	(ISA) 0x000001B3 (435)	Microsoft ACPI-Compliant System
	(ISA) 0x000001B4 (436)	Microsoft ACPI-Compliant System
	(ISA) 0x000001B5 (437)	Microsoft ACPI-Compliant System
	(ISA) 0x000001B6 (438)	Microsoft ACPI-Compliant System
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	(ISA) 0x000001B8 (440)	Microsoft ACPI-Compliant System
	(ISA) 0x000001B9 (441)	Microsoft ACPI-Compliant System
	(ISA) 0x000001BA (442)	Microsoft ACPI-Compliant System
	(ISA) 0x000001BB (443)	Microsoft ACPI-Compliant System
	(ISA) 0x000001BC (444)	Microsoft ACPI-Compliant System
	(ISA) 0x000001BD (445)	Microsoft ACPI-Compliant System
	(ISA) 0x000001BE (446)	Microsoft ACPI-Compliant System
	(ISA) 0x000001BF (447)	Microsoft ACPI-Compliant System
	(ISA) 0x000001C0 (448)	Microsoft ACPI-Compliant System
	(ISA) 0x000001C1 (449)	Microsoft ACPI-Compliant System
	(ISA) 0x000001C2 (450)	Microsoft ACPI-Compliant System
	(ISA) 0x000001C3 (451)	Microsoft ACPI-Compliant System

 (ISA) 0x000001C4 (452)	Microsoft ACPI-Compliant System
 (ISA) 0x000001C5 (453)	Microsoft ACPI-Compliant System
 (ISA) 0x000001C6 (454)	Microsoft ACPI-Compliant System
 (ISA) 0x000001C7 (455)	Microsoft ACPI-Compliant System
 (ISA) 0x000001C8 (456)	Microsoft ACPI-Compliant System
 (ISA) 0x000001C9 (457)	Microsoft ACPI-Compliant System
 (ISA) 0x000001CA (458)	Microsoft ACPI-Compliant System
 (ISA) 0x000001CB (459)	Microsoft ACPI-Compliant System
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 (ISA) 0x000001CD (461)	Microsoft ACPI-Compliant System
 (ISA) 0x000001CE (462)	Microsoft ACPI-Compliant System
 (ISA) 0x000001CF (463)	Microsoft ACPI-Compliant System
 (ISA) 0x000001D0 (464)	Microsoft ACPI-Compliant System
 (ISA) 0x000001D1 (465)	Microsoft ACPI-Compliant System
 (ISA) 0x000001D2 (466)	Microsoft ACPI-Compliant System
 (ISA) 0x000001D3 (467)	Microsoft ACPI-Compliant System
 (ISA) 0x000001D4 (468)	Microsoft ACPI-Compliant System
 (ISA) 0x000001D5 (469)	Microsoft ACPI-Compliant System
 (ISA) 0x000001D6 (470)	Microsoft ACPI-Compliant System
 (ISA) 0x000001D7 (471)	Microsoft ACPI-Compliant System
 (ISA) 0x000001D8 (472)	Microsoft ACPI-Compliant System
 (ISA) 0x000001D9 (473)	Microsoft ACPI-Compliant System
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 (ISA) 0x000001DB (475)	Microsoft ACPI-Compliant System
 (ISA) 0x000001DC (476)	Microsoft ACPI-Compliant System
 (ISA) 0x000001DD (477)	Microsoft ACPI-Compliant System
 (ISA) 0x000001DE (478)	Microsoft ACPI-Compliant System
 (ISA) 0x000001DF (479)	Microsoft ACPI-Compliant System
 (ISA) 0x000001E0 (480)	Microsoft ACPI-Compliant System
 (ISA) 0x000001E1 (481)	Microsoft ACPI-Compliant System
 (ISA) 0x000001E2 (482)	Microsoft ACPI-Compliant System
 (ISA) 0x000001E3 (483)	Microsoft ACPI-Compliant System
 (ISA) 0x000001E4 (484)	Microsoft ACPI-Compliant System
 (ISA) 0x000001E5 (485)	Microsoft ACPI-Compliant System
 (ISA) 0x000001E6 (486)	Microsoft ACPI-Compliant System
 (ISA) 0x000001E7 (487)	Microsoft ACPI-Compliant System
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 (ISA) 0x000001EA (490)	Microsoft ACPI-Compliant System
 (ISA) 0x000001EB (491)	Microsoft ACPI-Compliant System
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 (ISA) 0x000001EF (495)	Microsoft ACPI-Compliant System
 (ISA) 0x000001F0 (496)	Microsoft ACPI-Compliant System
 (ISA) 0x000001F1 (497)	Microsoft ACPI-Compliant System
 (ISA) 0x000001F2 (498)	Microsoft ACPI-Compliant System
 (ISA) 0x000001F3 (499)	Microsoft ACPI-Compliant System
 (ISA) 0x000001F4 (500)	Microsoft ACPI-Compliant System
 (ISA) 0x000001F5 (501)	Microsoft ACPI-Compliant System
 (ISA) 0x000001F6 (502)	Microsoft ACPI-Compliant System
 (ISA) 0x000001F7 (503)	Microsoft ACPI-Compliant System

Appendix C

Digital I/O Ports

C.1 Electrical Specifications for Digital I/O Ports



GPIO20	DIO_0
GPIO21	DIO_1
GPIO22	DIO_2
GPIO23	DIO_3
GPIO31	DIO_4
GPIO32	DIO_5
GPIO33	DIO_6
GPIO35	DIO_7

C.2 DIO Programming

The BOXER-6404U utilizes FINTEK F81801 chipset as its Digital I/O controller. Below are the procedures to complete its configuration. AAEON initial DI/O program is also attached for developing customized program for your application.

There are three steps to complete the configuration setup:

- (1) Enter the MB PnP Mode
- (2) Modify the data of configuration registers
- (3) Exit the MB PnP Mode. Undesired result may occur if the MB PnP Mode is not exited normally.

C.3 Digital I/O Register

Table 1 : SuperIO relative register table

	Default Value	Note
Index	0x2E(Note1)	SIO MB PnP Mode Index Register 0x2E or 0x4E
Data	0x2F(Note2)	SIO MB PnP Mode Data Register 0x2F or 0x4F

Table 2 : Digital Input relative register table

	LDN	Register	BitNum	Value	Note
DIO-1 Pin Status	0x06(Note3)	0xD2(Note4)	0(Note5)		GPIO20
DIO-2 Pin Status	0x06(Note6)	0xD2(Note7)	1(Note8)		GPIO21
DIO-3 Pin Status	0x06(Note9)	0xD2(Note10)	2(Note11)		GPIO22
DIO-4 Pin Status	0x06(Note12)	0xD2(Note13)	3(Note14)		GPIO23
DIO-5 Pin Status	0x06(Note15)	0xC2(Note16)	1(Note17)		GPIO31
DIO-6 Pin Status	0x06(Note18)	0xC2(Note19)	2(Note20)		GPIO32
DIO-7 Pin Status	0x06(Note21)	0xC2(Note22)	3(Note23)		GPIO33
DIO-8 Pin Status	0x06(Note24)	0xC2(Note25)	5(Note26)		GPIO35

Table 3 : Digital Output relative register table

	LDN	Register	BitNum	Value	Note
DIO-1 Output Data	0x06(Note27)	0xD1(Note28)	0(Note29)	(Note30)	GPIO20
DIO-2 Output Data	0x06(Note31)	0xD1(Note32)	1(Note33)	(Note34)	GPIO21
DIO-3 Output Data	0x06(Note35)	0xD1(Note36)	2(Note37)	(Note38)	GPIO22
DIO-4 Output Data	0x06(Note39)	0xD1(Note40)	3(Note41)	(Note42)	GPIO23
DIO-5 Output Data	0x06(Note43)	0xC1(Note44)	1(Note45)	(Note46)	GPIO31
DIO-6 Output Data	0x06(Note47)	0xC1(Note48)	2(Note49)	(Note50)	GPIO32
DIO-7 Output Data	0x06(Note51)	0xC1(Note52)	3(Note53)	(Note54)	GPIO33
DIO-8 Output Data	0x06(Note55)	0xC1(Note56)	5(Note57)	(Note58)	GPIO35

C.4 Digital I/O Sample Program

```
*****
// SuperIO relative definition (Please reference to Table 1)
#define byte   SIOIndex   //This parameter is represented from Note1
#define byte   SIOData    //This parameter is represented from Note2
#define void   IOWriteByte(byte IOPort, byte Value);
#define byte   IOReadByte(byte IOPort);
// Digital Input Status relative definition (Please reference to Table 2)
#define byte   DInput1LDN // This parameter is represented from Note3
#define byte   DInput1Reg // This parameter is represented from Note4
#define byte   DInput1Bit // This parameter is represented from Note5
#define byte   DInput2LDN // This parameter is represented from Note6
#define byte   DInput2Reg // This parameter is represented from Note7
#define byte   DInput2Bit // This parameter is represented from Note8
#define byte   DInput3LDN // This parameter is represented from Note9
#define byte   DInput3Reg // This parameter is represented from Note10
#define byte   DInput3Bit // This parameter is represented from Note11
#define byte   DInput4LDN // This parameter is represented from Note12
#define byte   DInput4Reg // This parameter is represented from Note13
#define byte   DInput4Bit // This parameter is represented from Note14
#define byte   DInput5LDN // This parameter is represented from Note15
#define byte   DInput5Reg // This parameter is represented from Note16
#define byte   DInput5Bit // This parameter is represented from Note17
#define byte   DInput6LDN // This parameter is represented from Note18
#define byte   DInput6Reg // This parameter is represented from Note19
#define byte   DInput6Bit // This parameter is represented from Note20
#define byte   DInput7LDN // This parameter is represented from Note21
#define byte   DInput7Reg // This parameter is represented from Note22
#define byte   DInput7Bit // This parameter is represented from Note23
#define byte   DInput8LDN // This parameter is represented from Note24
#define byte   DInput8Reg // This parameter is represented from Note25
#define byte   DInput8Bit // This parameter is represented from Note26
*****
```

```

*****
// Digital Output control relative definition (Please reference to Table 3)
#define byte DOutput1LDN // This parameter is represented from Note27
#define byte DOutput1Reg // This parameter is represented from Note28
#define byte DOutput1Bit // This parameter is represented from Note29
#define byte DOutput1Val // This parameter is represented from Note30
#define byte DOutput2LDN // This parameter is represented from Note31
#define byte DOutput2Reg // This parameter is represented from Note32
#define byte DOutput2Bit // This parameter is represented from Note33
#define byte DOutput2Val // This parameter is represented from Note34
#define byte DOutput3LDN // This parameter is represented from Note35
#define byte DOutput3Reg // This parameter is represented from Note36
#define byte DOutput3Bit // This parameter is represented from Note37
#define byte DOutput3Val // This parameter is represented from Note38
#define byte DOutput4LDN // This parameter is represented from Note39
#define byte DOutput4Reg // This parameter is represented from Note40
#define byte DOutput4Bit // This parameter is represented from Note41
#define byte DOutput4Val // This parameter is represented from Note42
#define byte DOutput5LDN // This parameter is represented from Note43
#define byte DOutput5Reg // This parameter is represented from Note44
#define byte DOutput5Bit // This parameter is represented from Note45
#define byte DOutput5Val // This parameter is represented from Note46
#define byte DOutput6LDN // This parameter is represented from Note47
#define byte DOutput6Reg // This parameter is represented from Note48
#define byte DOutput6Bit // This parameter is represented from Note49
#define byte DOutput6Val // This parameter is represented from Note50
#define byte DOutput7LDN // This parameter is represented from Note51
#define byte DOutput7Reg // This parameter is represented from Note52
#define byte DOutput7Bit // This parameter is represented from Note53
#define byte DOutput7Val // This parameter is represented from Note54
#define byte DOutput8LDN // This parameter is represented from Note55
#define byte DOutput8Reg // This parameter is represented from Note56
#define byte DOutput8Bit // This parameter is represented from Note57
#define byte DOutput8Val // This parameter is represented from Note58
*****

```

```

*****
VOID  Main(){
    Boolean PinStatus ;

    // Procedure : AaeonReadPinStatus
    // Input :
    //      Example, Read Digital I/O Pin 3 status
    // Output :
    //      InputStatus :
    //          0: Digital I/O Pin level is low
    //          1: Digital I/O Pin level is High
    PinStatus = AaeonReadPinStatus(DInput3LDN, DInput3Reg, DInput3Bit);

    // Procedure : AaeonSetOutputLevel
    // Input :
    //      Example, Set Digital I/O Pin 6 level
    AaeonSetOutputLevel(DOutput6LDN, DOutput6Reg, DOutput6Bit,
DOutput6Val);
}
*****

```

```
*****
Boolean  AaeonReadPinStatus(byte LDN, byte Register, byte BitNum){
    Boolean PinStatus ;

    PinStatus = SIOBitRead(LDN, Register, BitNum);
    Return PinStatus ;
}
VOID  AaeonSetOutputLevel(byte LDN, byte Register, byte BitNum, byte Value){
    ConfigToOutputMode(LDN, Register, BitNum);
    SIOBitSet(LDN, Register, BitNum, Value);
}
*****
```

```

*****
VOID  SIOEnterMBPnPMode(){
    IOWriteByte(SIOIndex, 0x87);
    IOWriteByte(SIOIndex, 0x87);
}

VOID  SIOExitMBPnPMode(){
    IOWriteByte(SIOIndex, 0xAA);
}

VOID  SIOSelectLDN(byte LDN){
    IOWriteByte(SIOIndex, 0x07); // SIO LDN Register Offset = 0x07
    IOWriteByte(SIOData, LDN);
}

VOID  SIOBitSet(byte LDN, byte Register, byte BitNum, byte Value){
    Byte TmpValue;

    SIOEnterMBPnPMode();
    SIOSelectLDN(byte LDN);
    IOWriteByte(SIOIndex, Register);
    TmpValue = IOReadByte(SIOData);
    TmpValue &= ~(1 << BitNum);
    TmpValue |= (Value << BitNum);
    IOWriteByte(SIOData, TmpValue);
    SIOExitMBPnPMode();
}

VOID  SIOByteSet(byte LDN, byte Register, byte Value){
    SIOEnterMBPnPMode();
    SIOSelectLDN(LDN);
    IOWriteByte(SIOIndex, Register);
    IOWriteByte(SIOData, Value);
    SIOExitMBPnPMode();
}
*****

```

```

*****
Boolean  SIOBitRead(byte LDN, byte Register, byte BitNum){
    Byte TmpValue;

    SIOEnterMBPnPMode();
    SIOSelectLDN(LDN);
    IOWriteByte(SIOIndex, Register);
    TmpValue = IOReadByte(SIOData);
    TmpValue &= (1 << BitNum);
    SIOExitMBPnPMode();
    If(TmpValue == 0)
        Return 0;
    Return 1;
}
VOID  ConfigToOutputMode(byte LDN, byte Register, byte BitNum){
    Byte TmpValue, OutputEnableReg;

    OutputEnableReg = Register-1;
    SIOEnterMBPnPMode();
    SIOSelectLDN(LDN);
    IOWriteByte(SIOIndex, OutputEnableReg);
    TmpValue = IOReadByte(SIOData);
    TmpValue |= (1 << BitNum);
    IOWriteByte(SIOData, OutputEnableReg);
    SIOExitMBPnPMode();
}
*****

```