



SBC-558P A0.3

Temperature cycle Test Report

Report NO : 02E020063

Issued by:

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12/27/2002

Rex Chang
DV Engineer

Date

Reviewed by:

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12/27/2002

Wayne Chen
DV Manager

Date

SBC-558P rev:A0.3

Test Date: 12-23~12-26-2002

Test Product: SBC-558P (PCB Rev:A0.3 BIOS Rev: 1.0)

Test Site: AAEON DV Internal Lab.

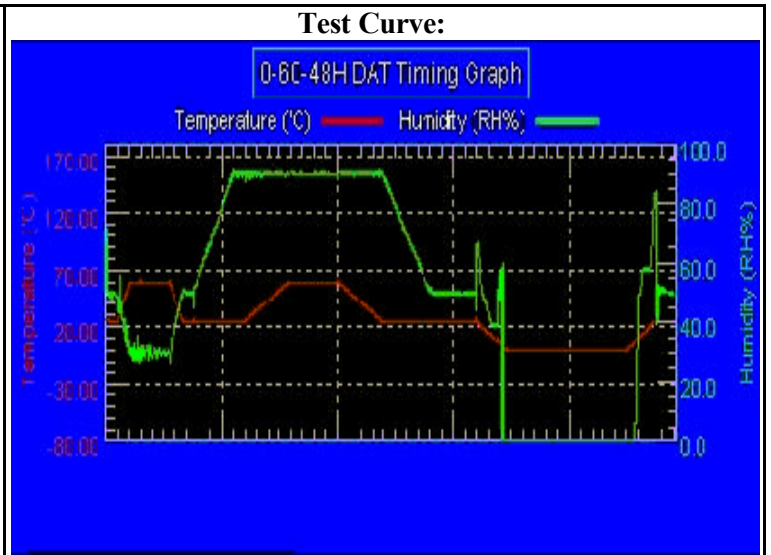
Performed By : Rex Chang

Test Standard : Reference IEC 68-2-30 Testing procedures
 Test DB : Damp Heat Test
 Reference IEC 68-2-61 Testing procedures
 Test Z/ABD : Climatic Sequence Test

Test Equipment:
 Programmable Temperature & Humidity Chamber
 K.SON. INS. TECH. CORP.
 Model : THS-D4H+-100
 Date of Calibration : 06/10/01
 Serial Number: 1241

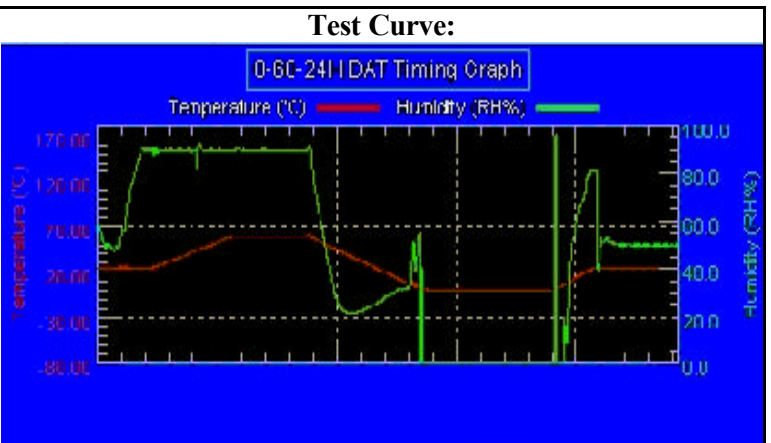
Temperature & Humidity Cycle Test:

Testing Specification			
Step	Temperature ()	Humidity (%RH)	Duration (HH:MM)
1	25	50	00:30
2	25	50	00:30
3	60	30	01:10
4	60	30	03:20
5	25	50	01:10
6	25	50	00:50
7	25	90	03:30
8	25	90	01:00
9	60	90	03:53
10	60	90	04:07
11	25	90	03:53
12	25	50	04:07
13	25	50	03:30
14	25	50	00:30
15	0	0	02:30
16	0	0	10:30
17	25	50	02:30
18	25	50	00:30



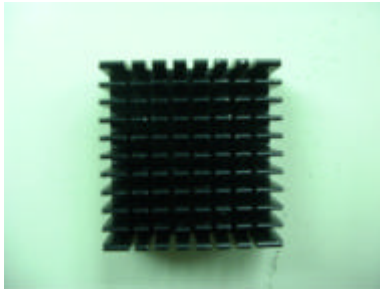
Temperature & Humidity Power On/Off Test

Testing Specification:			
Step	Temperature ()	Humidity (%RH)	Duration (HH:MM)
1	25	50	00:30
2	25	50	00:30
3	25	90	01:00
4	25	90	00:30
5	60	90	03:30
6	60	90	03:00
7	0	0	04:50
8	0	0	05:23
9	25	50	01:47
10	25	50	03:00



Sample Configuration & Quantity Under Test:

1. CPU : Onboard Tillamook-MMX 266MHz
2. SDRAM : NEC D45128163G5-A80-9JF 128MB SO-DIMM(PC-100)
3. Chipset : Intel 430TX
4. VGA : C&T 69000
5. Heat Sink : 1961558000

**Test Result :****Passed.**

The SBC-558P Half-size CPU Card meets temperature cycle test.