





# Temperature cycle test

Report NO.04E020016

PCM-6892 B0.2 (Intel Low Voltage Celeron 400MHz) With PCMCIA (ATX Power)

Test Date: 05-20~23-2004

Test Product: PCM-6892 (Intel Low Voltage Celeron 400MHz) With PCMCIA (ATX Power) Rev: B0.2

Test Site: AAEON QA Internal Lab.

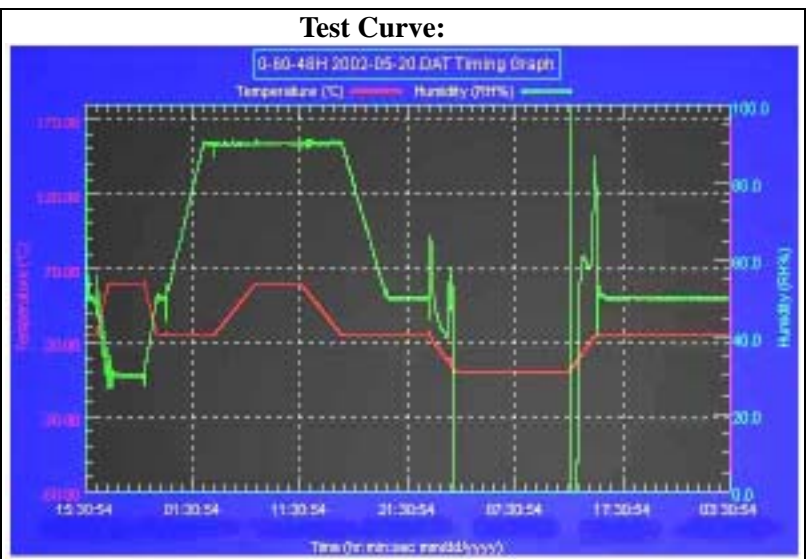
Performed By: Rex Chang

**Test Standard:** Reference IEC 68-2-30 Testing procedures  
 Test DB: Damp Heat Test  
 Reference IEC 68-2-61 Testing procedures  
 Test Z/ABD: Climatic Sequence Test

**Test Equipment:** Programmable Temperature & Humidity Chamber  
 K.SON. INS. TECH. CORP.  
 Model: THS-D4H+-100  
 Date of Calibration: 06/03/03  
 Serial Number: 1241

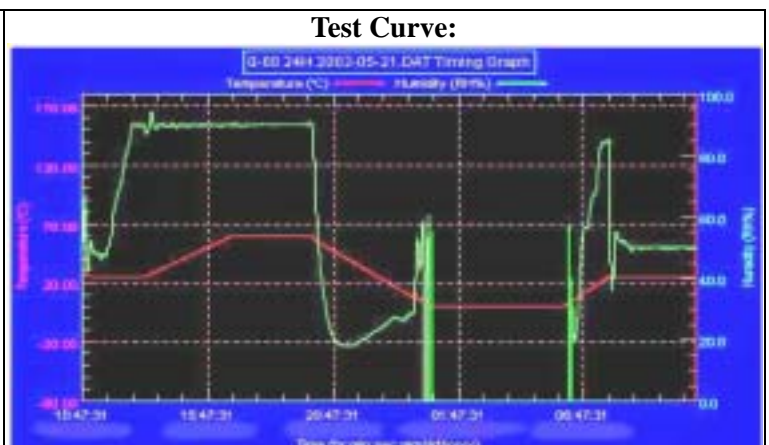
## Temperature & Humidity Cycle Test:

Testing Specification			
Step	Temperature ( )	Humidity (%RH)	Duration (HH:MM)
1	25	50	00:30
2	25	50	00:30
3	60	30	01:10
4	60	30	03:20
5	25	50	01:10
6	25	50	00:50
7	25	90	03:30
8	25	90	01:00
9	60	90	03:53
10	60	90	04:07
11	25	90	03:53
12	25	50	04:07
13	25	50	03:30
14	25	50	00:30
15	0	0	02:30
16	0	0	10:30
17	25	50	02:30
18	25	50	00:30



## Temperature & Humidity Power On/Off Test

Testing Specification:			
Step	Temperature ( )	Humidity (%RH)	Duration (HH:MM)
1	25	50	00:30
2	25	50	00:30
3	25	90	01:00
4	25	90	00:30
5	60	90	03:30
6	60	90	03:00
7	0	0	04:50
8	0	0	05:23
9	25	50	01:47
10	25	50	03:00



**Sample Configuration & Quantity Under Test:**

1. CPU: Onboard Intel Low Voltage Celeron 400MHz CPU (Bios Ver.B0.2);  
7331A758 SL6SE RJ80530 400/256
2. SDRAM: 64MB NANYA NT56V6620COT-75 (PC-133)
3. Chipset: VIA 8606 PN133T FJC0AC 0415 Q47536.1  
/ VIA82C686B 0411CD 13C0K3973
4. VGA: North Bridge VT8606 (Share memory up to 32MB)
5. LAN: Realtek RTL 8100BL 10/100 Base-T Fast Ethernet \* 2
6. CFD: PQI 32MB MB
7. Heat Sink: M166892000

**Test Result:****Passed**



**Test Date:** 05-27~31-2004

**Test Product:** PCM-6892 (Intel Low Voltage Celeron 650MHz) With PCMCIA (ATX Power) Rev : B0.2

**Test Site:** AAEON QA Internal Lab.

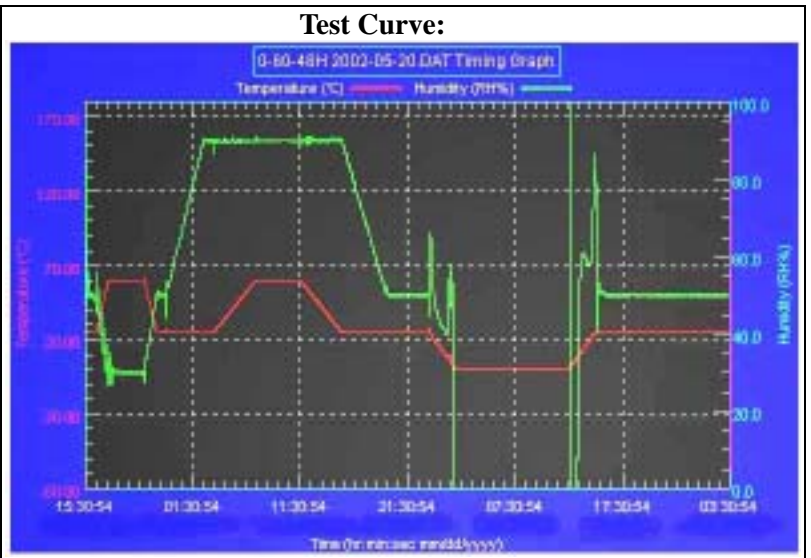
**Performed By:** Rex Chang

**Test Standard:** Reference IEC 68-2-30 Testing procedures  
 Test DB : Damp Heat Test  
 Reference IEC 68-2-61 Testing procedures  
 Test Z/ABD: Climatic Sequence Test

**Test Equipment:**  
 Programmable Temperature & Humidity Chamber  
 K.SON. INS. TECH. CORP.  
 Model: THS-D4H+-100  
 Date of Calibration: 05/24/04  
 Serial Number: 1241

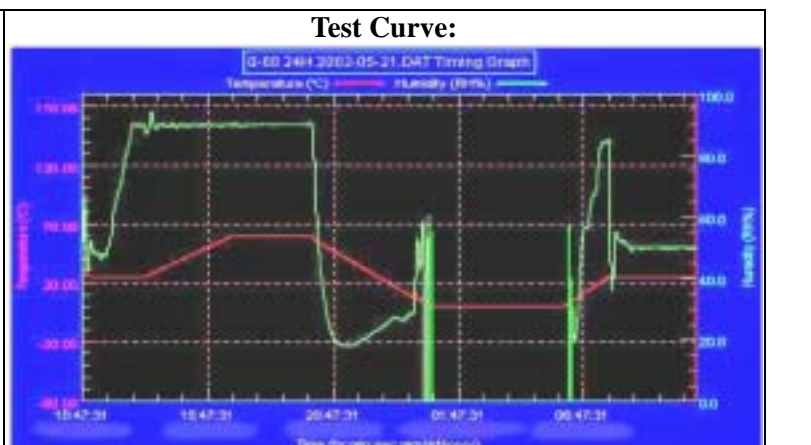
**Temperature & Humidity Cycle Test:**

Testing Specification			
Step	Temperature ( )	Humidity (%RH)	Duration (HH:MM)
1	25	50	00:30
2	25	50	00:30
3	60	30	01:10
4	60	30	03:20
5	25	50	01:10
6	25	50	00:50
7	25	90	03:30
8	25	90	01:00
9	60	90	03:53
10	60	90	04:07
11	25	90	03:53
12	25	50	04:07
13	25	50	03:30
14	25	50	00:30
15	0	0	02:30
16	0	0	10:30
17	25	50	02:30
18	25	50	00:30



**Temperature & Humidity Power On/Off Test**

Testing Specification:			
Step	Temperature ( )	Humidity (%RH)	Duration (HH:MM)
1	25	50	00:30
2	25	50	00:30
3	25	90	01:00
4	25	90	00:30
5	60	90	03:30
6	60	90	03:00
7	0	0	04:50
8	0	0	05:23
9	25	50	01:47
10	25	50	03:00



**Sample Configuration & Quantity Under Test:**

1. CPU: Onboard Intel Low Voltage Celeron 650MHz CPU (Bios Ver.B0.2);  
7318A843 SL6B8 RJ80530 650/256
2. SDRAM: 256MB HYNIX HY57V28820HCT-H (PC-133)
3. Chipset : VIA 8606 PN133T FJC0AC 0415 Q47536.1  
/ VIA82C686B 0411CD 13C0K3973
4. VGA: North Bridge VT8606 (Share memory up to 32MB)
5. LAN: Realtek RTL 8100BL 10/100 Base-T Fast Ethernet \* 2
6. CFD: PQI 64MB
7. Heat Sink:

**Test Result :****Passed**