



PCM-5896C10-02

Temperature cycle Test Report

Report NO : 02E020022

Issued by:

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08/14/2002

Art Liao
QE Engineer

Date

Reviewed by:

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08/14/2002

Wen - Yuan Yang
QE Manager

Date

PCM-5896

Test Date: 08-13-2002

Test Product: PCM-5896

Test Site: AAEON QA Internal Lab.

Performed By : Art Liao

Test Standard : Reference IEC 68-2-30 Testing procedures
 Test DB : Damp Heat Test
 Reference IEC 68-2-61 Testing procedures
 Test Z/ABD : Climatic Sequence Test

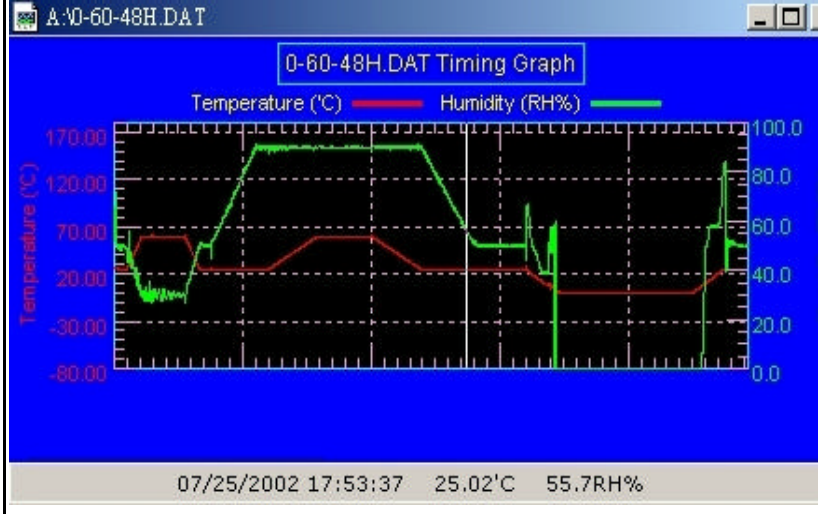
Test Equipment:
 Programmable Temperature & Humidity Chamber
 K.SON. INS. TECH. CORP.
 Model : THS-D4H+-100
 Date of Calibration : 06/10/01
 Serial Number: 1241

Temperature & Humidity Cycle Test:

Testing Specification

Step	Temperature ()	Humidity (%RH)	Duration (HH:MM)
1	25	0	00:30
2	25	0	00:30
3	60	0	01:10
4	60	0	03:20
5	25	0	01:10
6	25	0	00:50
7	25	0	03:30
8	25	0	01:00
9	60	0	03:53
10	60	0	04:07
11	25	0	03:53
12	25	0	04:07
13	25	0	03:30
14	25	0	00:30
15	0	0	02:30
16	0	0	10:30
17	25	0	02:30
18	25	0	00:30

Test Curve:

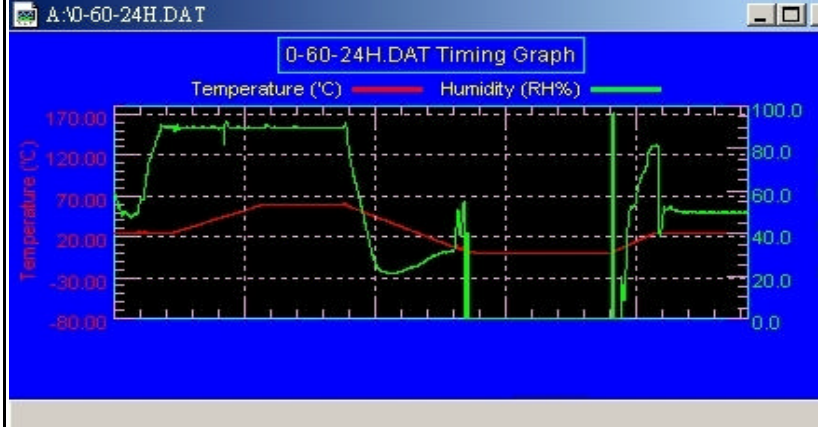


Temperature & Humidity Power On/Off Test

Testing Specification:

Step	Temperature ()	Humidity (%RH)	Duration (HH:MM)
1	25	0	00:30
2	25	0	00:30
3	25	0	01:00
4	25	0	00:30
5	60	0	03:30
6	60	0	03:00
7	0	0	04:50
8	0	0	05:23
9	25	0	01:47
10	25	0	03:00

Test Curve:



Sample Configuration & Quantity Under Test:

1. CPU : Pentium MMX tech 266MHz (Bios Ver.3.2)
2. SDRAM : NEC D45128841G5-A75-9JF
3. VGA : Chips B69000
4. LAN : Intel 82559
5. Audio : ESS ES1938S

Test Result :

Passed.

The PCM-5896 compact board meets temperature cycle test.