

HSB-440I A0.1

Test Date: 03-25~28-2003

Test Product: HSB-440I (PCB Rev:A0.1 BIOS Rev: 0.1)

Test Site: AAEON DV Internal Lab.

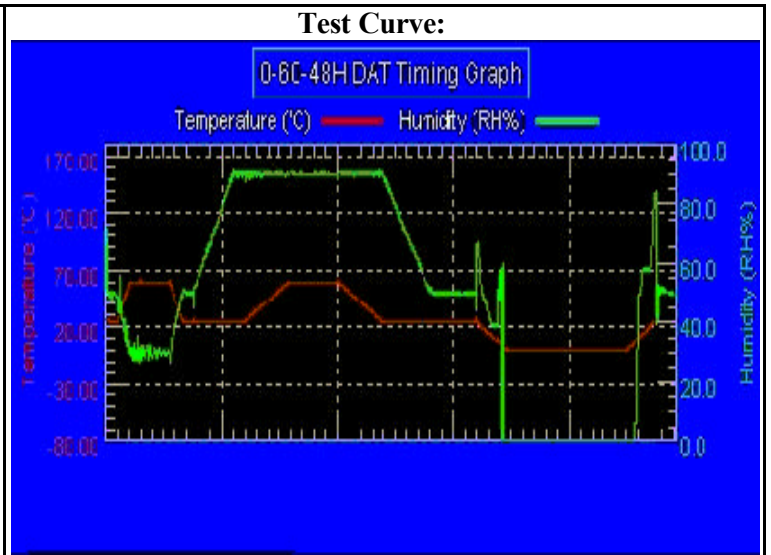
Performed By : Rex Chang

Test Standard : Reference IEC 68-2-30 Testing procedures
 Test DB : Damp Heat Test
 Reference IEC 68-2-61 Testing procedures
 Test Z/ABD : Climatic Sequence Test

Test Equipment:
 Programmable Temperature & Humidity Chamber
 K.SON. INS. TECH. CORP.
 Model : THS-D4H+-100
 Date of Calibration : 06/07/02
 Serial Number: 1241

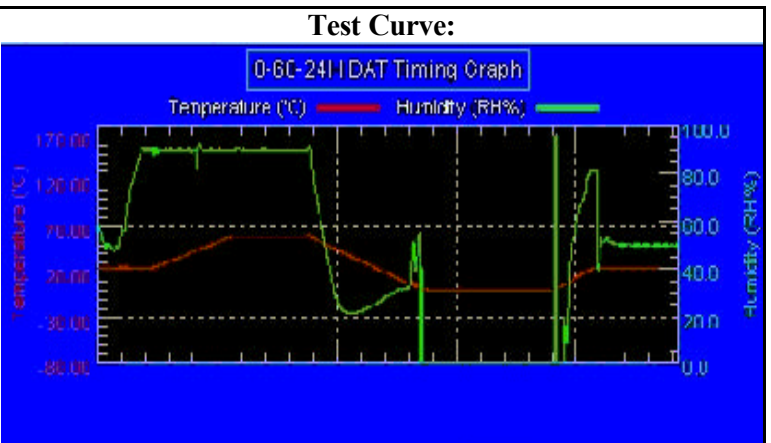
Temperature & Humidity Cycle Test:

Testing Specification			
Step	Temperature ()	Humidity (%RH)	Duration (HH:MM)
1	25	50	00:30
2	25	50	00:30
3	60	30	01:10
4	60	30	03:20
5	25	50	01:10
6	25	50	00:50
7	25	90	03:30
8	25	90	01:00
9	60	90	03:53
10	60	90	04:07
11	25	90	03:53
12	25	50	04:07
13	25	50	03:30
14	25	50	00:30
15	0	0	02:30
16	0	0	10:30
17	25	50	02:30
18	25	50	00:30



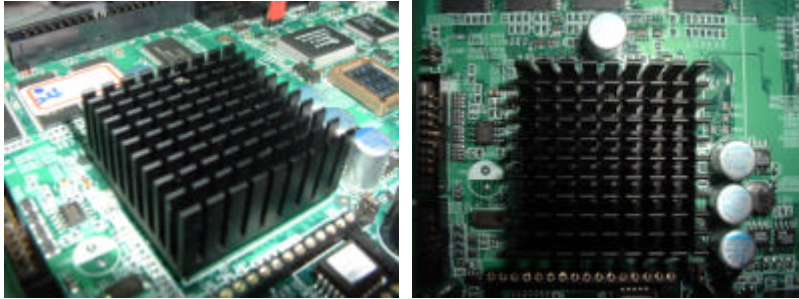
Temperature & Humidity Power On/Off Test

Testing Specification:			
Step	Temperature ()	Humidity (%RH)	Duration (HH:MM)
1	25	50	00:30
2	25	50	00:30
3	25	90	01:00
4	25	90	00:30
5	60	90	03:30
6	60	90	03:00
7	0	0	04:50
8	0	0	05:23
9	25	50	01:47
10	25	50	03:00



HSB-440I A0.1**Sample Configuration & Quantity Under Test:**

1. CPU : Embedded Low power STPC ATLAS 100/133 MHz processor
2. SDRAM : Onboard 64MB SDRAM ELPIDA D45128163G5-A75-9JF(PC-100)
3. VGA : STPC ATLAS 100/133 MHz processor (share system memory up to 4 MB)
4. Heat Sink(P/N): 1963558000

**Test Result :****Passed.**

The HSB-440I rev:A0.1 Half-size CPU card meets temperature cycle test.