



GENE-6330 rev:A0.2

Temperature cycle Test Report

Report NO : 02E020056

Issued by:

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12/24/2002

Rex Chang
DV Engineer

Date

Reviewed by:

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12/24/2002

Wayne-Chen
DV Manager

Date

GENE-6330 A0.2

Test Date: 12-18~12-23-2002

Test Product: GENE-6330 Rev : A0.2

Test Site: AAEON DV Internal Lab.

Performed By : Rex Chang

Test Standard : Reference IEC 68-2-30 Testing procedures
 Test DB : Damp Heat Test
 Reference IEC 68-2-61 Testing procedures
 Test Z/ABD : Climatic Sequence Test

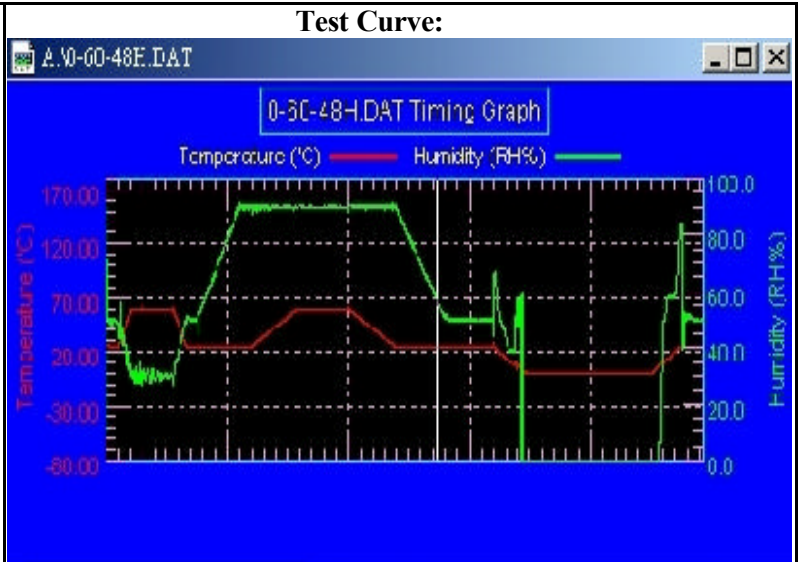
Test Equipment:
 Programmable Temperature & Humidity Chamber
 K.SON. INS. TECH. CORP.
 Model : THS-D4H+-100
 Date of Calibration : 06/10/02
 Serial Number: 1241

Temperature & Humidity Cycle Test:

Testing Specification

Step	Temperature ()	Humidity (%RH)	Duration (HH:MM)
1	25	50	00:30
2	25	50	00:30
3	60	30	01:10
4	60	30	03:20
5	25	50	01:10
6	25	50	00:50
7	25	90	03:30
8	25	90	01:00
9	60	90	03:53
10	60	90	04:07
11	25	90	03:53
12	25	50	04:07
13	25	50	03:30
14	25	50	00:30
15	0	0	02:30
16	0	0	10:30
17	25	50	02:30
18	25	50	00:30

Test Curve:

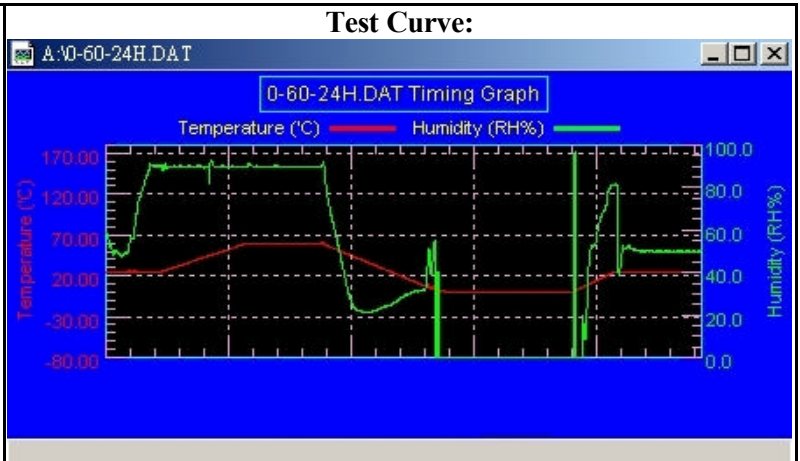


Temperature & Humidity Power On/Off Test

Testing Specification:

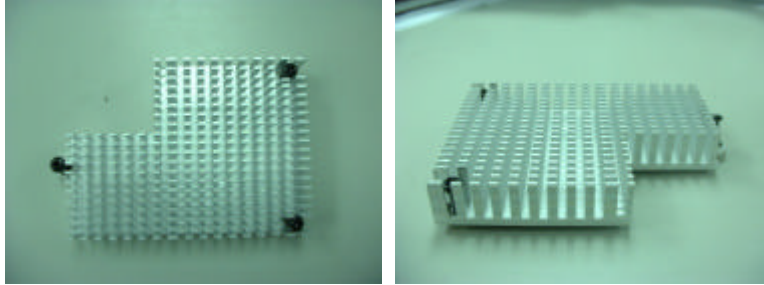
Step	Temperature ()	Humidity (%RH)	Duration (HH:MM)
1	25	50	00:30
2	25	50	00:30
3	25	90	01:00
4	25	90	00:30
5	60	90	03:30
6	60	90	03:00
7	0	0	04:50
8	0	0	05:23
9	25	50	01:47
10	25	50	03:00

Test Curve:



Sample Configuration & Quantity Under Test:

1. CPU : Onboard Transmeta Crusoe TM5400 600MHz (Bios Ver.0.2)
2. SDRAM : Onboard 64MB SDRAM+HITACHI 256MB 52251658TT75(PC-133 SDRAM)
3. VGA : SMI 712 LynxEM+ (Built-in 4MB)
4. LAN : RTL 8139D
5. Heat sink (P/N) : M166330000

**Test Result :****Passed.**

The GENE-6330 Sub Compact Board meets temperature cycle test.