Report NO: 13E010015

PFM-T096P

PC/104 module PCI-104 96-channel DIO Module

P5 Verification Test Report

Summary	☐ Pass ☐ Fail ☒ Pass with Deviation (Comment: In Windows XP only Supply one Pcs is Limitaion.)

Test Results Category				
	Critical	Major	Minor	Enhancement
Defect Found	0	0	0	0
Defect Unsolved	0	0	0	0

Issue date	Approval	Test Engineer
2013/12/31	Tom Lin	Jack Huang

Version Released Records

Date	Version	Change History	Note
2010/04/21	P3-1001	1. Re-composing test Items	
2010/06/01	P2P3-1001	1. New Test Report	
2010/06/21	P2P3-1002	1. Add BIOS -> SPI ROM Setting (South Bridge - GPIO)	
2010/12/02	P2P2-1003	1. New Test Report	
2011/02/23	P2P3-1101	1. Re-composing test Items	
2011/03/04	P2P3-1102	1. Add Specification Validation	

Note :

For all test items in this report, 3 results have been defined and described as following:

Pass:	Functionality work perfectly
Fail:	Functionality failed and must be resolved in the next version
N/A:	Functionality Not Applicable or Not Available

This test report would be updated when re-test completed in product next change version.

Specification Validation

Main Specification

Item	Specification	Result			Note
		Pass	Fail	N/A	
Product Name	PFM-T096P	☒	☐	☐	Test N2800/N2600
Form Factor	PC/104 Board – 90mm x 96mm	☒	☐	☐	
Host Interface	PCI-104 bus	☒	☐	☐	
Chipset	XILINX XC3S200AN BGA 256, SN74ALVC245	☒	☐	☐	
Digital I/O	96-bit Digital I/O; Output with 20mA driver capability for Digital Output. Software configurable Input and Output	☒	☐	☐	
Expansion Interface	NA	☒	☐	☐	
Watchdog Timer	NA	☒	☐	☐	
Hardware Monitor	NA	☒	☐	☐	
Power Supply Request	+5V/3.3V through PCI-104 connector	☒	☐	☐	
Operating Temp.	0ºC ~ 60ºC (or optional -20~70 ºC)	☒	☐	☐	

O.S. Support

Item	Specification	Result			Note
		Pass	Fail	N/A	
Microsoft Embedded	Windows CE	☐	☐	☒	
	Windows XP Embedded	☐	☐	☒	
	Windows 7 Embedded	☐	☐	☒	
Microsoft Windows	Windows XP Pro 32-bit	☐	☐	☒	
	Windows 7 32-bit	☒	☐	☐	
Linux	Fedora Linux	☒	☐	☐	

Environmental Specifications

Item	Specification
Operating temperature	0ºC ~ 60ºC (or optional -20~70 ºC)
EMC	CE /FCC class A

Note: Please refer Environment Test Report for detail information

Platform Information

Item	Device Information	Test Item
PCB Model / Version	PFM-T096P A1.0	1
CPU Board	PFM-CVS A1.0	N/A
Carrier Board	N/A	N/A
BIOS / Version	PFM-CVS R1.2(PFCVAM12) (04/12/2012)	1
CPU Type	Intel Atom N2600 1.6GHz	1
Memory Type	Transcend DDR3 1333 4GB CL9 SEC K482G846D	1
SATA HDD	Seagate ST31000524AS 7200 3.5” 1TB	1
SATA DVD-ROM	LG super multi DVD rewriter GH20 NS15	1
USB DVD-ROM	ASUS SDRW-08D2S-U DVD RW	1
LCD Monitor	LG E2260 22” LED monitor	1
LVDS	AU B116XW02 V.0 / 12" / 1366*768 / 18bit (16:9)	1
Compact Flash	N/A	1
Daughter Board	N/A	N/A
Expansion Board	N/A	N/A
	N/A	N/A
Operating System	☒ Windows 7 Ultimate 32bit	1-1
	☒ Windows XP Professional English SP3 32 Bit	1-1
Power Supply	ATX Power Supply : N/A	N/A
	AT Power Supply: Seventeam ST-300BLP	1
	DC Adapter : N/A	N/A
Battery Model	N/A	N/A
	Chipset Information with Windows 7 Ultimate English Version 32Bit Driver Version	
Chipset Software	Intel(R) Chipset Device Software 9.2.2.1034(7/20/2011)	
North Bridge	Intel Gedarview M/D	
South Bridge	Intel NM10	
Super IO Chipset	ITE IT8783F	
VGA Chipset	Intel® Graphics Media Accelerator 3600 Series 8.14.8.1086(8/17/2012)	
Ethernet Chipset	Realtek PCIe GBE Family Controller 7.43.321.2011(3/21/2011)	

Summary Table of contents:

1. Hardware Test

1.1. Support OS

CPU Information	Result			Note
	Pass	Fail	N/A	
Windows 7 Ultimate 32bit	☒	☐	☐	Test by Windows 7
Windows XP Professional English SP3 32 Bit	☒	☐	☐	Test by Windows 7 Test by Fedora 15 kernel 2.6.38.6-26.rc1.fc15.i686.PAE

1.2. Function Test

Test Item		Result			Note
		Pass	Fail	N/A	
0.Disable (Output)	Port1(Output)	☒	☐	☐	Use LED & tool test
	Port2(Output)	☒	☐	☐	
	Port3(Output)	☒	☐	☐	
	Port4(Output)	☒	☐	☐	
	Disable	☒	☐	☐	
1.Edge	Port1	☒	☐	☐	Check (Low->High) & (High->Low)
	Port2	☒	☐	☐	
	Port3	☒	☐	☐	
	Port4	☒	☐	☐	
2.Level	Port1	☒	☐	☐	Check (High) & (Low)
	Port2	☒	☐	☐	
	Port3	☒	☐	☐	
	Port4	☒	☐	☐	
3. Change state	Port1	☒	☐	☐	Tick changes trigger, leave blank to not trigger (Low)
	Port2	☒	☐	☐	
	Port3	☒	☐	☐	
	Port4	☒	☐	☐	
4. Pattern Match	Port1	☒	☐	☐	In full compliance with the GROUP state will trigger
	Port2	☒	☐	☐	
	Port3	☒	☐	☐	
	Port4	☒	☐	☐	