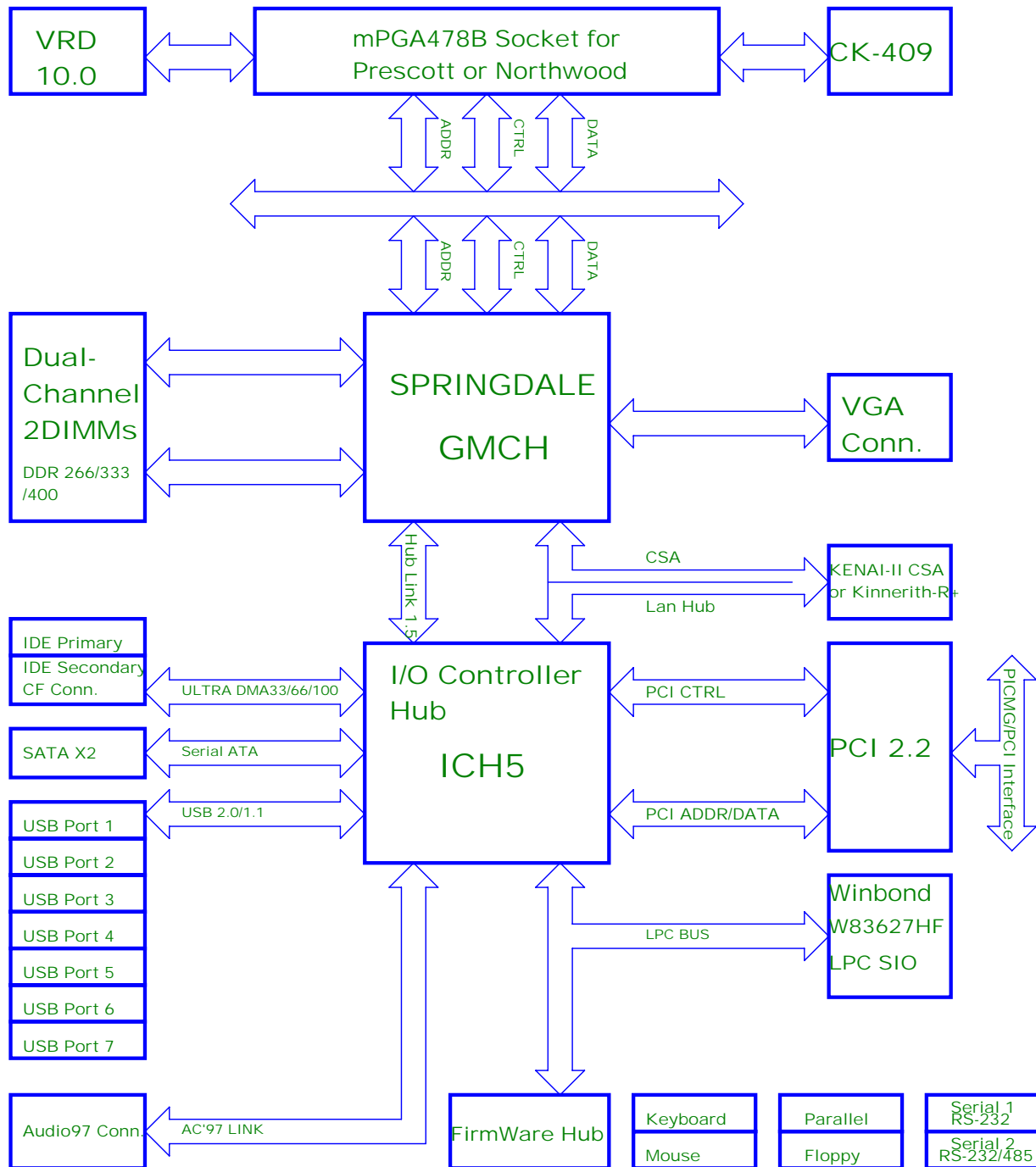


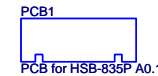
BLOCK DIAGRAM



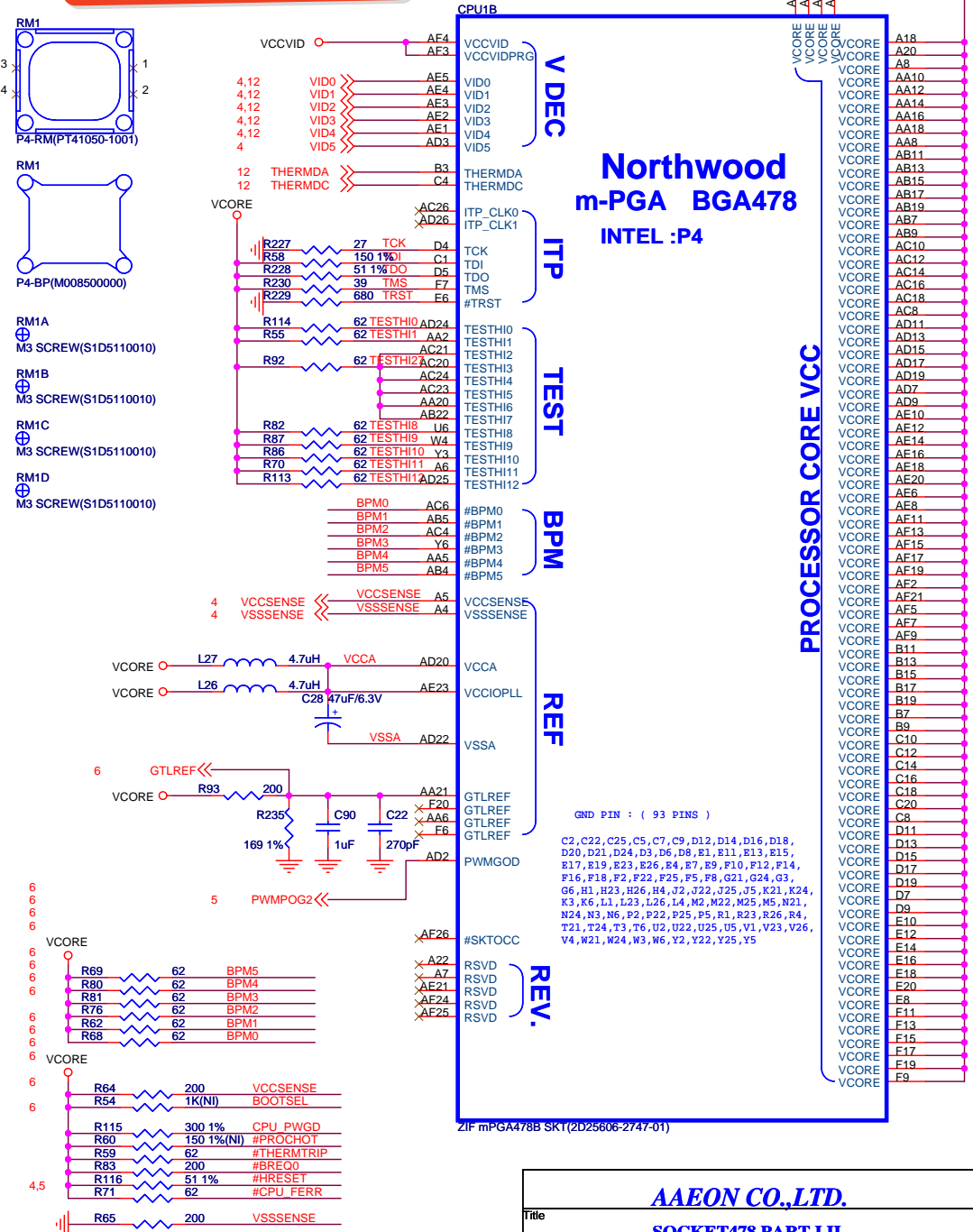
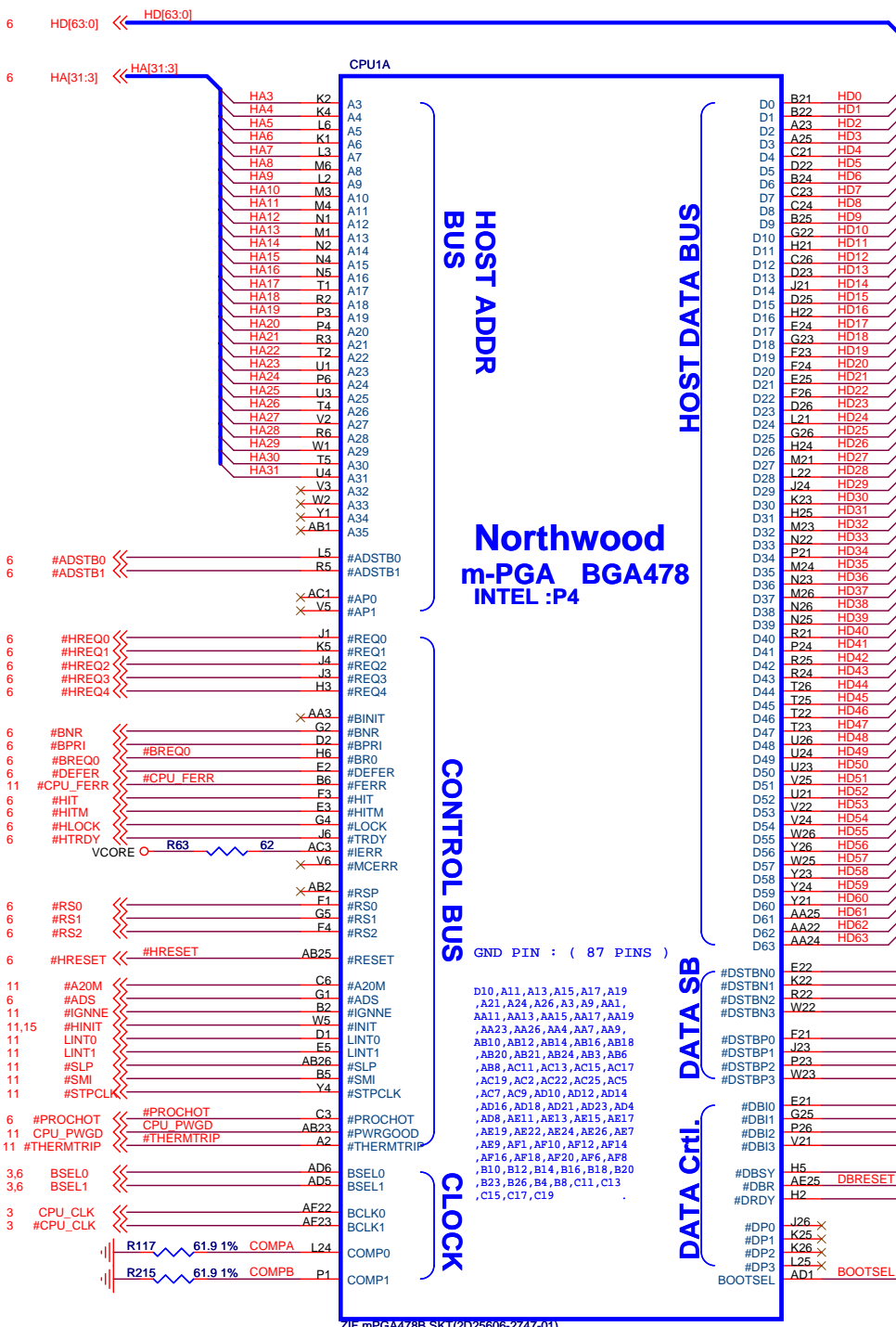
PAGE TITLE

01. Block Diagram
02. SOCKET478 PART I,II
03. CLK GEN,FREQ SEL
04. Multi-Phase PWM for VCORE
05. 2.5V/1.5V/1.25V/3VSB Reg.
06. GMCH MAIN
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08. GMCH/ICH POWER
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12. LPC I/O W83627HF
13. COM & PRN & IR & FDD & KB
14. VGA / USB CONNECTOR
15. FW HUB,IDE & Compact Flash
16. 100/1000M 82562/82547 LAN
17. PICMG/PCI GF
18. REVERSION HISTORY

機 密

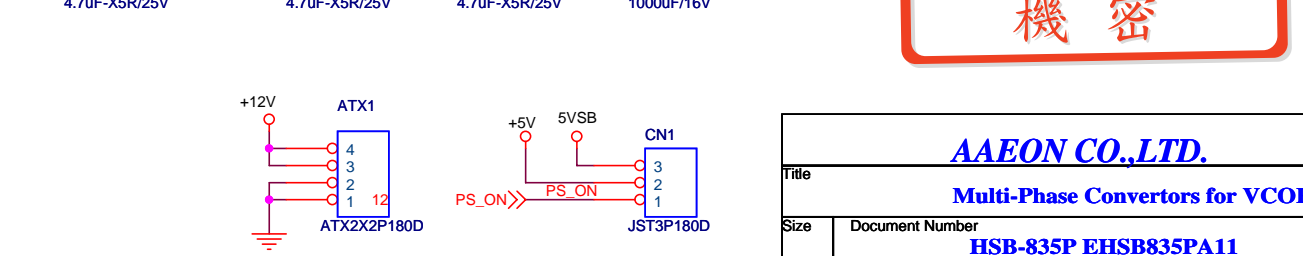
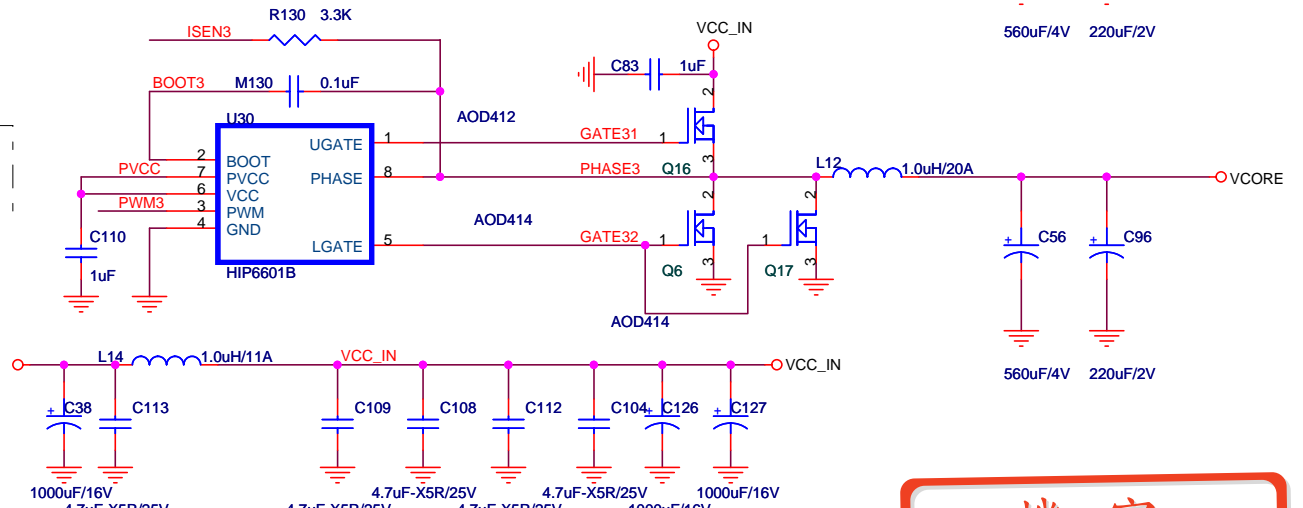
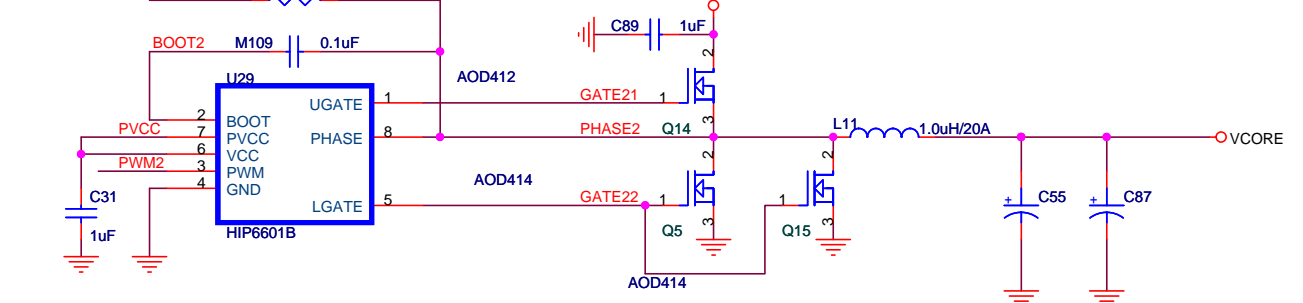
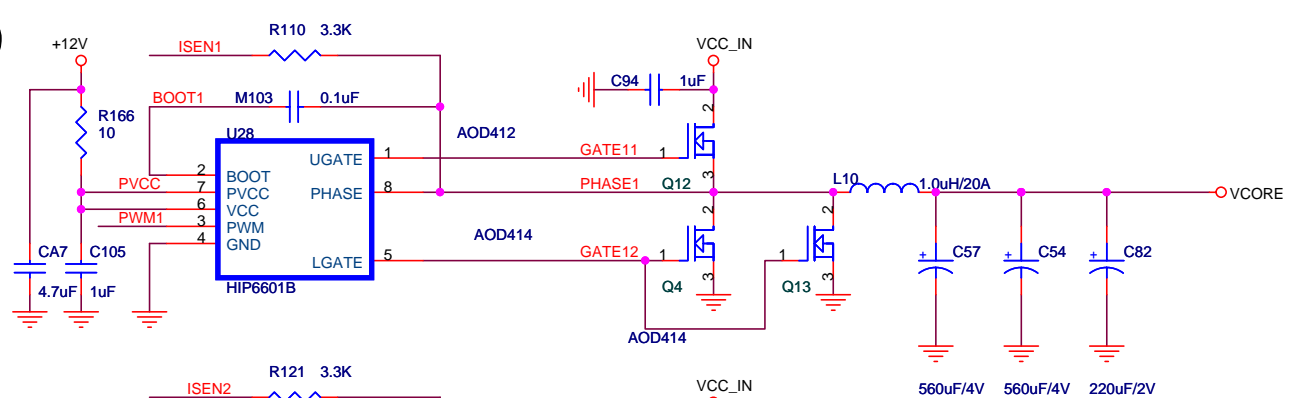
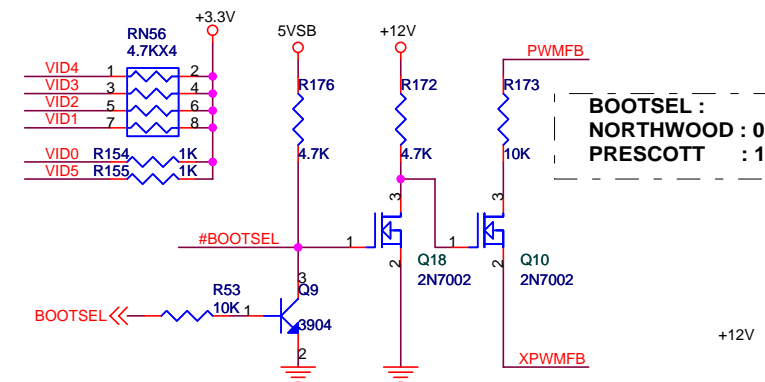
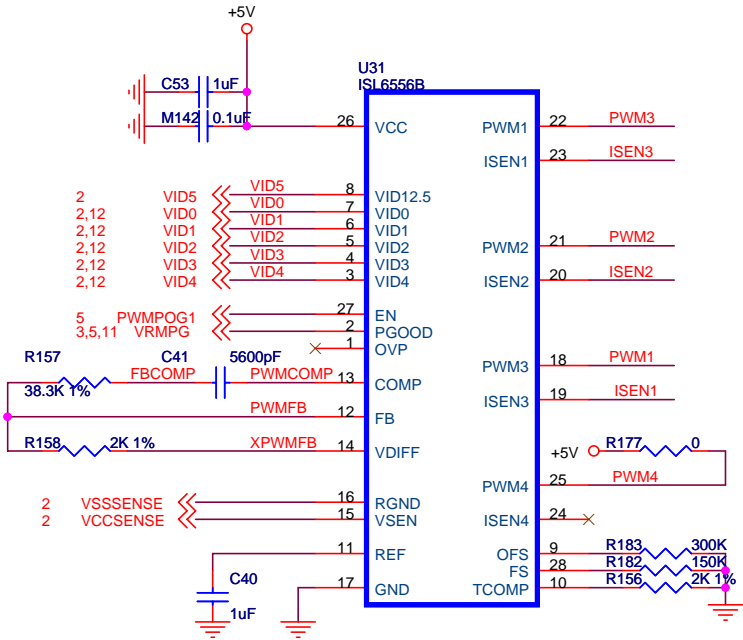


機密

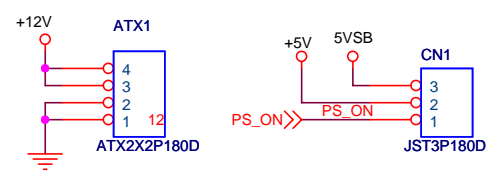


Title		
Aeon Co., Ltd.		
Socket478 Part LH		
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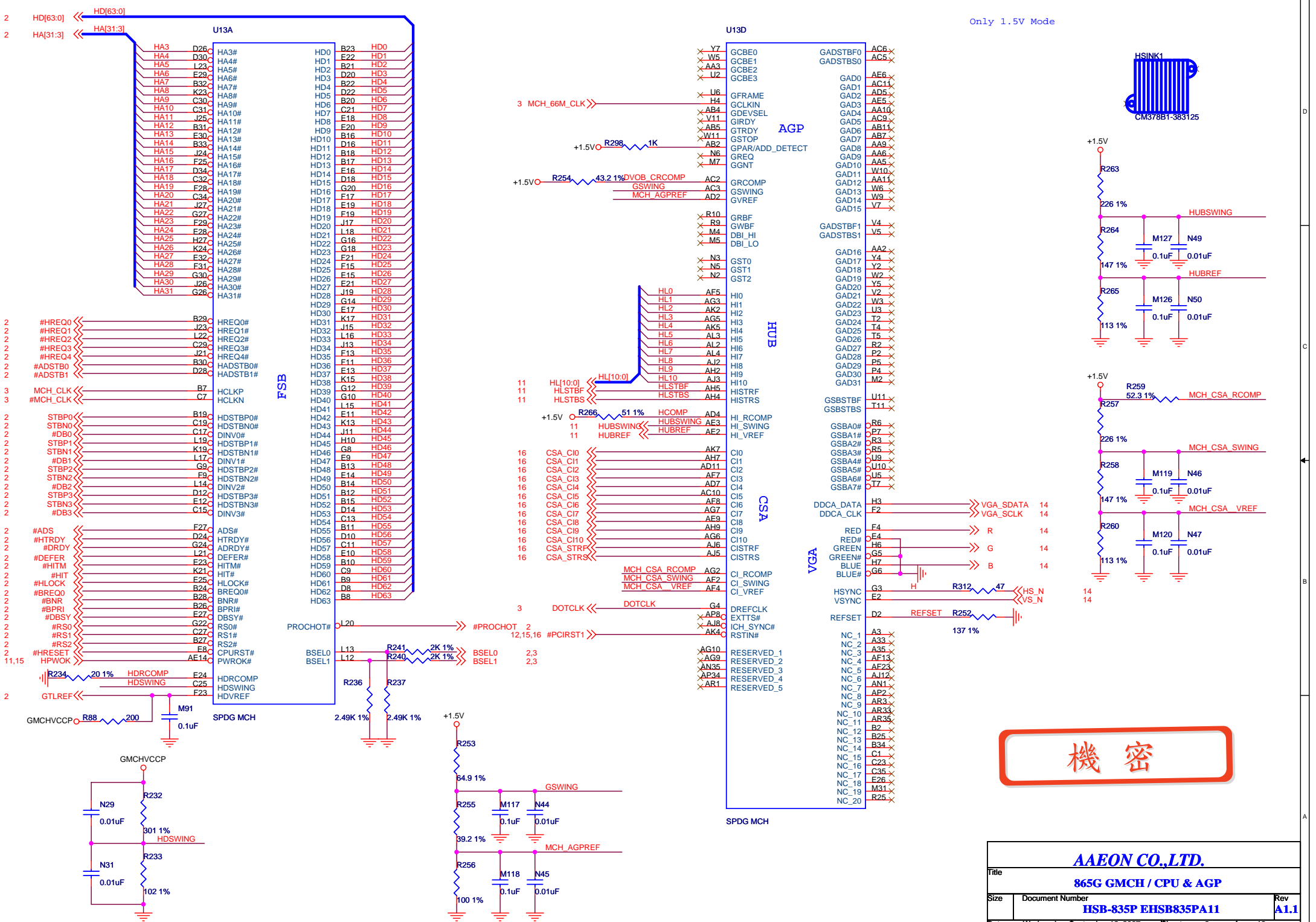
VCORE(1.1V-1.85V/60A)



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AAEON CO.,LTD.		
Multi-Phase Convertors for VCORE		
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Only 1.5V Mode



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U13B

U13C

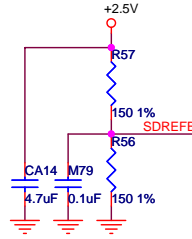
9	MAAA0	AJ34	SMAA_A0	SDQS_A0	AN11	DQSA0
9	MAAA1	AL33	SMAA_A1	SDM_A0	AP12	SDMA0
9	MAAA2	AK29	SMAA_A2	SDQ_A0	AP10	MDA0
9	MAAA3	AN31	SMAA_A3	SDQ_A1	AP11	MDA1
9	MAAA4	AL30	SMAA_A4	SDQ_A2	AM12	MDA2
9	MAAA5	AL26	SMAA_A5	SDQ_A3	AN13	MDA3
9	MAAA6	AL28	SMAA_A6	SDQ_A4	AM10	MDA4
9	MAAA7	AN25	SMAA_A7	SDQ_A5	AL10	MDA5
9	MAAA8	AP26	SMAA_A8	SDQ_A6	AL12	MDA6
9	MAAA9	AP24	SMAA_A9	SDQ_A7	AP13	MDA7
9	MAAA10	AJ33	SMAA_A10	SDQS_A1	AP15	DQSA1
9	MAAA11	AN23	SMAA_A11	SDM_A1	AP16	SDMA1
9	MAAA12	AN21	SMAA_A12	SDQ_A8	AP14	MDA8
		XAL34	SMAB_A1	SDQ_A9	AM14	MDA9
		XAM34	SMAB_A2	SDQ_A10	AL18	MDA10
		XAF32	SMAB_A3	SDQ_A11	AP19	MDA11
		XEP31	SMAB_A4	SDQ_A12	AL14	MDA12
		XAM26	SMAB_A5	SDQ_A13	AN15	MDA13
				SDQ_A14	AP18	MDA14
				SDQ_A15	AM18	MDA15
				SDQS_A2	AP23	DQSA2
				SDM_A2	AM24	SDMA2
				SDQ_A16	AP22	MDA16
				SDQ_A17	AM22	MDA17
				SDQ_A18	AL24	MDA18
				SDQ_A19	AN27	MDA19
				SDQ_A20	AP21	MDA20
				SDQ_A21	AL22	MDA21
				SDQ_A22	AP25	MDA22
				SDQ_A23	AP27	MDA23
				SDQS_A3	AM30	DQSA3
				SDM_A3	AP30	SDMA3
				SDQ_A24	AP28	MDA24
				SDQ_A25	AP29	MDA25
				SDQ_A26	AP33	MDA26
				SDQ_A27	AM33	MDA27
				SDQ_A28	AM28	MDA28
				SDQ_A29	AN29	MDA29
				SDQ_A30	AM31	MDA30
				SDQ_A31	AN34	MDA31
				SDQS_A4	AF34	DQSA4
				SDM_A4	AF31	SDMA4
				SDQ_A32	AH32	MDA32
				SDQ_A33	AG34	MDA33
				SDQ_A34	AF32	MDA34
				SDQ_A35	AD32	MDA35
				SDQ_A36	AH31	MDA36
				SDQ_A37	AG33	MDA37
				SDQ_A38	AE34	MDA38
				SDQ_A39	AD34	MDA39
				SDQS_A5	V34	DQSA5
				SDM_A5	W33	SDMA5
				SDQ_A40	AC34	MDA40
				SDQ_A41	AB31	MDA41
				SDQ_A42	V32	MDA42
				SDQ_A43	V31	MDA43
				SDQ_A44	AD31	MDA44
				SDQ_A45	AB32	MDA45
				SDQ_A46	U34	MDA46
				SDQ_A47	U33	MDA47
				SDQS_A6	M32	DQSA6
				SDM_A6	M34	SDMA6
				SDQ_A48	T34	MDA48
				SDQ_A49	T32	MDA49
				SDQ_A50	K34	MDA50
				SDQ_A51	K32	MDA51
				SDQ_A52	T31	MDA52
				SDQ_A53	P34	MDA53
				SDQ_A54	L34	MDA54
				SDQ_A55	L33	MDA55
				SDQS_A7	H31	DQSA7
				SDM_A7	H32	SDMA7
				SDQ_A56	J33	MDA56
				SDQ_A57	H34	MDA57
				SDQ_A58	E33	MDA58
				SDQ_A59	F33	MDA59
				SDQ_A60	K31	MDA60
				SDQ_A61	J34	MDA61
				SDQ_A62	G34	MDA62
				SDQ_A63	F34	MDA63

DDR Channel A

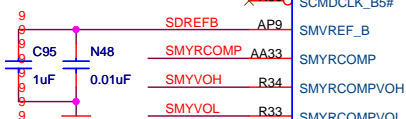
SPDG MCH



close to pin E34



close to pin E34

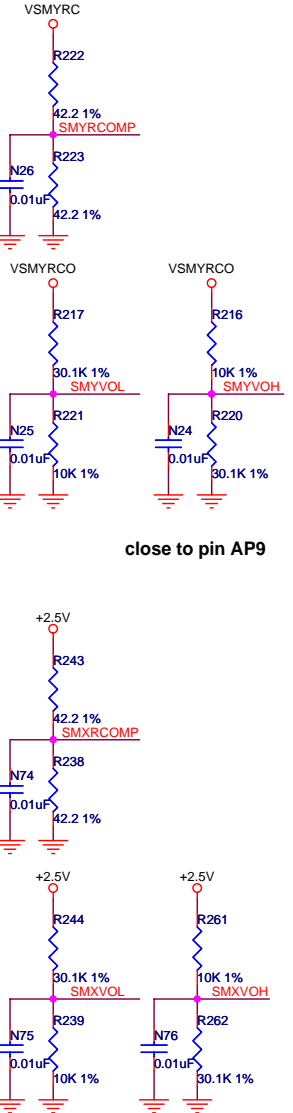


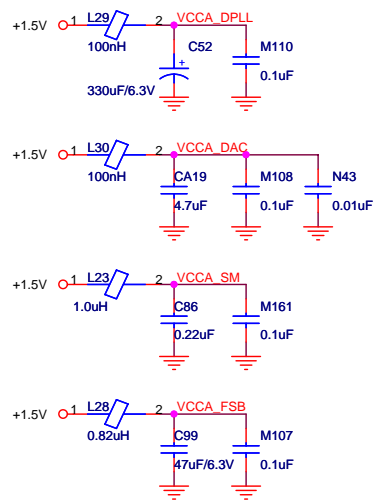
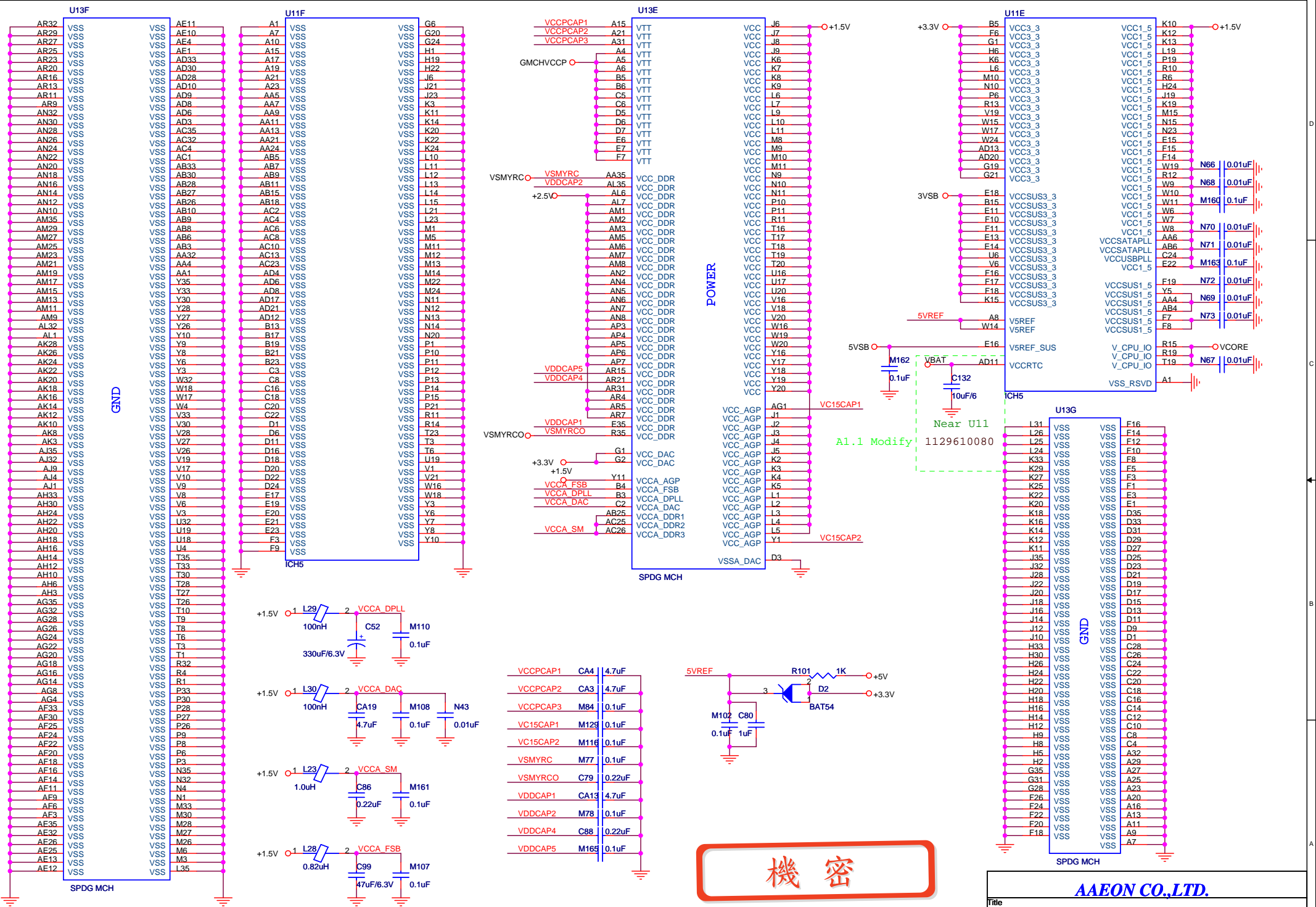
close to pin AP9

SPDG MCH

9	MAAB0	AG31	SMAA_B0	SDQS_B0	AF15	DQSB0
9	MAAB1	AJ31	SMAA_B1	SDM_B0	AG11	SDMB0
9	MAAB2	AD27	SMAA_B2	SDQ_B0	AJ10	MDB0
9	MAAB3	AE27	SMAA_B3	SDQ_B1	AE15	MDB1
9	MAAB4	AK27	SMAA_B4	SDQ_B2	AL11	MDB2
9	MAAB5	AG25	SMAA_B5	SDQ_B3	AE16	MDB3
9	MAAB6	AL25	SMAA_B6	SDQ_B4	AL8	MDB4
9	MAAB7	AF21	SMAA_B7	SDQ_B5	AF12	MDB5
9	MAAB8	AL23	SMAA_B8	SDQ_B6	AK11	MDB6
9	MAAB9	AJ22	SMAA_B9	SDQ_B7	AG12	MDB7
9	MAAB10	AF29	SMAA_B10	SDQS_B1	AG13	DQSB1
9	MAAB11	AL21	SMAA_B11	SDM_B1	AG15	SDMB1
9	MAAB12	AJ20	SMAA_B12	SDQ_B8	AE17	MDB8
		XAE27	SMAB_B1	SDQ_B9	AL13	MDB9
		XAD26	SMAB_B2	SDQ_B10	AK17	MDB10
		XAL29	SMAB_B3	SDQ_B11	AL17	MDB11
		XAL27	SMAB_B4	SDQ_B12	AK13	MDB12
		XAE23	SMAB_B5	SDQ_B13	AL14	MDB13
				SDQ_B14	AL16	MDB14
				SDQ_B15	AJ18	MDB15
				SDQS_B2	AG21	DQSB2
				SDM_B2	AE21	SDMB2
				SDQ_B16	AE19	MDB16
				SDQ_B17	AE20	MDB17
				SDQ_B18	AG23	MDB18
				SDQ_B19	AK23	MDB19
				SDQ_B20	AL19	MDB20
				SDQ_B21	AK21	MDB21
				SDQ_B22	AJ24	MDB22
				SDQ_B23	AE22	MDB23
				SDQS_B3	AH27	DQSB3
				SDM_B3	AJ28	SDMB3
				SDQ_B24	AK25	MDB24
				SDQ_B25	AH26	MDB25
				SDQ_B26	AG27	MDB26
				SDQ_B27	AF27	MDB27
				SDQ_B28	AJ26	MDB28
				SDQ_B29	AJ27	MDB29
				SDQ_B30	AD25	MDB30
				SDQ_B31	AE28	MDB31
				SDQS_B4	AD29	DQSB4
				SDM_B4	AC31	SDMB4
				SDQ_B32	AE30	MDB32
				SDQ_B33	AC27	MDB33
				SDQ_B34	AC30	MDB34
				SDQ_B35	Y29	MDB35
				SDQ_B36	AE31	MDB36
				SDQ_B37	AB29	MDB37
				SDQ_B38	AA26	MDB38
				SDQ_B39	AA27	MDB39
				SDQS_B5	U30	DQSB5
				SDM_B5	U31	SDMB5
				SDQ_B40	AA30	MDB40
				SDQ_B41	W30	MDB41
				SDQ_B42	U27	MDB42
				SDQ_B43	T25	MDB43
				SDQ_B44	AA31	MDB44
				SDQ_B45	V29	MDB45
				SDQ_B46	U25	MDB46
				SDQ_B47	R27	MDB47
				SDQS_B6	L27	DQSB6
				SDM_B6	M29	SDMB6
				SDQ_B48	P29	MDB48
				SDQ_B49	R30	MDB49
				SDQ_B50	K28	MDB50
				SDQ_B51	L30	MDB51
				SDQ_B52	R31	MDB52
				SDQ_B53	R26	MDB53
				SDQ_B54	P25	MDB54
				SDQ_B55	L32	MDB55
				SDQS_B7	J30	DQSB7
				SDM_B7	J31	SDMB7
				SDQ_B56	K30	MDB56
				SDQ_B57	H29	MDB57
				SDQ_B58	F32	MDB58
				SDQ_B59	G33	MDB59
				SDQ_B60	N25	MDB60
				SDQ_B61	M25	MDB61
				SDQ_B62	J29	MDB62
				SDQ_B63	G32	MDB63

DDR Channel B

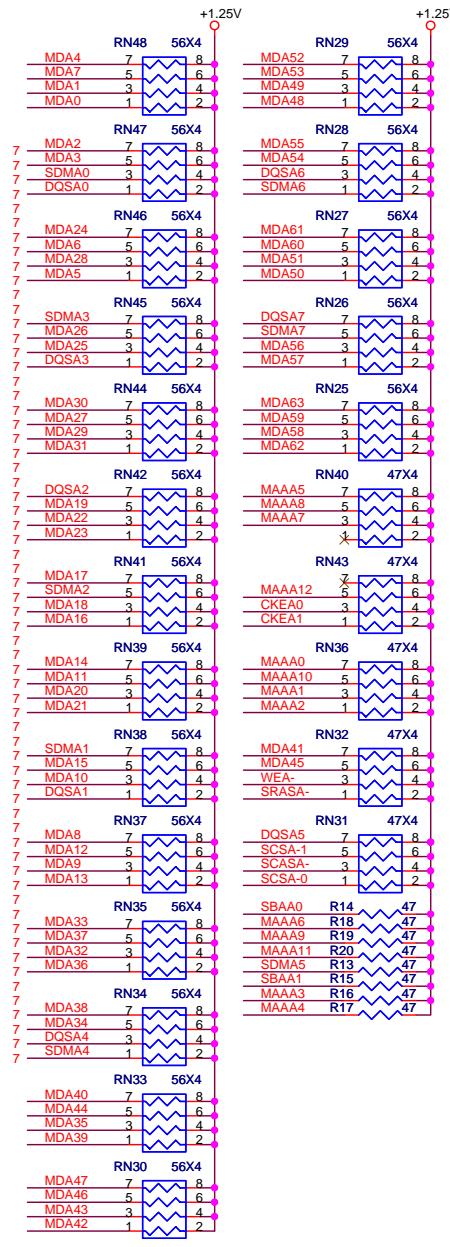
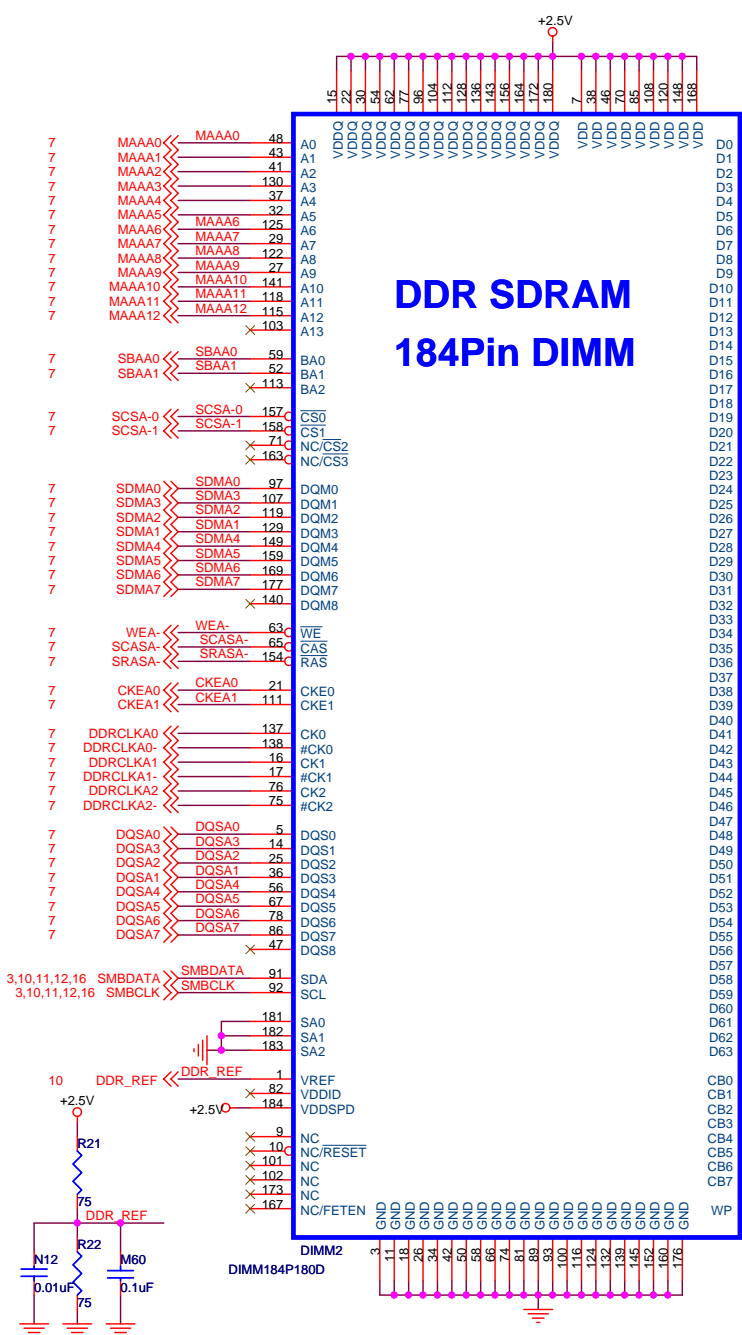




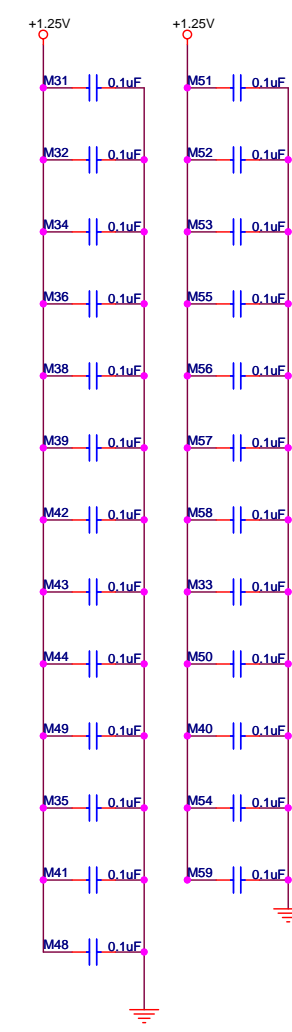
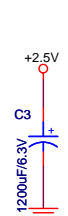
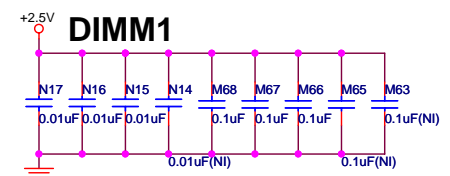
DCR under 60mOhm Close to GMCH



DDR SDRAM 184Pin DIMM



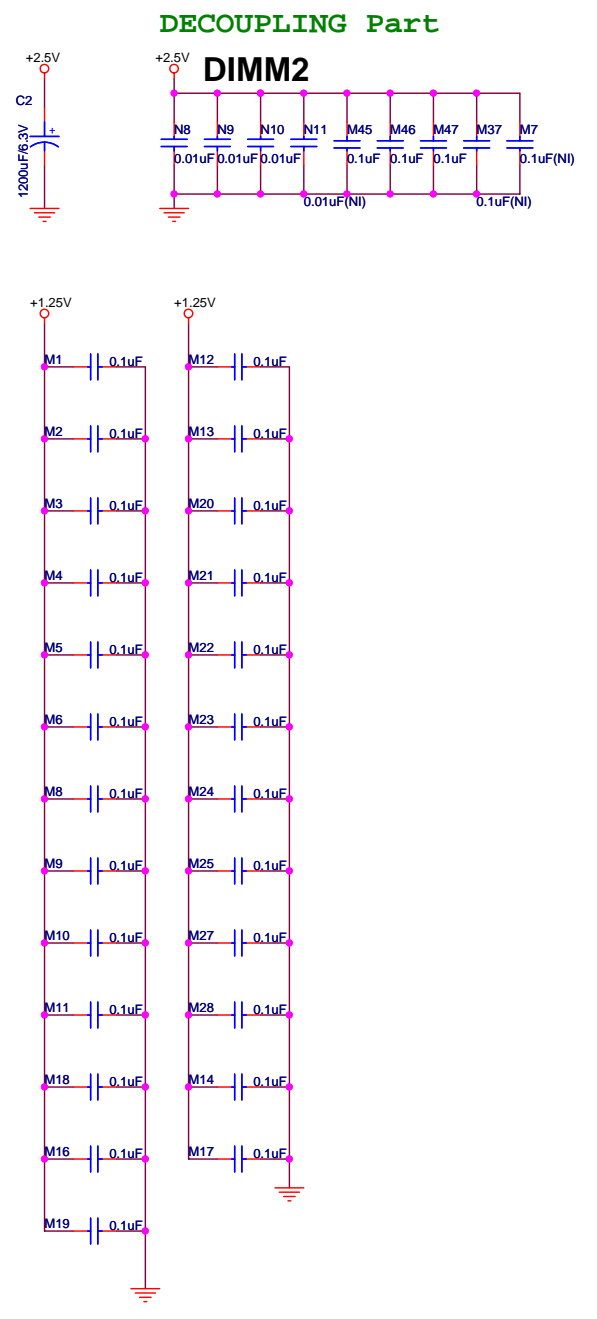
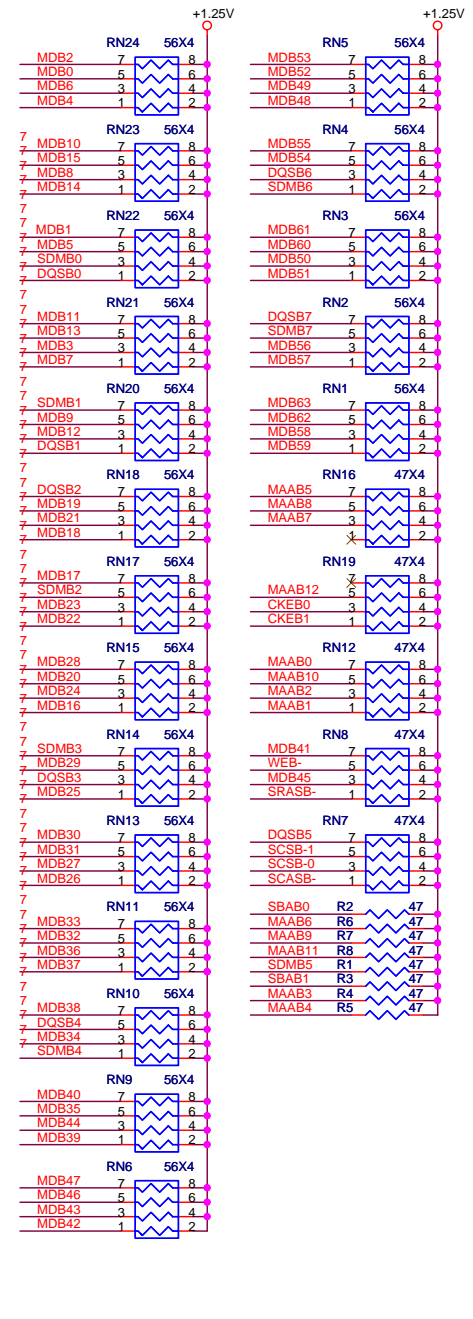
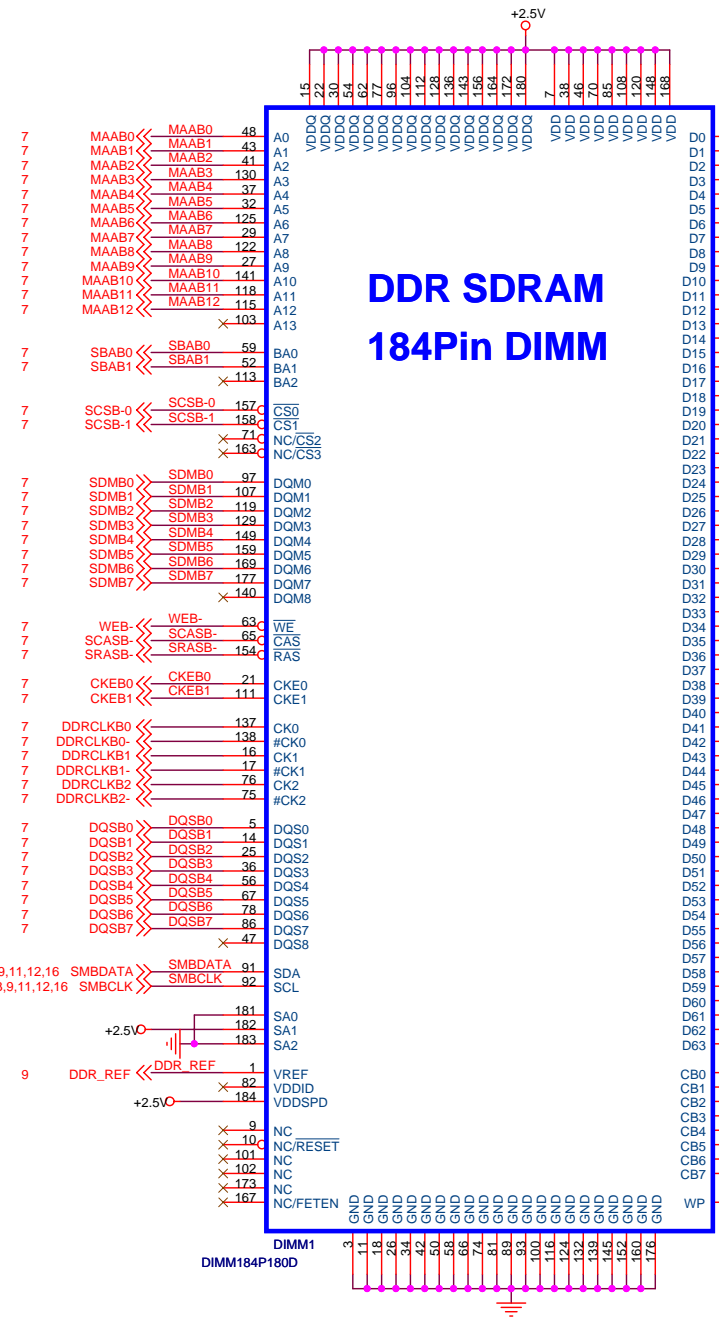
DECOUPLING Part



機密

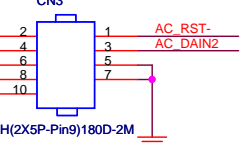
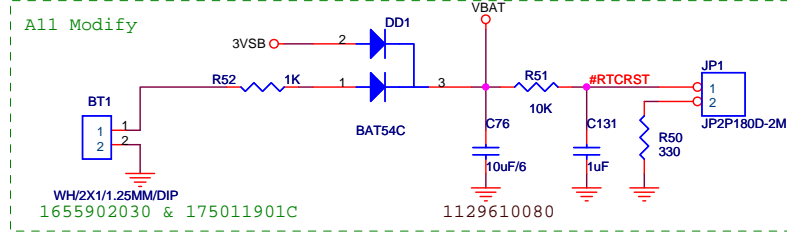
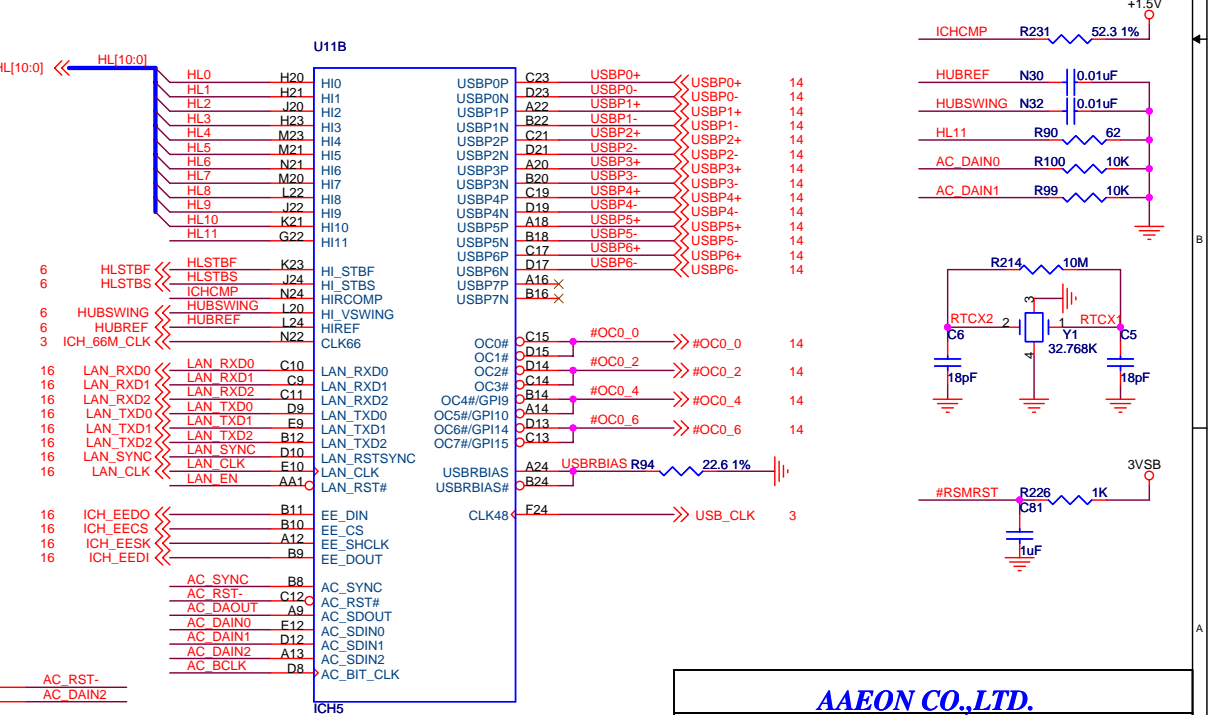
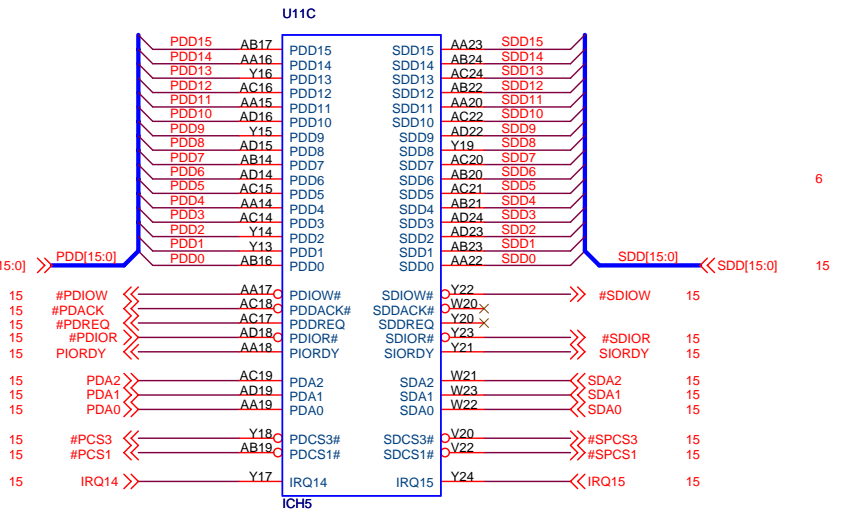
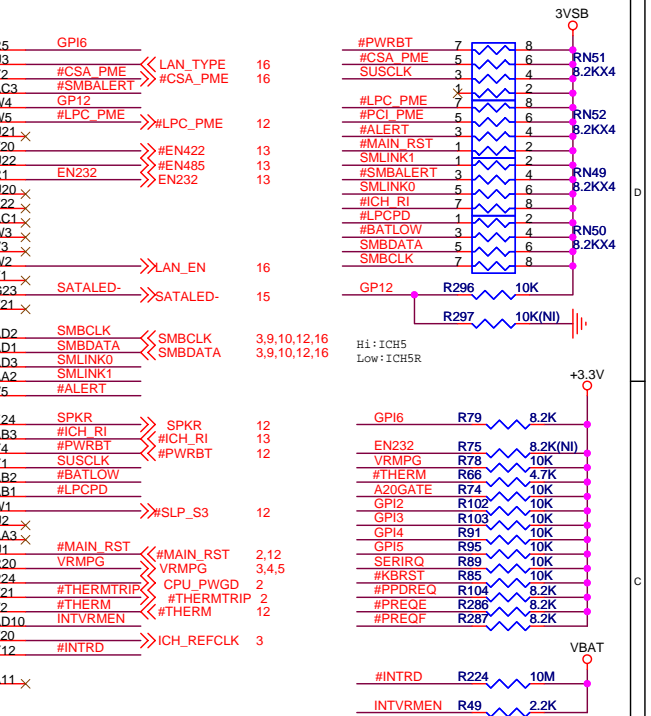
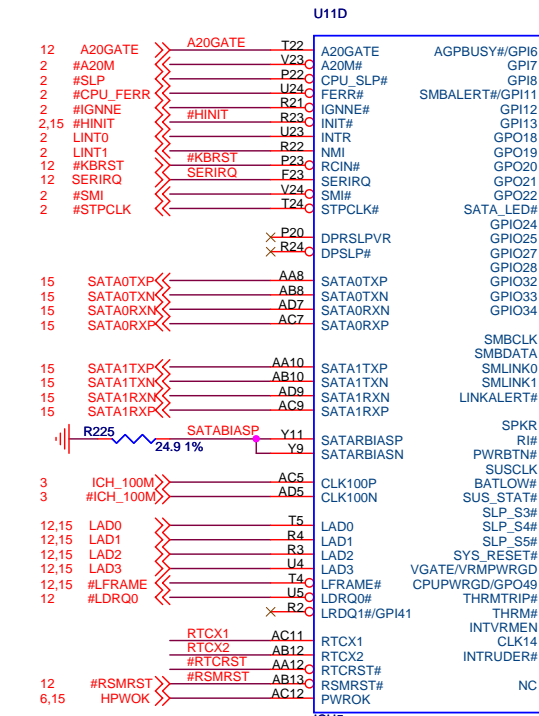
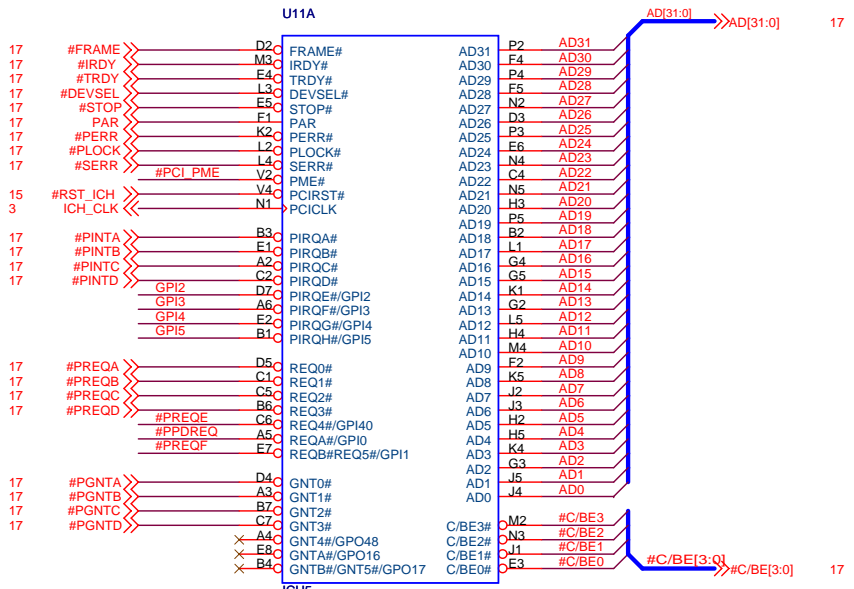
AAEON CO.,LTD.		
DDR DIMM1,2		
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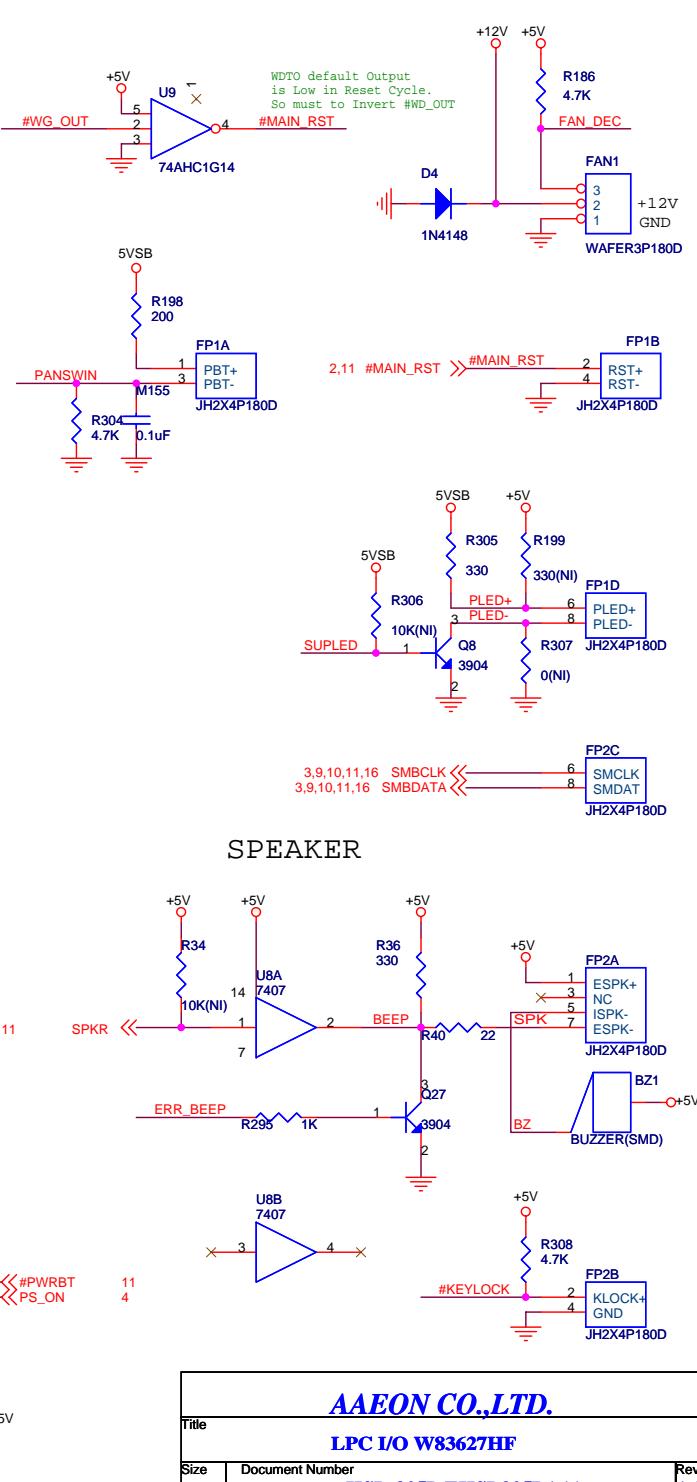
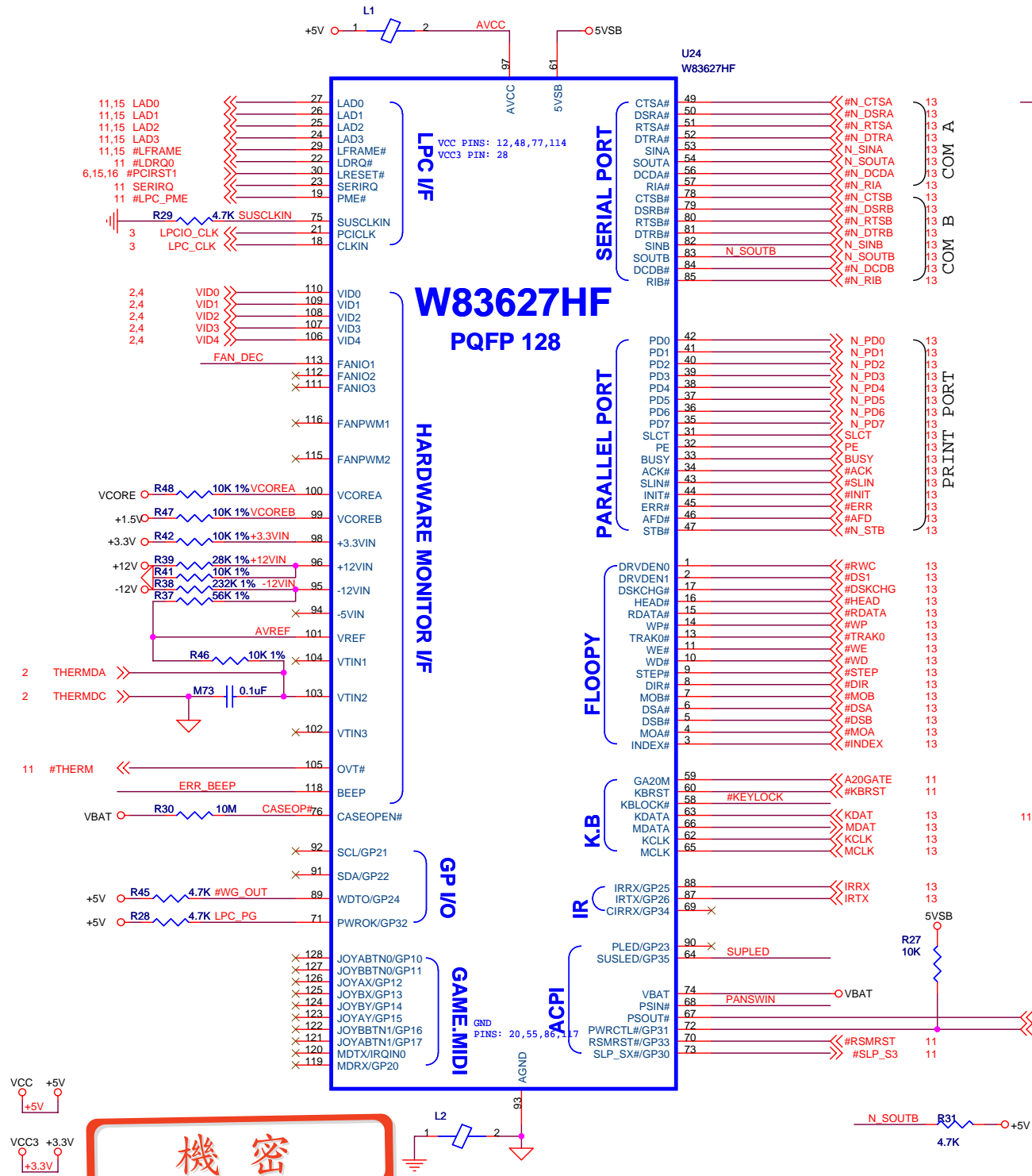


機 密

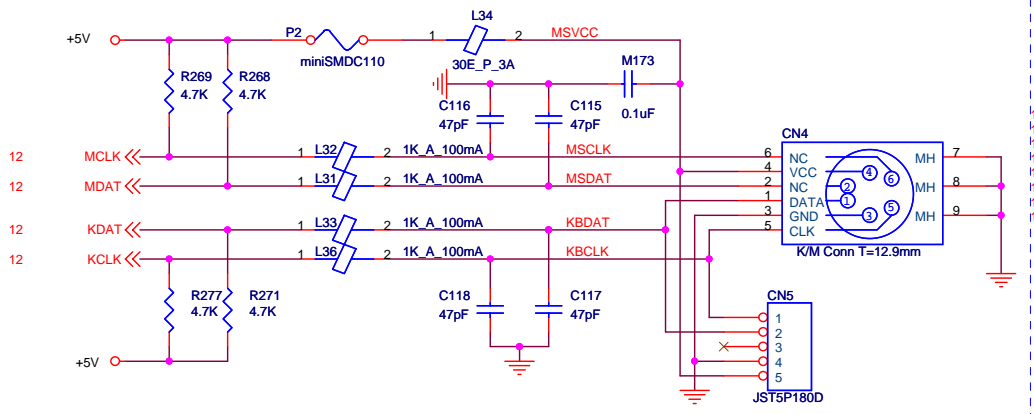
AAEON CO.,LTD.		
DDR DIMM 3		
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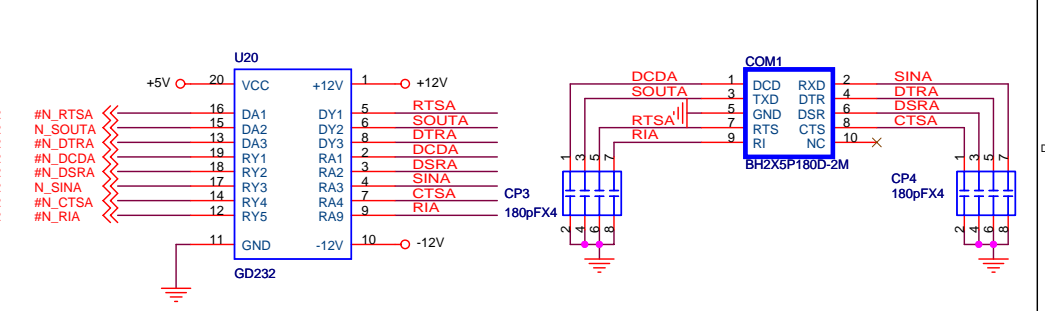
Title			AEEON CO.,LTD.		
Size			ICH5		
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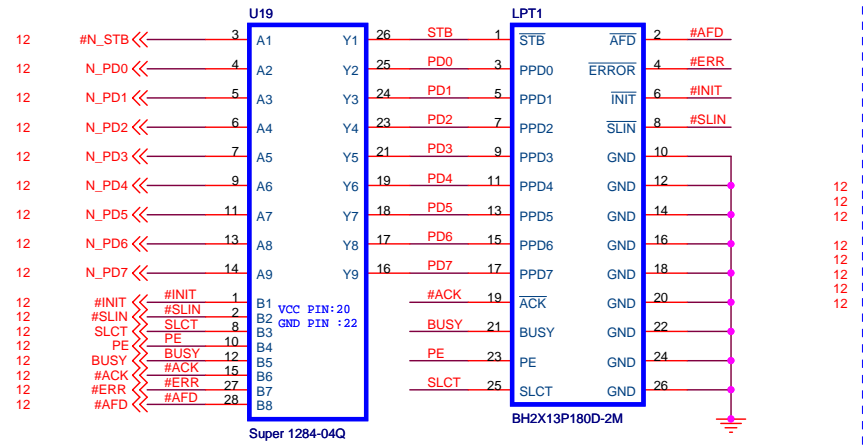
PS2 KB/MS



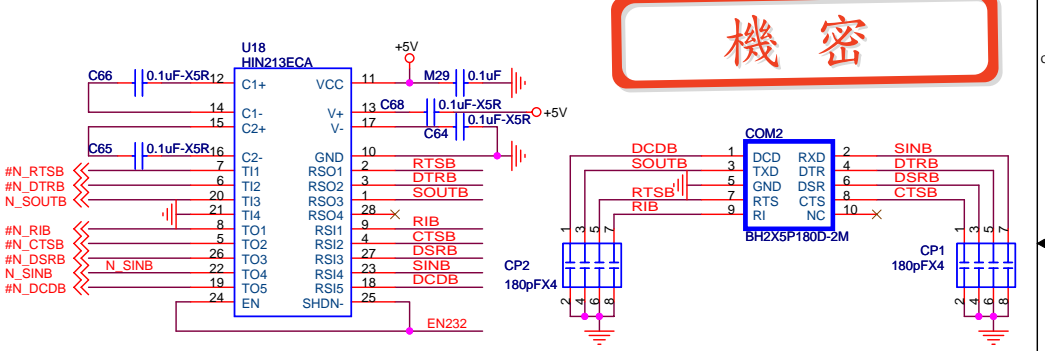
COM1:RS232



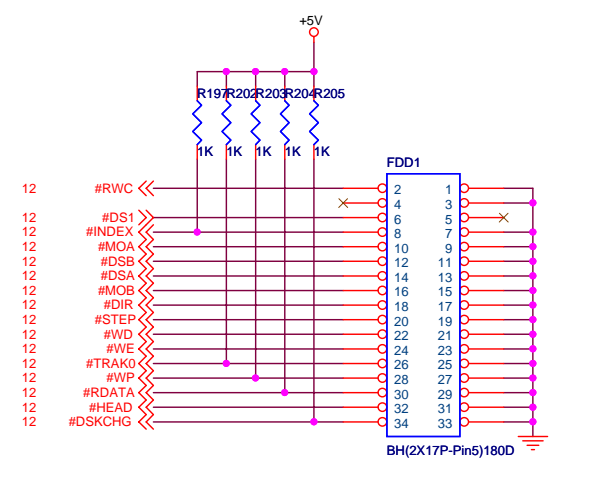
PARALLEL



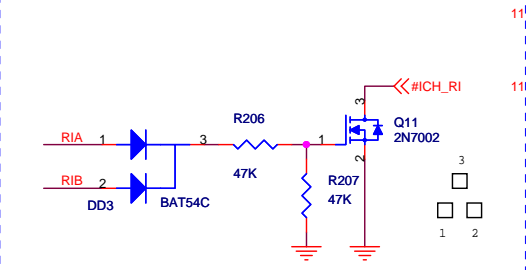
COM2:RS232/422/485



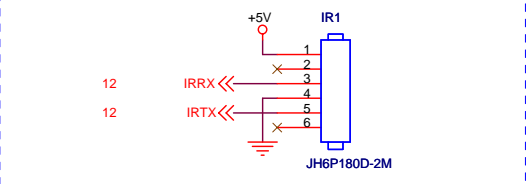
FLOPPY



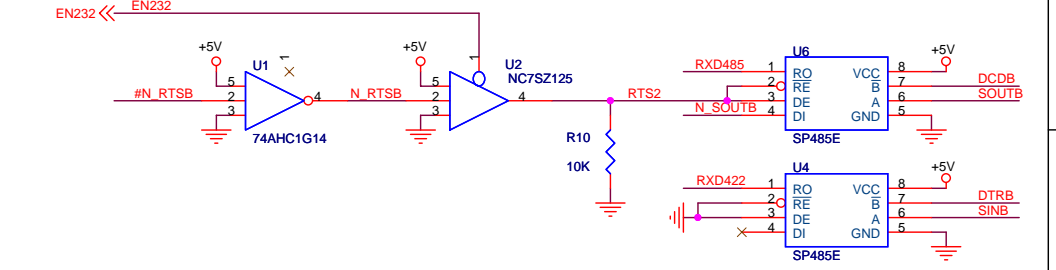
Ring IN



IR



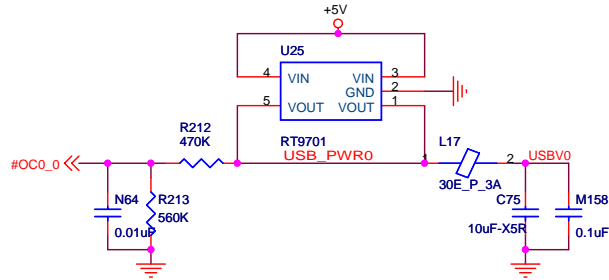
RS232/422/485 Select



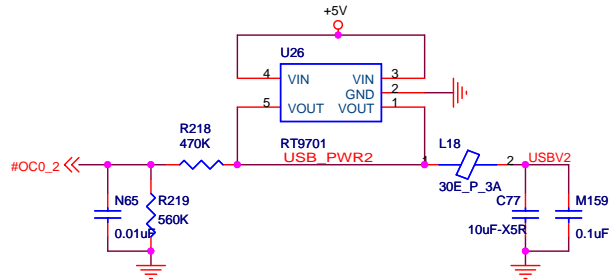
check
2G125
working
voltage

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KB/COM/Printer/FDD/Connector		
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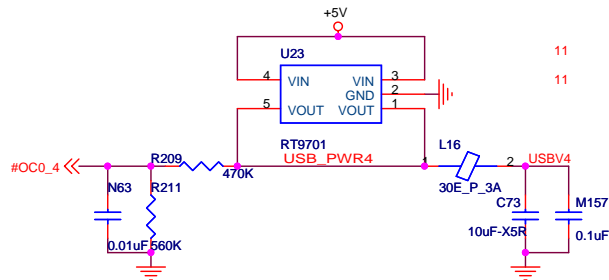
USB A,B



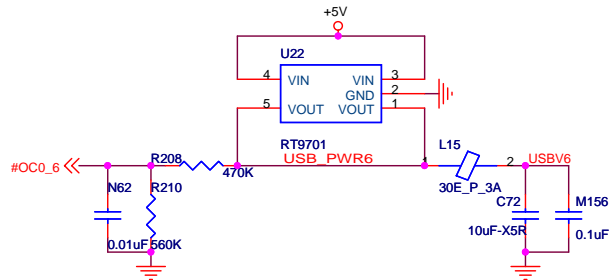
USB C,D



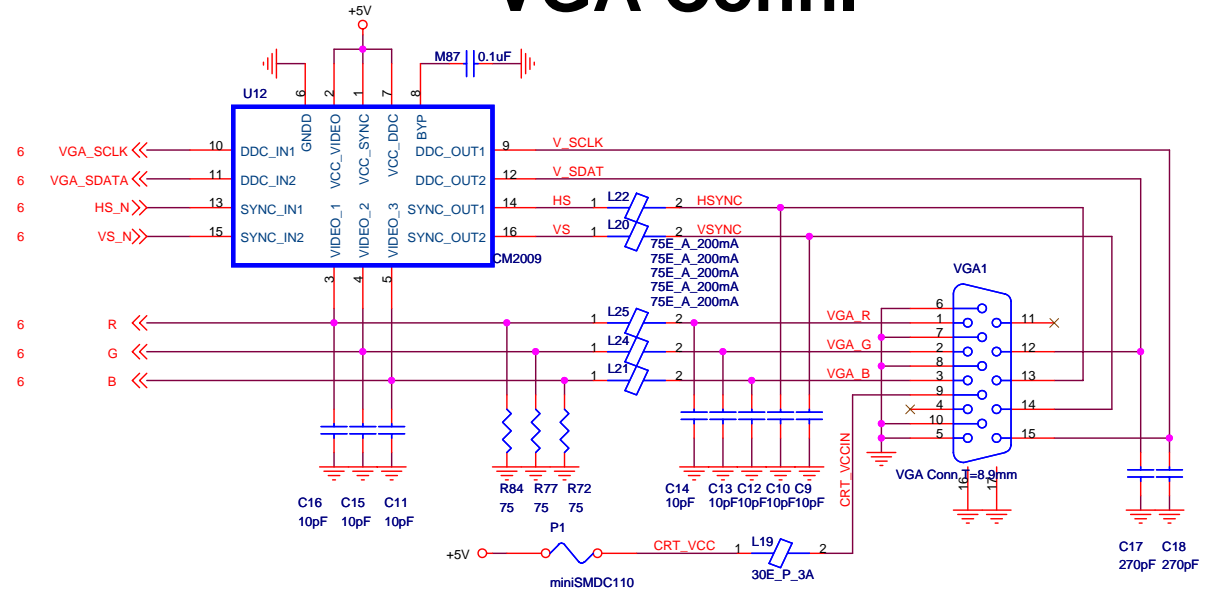
USB E,F



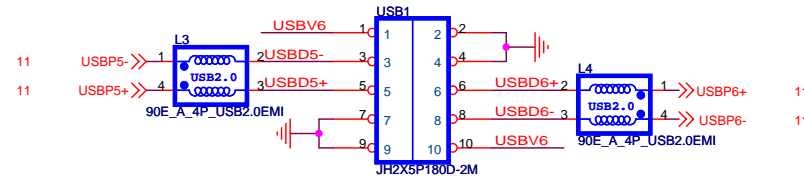
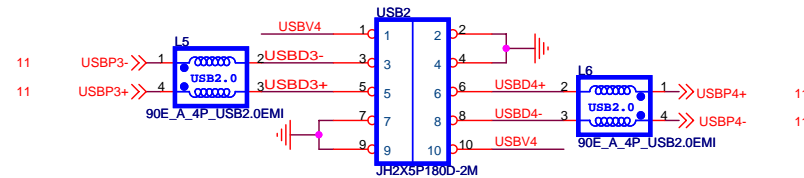
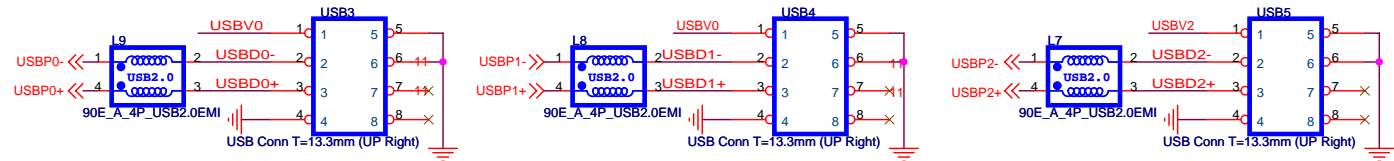
USB G,H



VGA Conn.



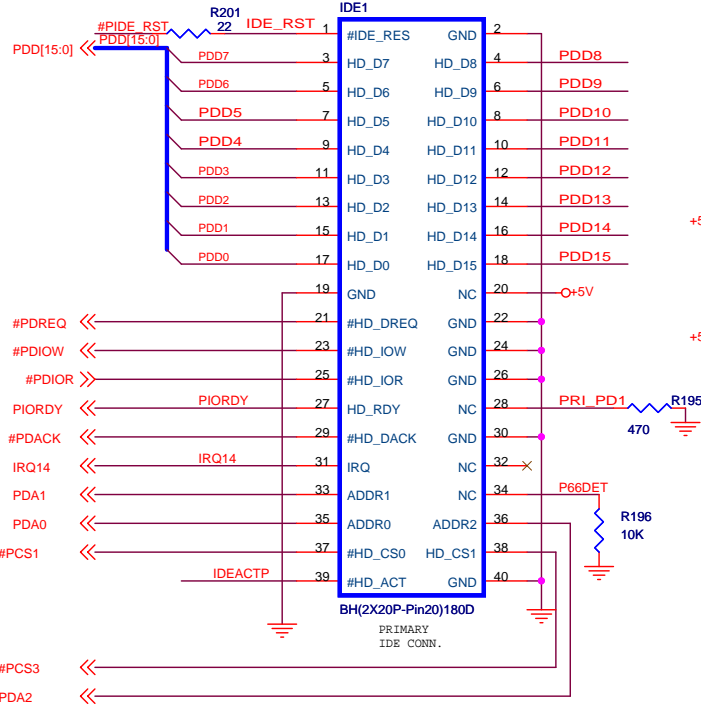
USB Conn.



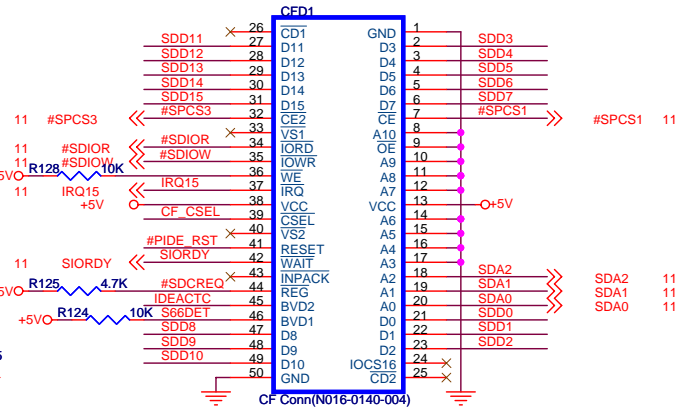
機密

IDE CONNECTOR

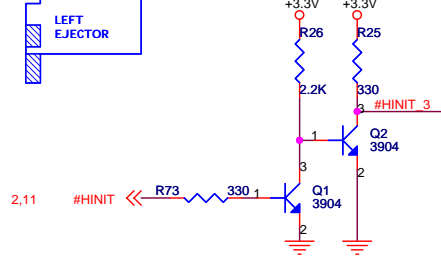
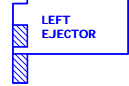
PRIMARY IDE



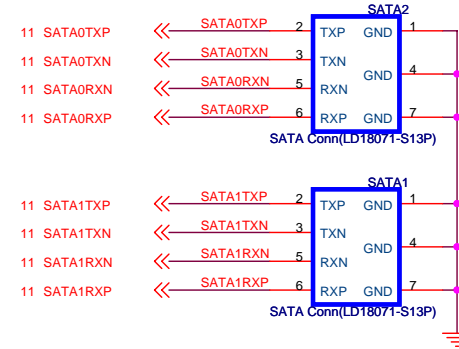
Compact Flash IDE



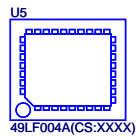
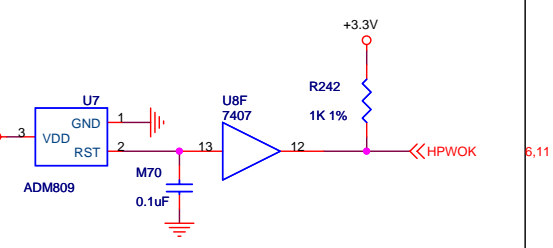
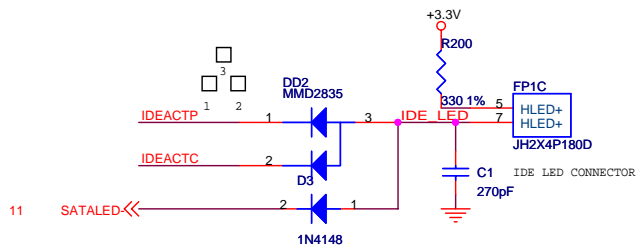
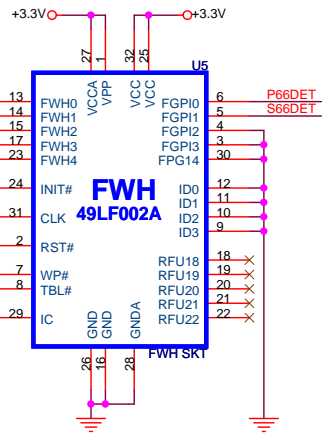
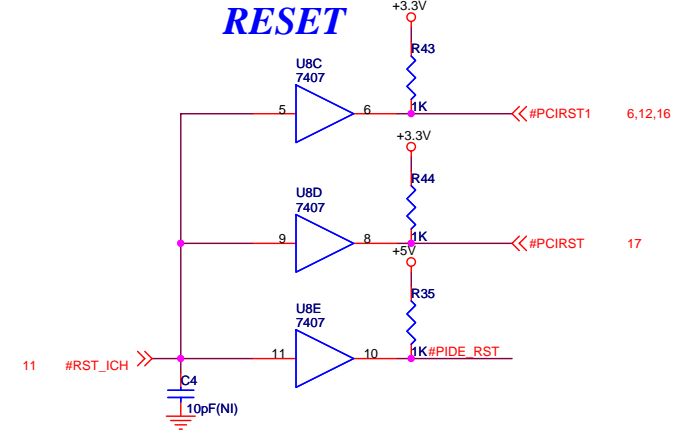
CFID1 CF Ejector(N003-0011-210)



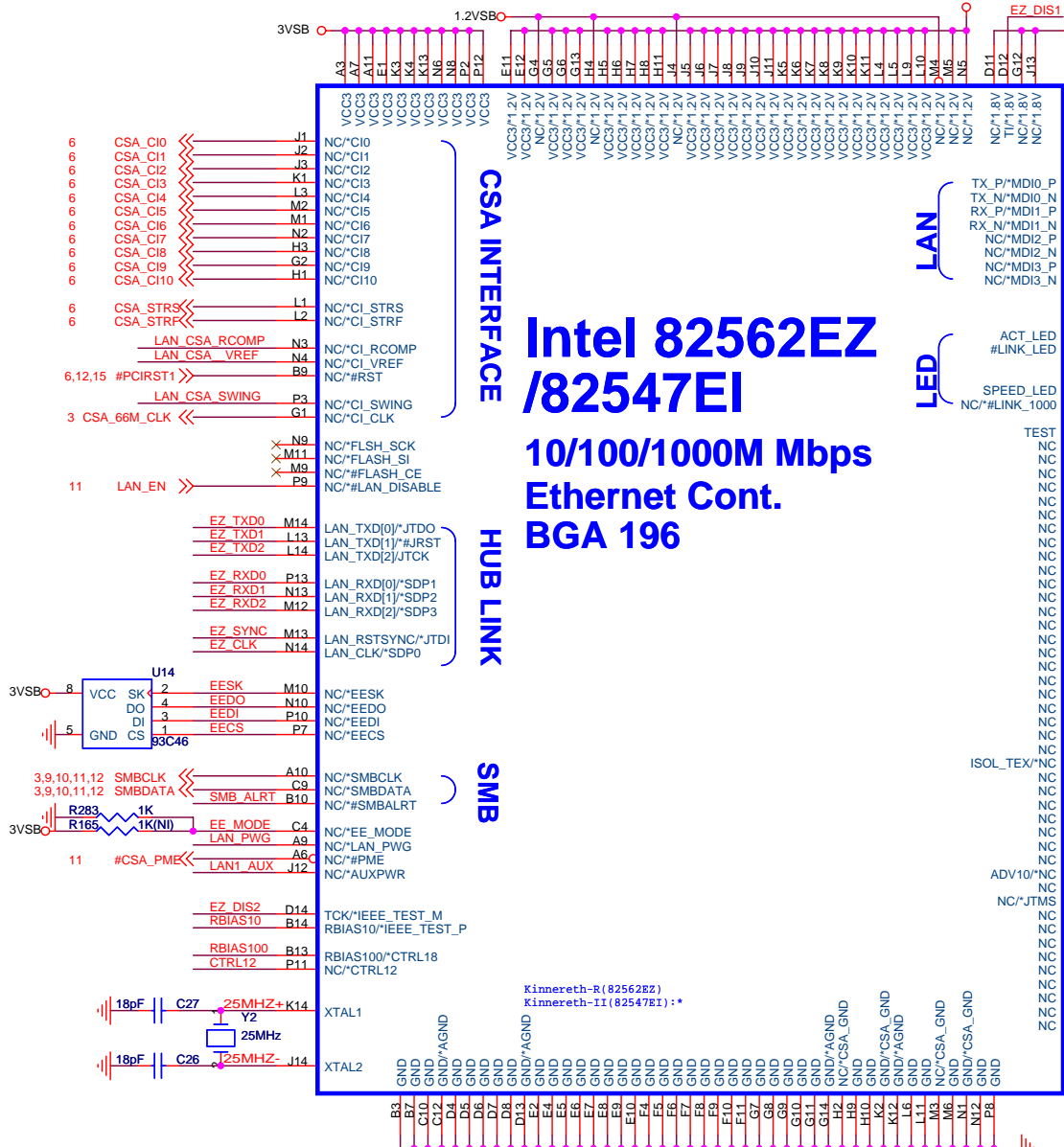
Serial ATA IDE



RESET



AAEON CO.,LTD.			
IDE & Compact Flash & S-ATA			
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Intel 82562EZ /82547EI
10/100/1000M Mbps Ethernet Cont. BGA 196

CSA INTERFACE

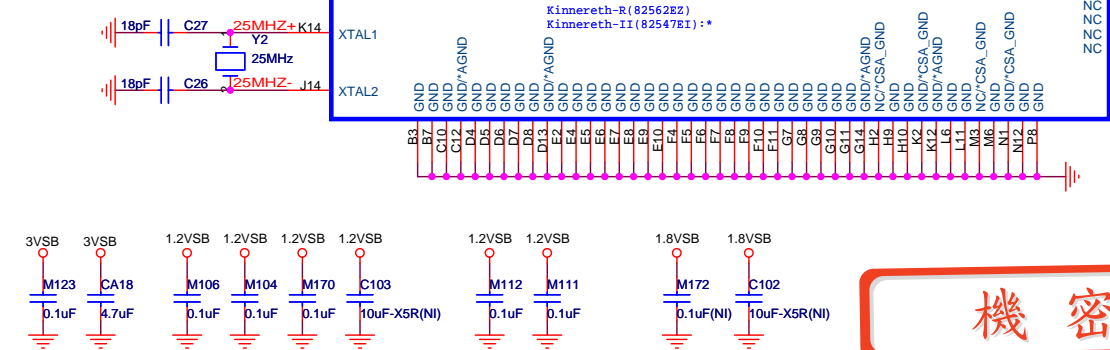
- 6 CSA_C10 << J1
- 6 CSA_C11 << J2
- 6 CSA_C12 << J3
- 6 CSA_C13 << J4
- 6 CSA_C14 << J5
- 6 CSA_C15 << J6
- 6 CSA_C16 << J7
- 6 CSA_C17 << J8
- 6 CSA_C18 << J9
- 6 CSA_C19 << J10
- 6 CSA_C10 << J11

HUB LINK

- 6 CSA_STRS << L1
- 6 CSA_STRF << L2
- LAN_CSA_RCOMP N3
- LAN_CSA_VREF N4
- 6,12,15 #PCIRST1 << B9
- LAN_CSA_SWING P3
- 3 CSA_66M_CLK << G1

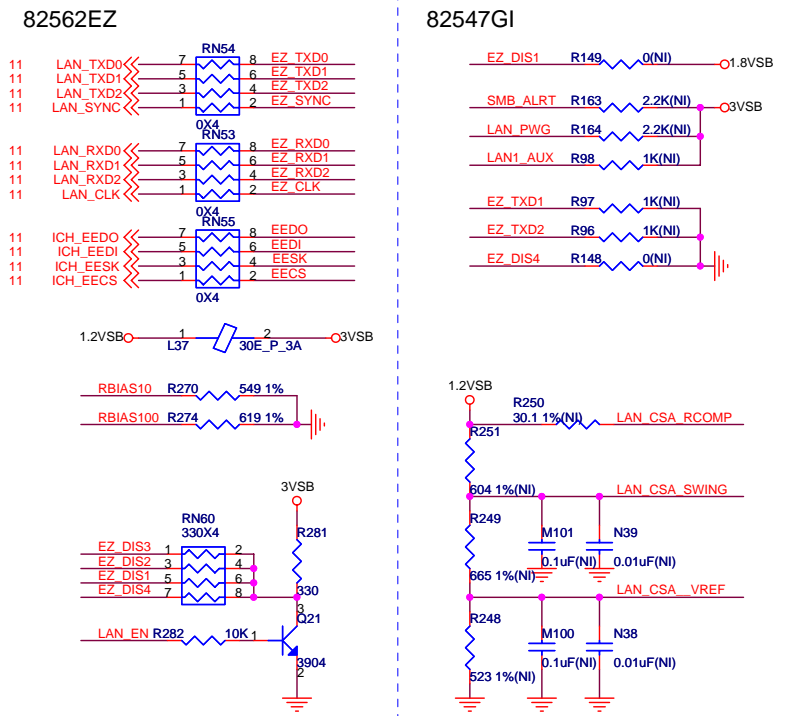
SMB

- 3,9,10,11,12 SMBCLK << A10
- 3,9,10,11,12 SMBDATA << C9
- R283 1K << R283
- R165 1K(NI) << R165
- 11 #CSA_PME << J12



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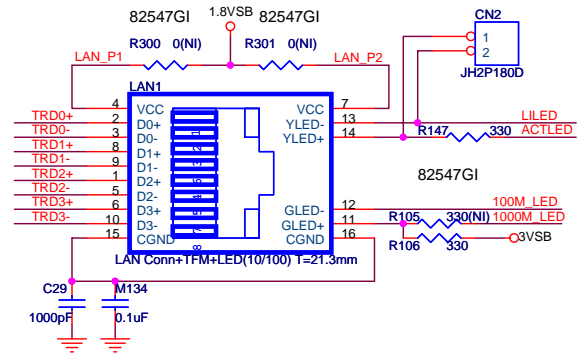
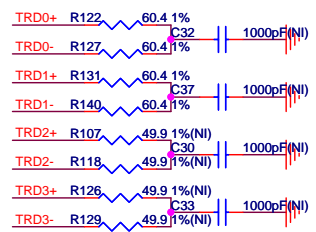
LAN (I82547GI/I82562EZ) --> Default I82562



82562EZ : Low
 82547GI : Hi

LAN_TYPE << R284 10K(NI) +3.3V
 R285 10K

82562EZ : Install 60.4 ohm
 82547GI : Install 49.9 ohm



Reversion History

Version	Date	Reason Description	Action	Modify Part	Designed By
A1.0	2005 / 02 / 23	First Release.	Finished	Schematic / Board File / BOM	ISD Engineer
A1.1	2007 / 09 / 11	Modify Battery socket	Change Battery connector	Schematic / Board File / BOM	Fred
		Modify RTC Design	Add RTC Bypass Cap	Schematic / Board File / BOM	Fred

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AAEON CO.,LTD.		
Title Reversion History		
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