

OMNI-3155-UP

Industrial Modular Touch Panel PC With Intel® X5-Z8350 Processor

User's Manual 1st Ed

Last Updated: September 12, 2018

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Packing List

Before setting up your product, please make sure the following items have been shipped:

| ltem | | Quantity |
|------|--|----------|
| • | OMNI-3155-UP Panel PC | 1 |
| • | USB Adapter Micro B male to A female | 1 |
| • | Product CD with drivers | 1 |
| • | AC to DC Power Adapter (power cord for optional) | 1 |
| • | Water Proof Sponge | 1 |
| • | Panel Mount Screws package | 1 |

If any of these items are missing or damaged, please contact your distributor or sales representative immediately.

This User's Manual contains all the essential information, such as detailed descriptions and explanations on the product's hardware and software features (if any), its specifications, dimensions, jumper/connector settings/definitions, and driver installation instructions (if any), to facilitate users in setting up their product.

Users may refer to the AAEON.com for the latest version of this document.

Safety Precautions

Please read the following safety instructions carefully. It is advised that you keep this manual for future references

- 1. All cautions and warnings on the device should be noted.
- All cables and adapters supplied by AAEON are certified and in accordance with the material safety laws and regulations of the country of sale. Do not use any cables or adapters not supplied by AAEON to prevent system malfunction or fires.
- 3. Make sure the power source matches the power rating of the device.
- 4. Position the power cord so that people cannot step on it. Do not place anything over the power cord.
- Always completely disconnect the power before working on the system's hardware.
- No connections should be made when the system is powered as a sudden rush of power may damage sensitive electronic components.
- If the device is not to be used for a long time, disconnect it from the power supply to avoid damage by transient over-voltage.
- 8. Always disconnect this device from any AC supply before cleaning.
- 9. While cleaning, use a damp cloth instead of liquid or spray detergents.
- 10. Make sure the device is installed near a power outlet and is easily accessible.
- 11. Keep this device away from humidity.
- 12. Place the device on a solid surface during installation to prevent falls
- 13. Do not cover the openings on the device to ensure optimal heat dissipation.
- 14. Watch out for high temperatures when the system is running.
- 15. Do not touch the heat sink or heat spreader when the system is running
- 16. Never pour any liquid into the openings. This could cause fire or electric shock.

Preface

- 17. As most electronic components are sensitive to static electrical charge, be sure to ground yourself to prevent static charge when installing the internal components. Use a grounding wrist strap and contain all electronic components in any static-shielded containers.
- 18. If any of the following situations arises, please the contact our service personnel:
 - i. Damaged power cord or plug
 - ii. Liquid intrusion to the device
 - iii. Exposure to moisture
 - iv. Device is not working as expected or in a manner as described in this manual
 - v. The device is dropped or damaged
 - vi. Any obvious signs of damage displayed on the device

DO NOT LEAVE THIS DEVICE IN AN UNCONTROLLED ENVIRONMENT WITH TEMPERATURES BEYOND THE DEVICE'S PERMITTED STORAGE TEMPERATURES (SEE CHAPTER 1) TO PREVENT DAMAGE.

FCC Statement



This device complies with Part 15 FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received including interference that may cause undesired operation.

Caution:

There is a danger of explosion if the battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions and your local government's recycling or disposal directives.

Attention:

Il y a un risque d'explosion si la batterie est remplacée de façon incorrecte. Ne la remplacer qu'avec le même modèle ou équivalent recommandé par le constructeur. Recycler les batteries usées en accord avec les instructions du fabricant et les directives gouvernementales de recyclage.

China RoHS Requirements (CN)

产品中有毒有害物质或元素名称及含量

AAEON Panel PC/ Workstation

| | 有毒有害物质或元素 | | | | | |
|--|-----------|------|------|----------|-------|--------|
| 部件名称 | 铅 | 汞 | 镉 | 六价铬 | 多溴联苯 | 多溴二苯醚 |
| | (Pb) | (Hg) | (Cd) | (Cr(VI)) | (PBB) | (PBDE) |
| 印刷电路板 | 0 | 0 | 0 | 0 | 0 | 0 |
| 及其电子组件 | 0 | 0 | 0 | 0 | 0 | 0 |
| 外部信号 | 0 | 0 | 0 | 0 | 0 | 0 |
| 连接器及线材 | 0 | 0 | 0 | 0 | 0 | 0 |
| 外壳 | 0 | 0 | 0 | 0 | 0 | 0 |
| 中央处理器 | 0 | 0 | 0 | 0 | 0 | 0 |
| 与内存 | 0 | 0 | 0 | 0 | 0 | 0 |
| 硬盘 | 0 | 0 | 0 | 0 | 0 | 0 |
| 液晶模块 | 0 | 0 | 0 | 0 | 0 | 0 |
| 光驱 | 0 | 0 | 0 | 0 | 0 | 0 |
| 触控模块 | 0 | 0 | 0 | 0 | 0 | 0 |
| 电源 | 0 | 0 | 0 | 0 | 0 | 0 |
| | | | | | | |
| O:表示该有毒有害物质在该部件所有均质材料中的含量均在 SJ/T 11363-2006标准规定的限量要求以下。 | | | | | | |

X:表示该有毒有害物质至少在该部件的某一均质材料中的含量超出 SJ/T 11363-2006 标准规定的限量要求。

备注:

一、此产品所标示之环保使用期限,系指在一般正常使用状况下。 二、上述部件物质中央处理器、内存、硬盘、光驱、触控模块为选购品。

China RoHS Requirement (EN)

Poisonous or Hazardous Substances or Elements in Products

AAEON Panel PC/ Workstation

| | Poisonous or Hazardous Substances or Elements | | | | | | | |
|--|---|-----------------|-----------------|------------------------------------|--------------------------------------|---|--|--|
| Component | Lead (Pb) | Mercury (Hg) | Cadmium (Cd) | Hexavalent Chromium (Cr(VI)) | Polybrominated Biphenyls (PBB) | Polybrominated Diphenyl Ethers (PBDE) | | |
| PCB & Other Components | 0 | 0 | 0 | 0 | 0 | 0 | | |
| Wires & Connectors for External Connections | 0 | 0 | 0 | 0 | 0 | Ο | | |
| Chassis | 0 | 0 | 0 | 0 | 0 | 0 | | |
| CPU & RAM | 0 | 0 | 0 | 0 | 0 | 0 | | |
| Hard Disk | 0 | 0 | 0 | 0 | 0 | 0 | | |
| LCD | 0 | 0 | 0 | 0 | 0 | 0 | | |
| Optical Drive | 0 | 0 | 0 | 0 | 0 | 0 | | |
| Touchscreen | 0 | 0 | 0 | 0 | 0 | 0 | | |
| PSU | 0 | 0 | 0 | 0 | 0 | 0 | | |

O: The quantity of poisonous or hazardous substances or elements found in each of the component's parts is below the SJ/T 11363-2006-stipulated requirement.

X: The quantity of poisonous or hazardous substances or elements found in at least one of the component's parts is beyond the SJ/T 11363-2006-stipulated requirement.

Note: The Environment Friendly Use Period as labeled on this product is applicable under normal usage only

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Chapter 1

Product Specifications

DMNI-3155-UP

Specifications 1.1

| Syste | System | | | |
|-------|----------------|--|--|--|
| • | Processor | Intel® Atom® X5-Z8350, 1.92 GHz | | |
| • | Graphics | Intel® HD 400 Graphics | | |
| • | System Memory | 1GB / 2GB / 4GB DDR3L-1600 | | |
| • | Ethernet | Gb Ethernet x 1 (full speed) RJ-45 | | |
| • | Side I/O | USB OTG x 1 | | |
| | | HDMI x 1 | | |
| | | DC Jack x 1 | | |
| • | Bottom I/O | DB-9 for RS232 x 1 | | |
| | | 10/100/1000Base-T, RJ-45 x 1 | | |
| | | USB 2.0 A Type x 4 | | |
| • | Storage | 16GB / 32 GB / 64 GB eMMC | | |
| • | Expansion Slot | 40 pin General Purpose bus, supported by | | |
| | | Altera Max V. ADC 8-bit@188ksos | | |
| • | OS Support | Windows® 10 | | |
| | | Android 6.0 | | |
| | | Linux Ubuntu 16.04 | | |
| | | | | |

Environmental ~ T . . (

| • | Operating Temperature | -20°C~55°C (-4 ~ 131°F) with 0.5 m/s air flow |
|---|-----------------------|---|
| • | Storage Temperature | -20 ~ 70°C (-4 ~ 158°F) |
| • | Operating Humidity | 90% @ 40°C, non-condensing |

- Anti-Vibration 1 Grms/ 5 ~ 500 Hz, X, Y, Z axes
- EMC CE/FCC Class A

Power Supply

DC Input

5 V

| Med | Mechanical | | |
|-----|-----------------------|--|--|
| • | Construction | IP65/ NEMA 4 for aluminum front bezel | |
| • | Mounting | Panel Mount / VESA 100 | |
| • | Dimension (W x H x D) | 369 x 314 x 56.7 mm (14.5 x 12.4 x 2.2") | |
| • | Carton Dimension | 510 x 460 x 250 mm (20.1 x 18.1 x 9.8") | |
| | (W x H x D) | | |
| • | Gross Weight | 5.3 kg (11.66 lb) | |

LCD• Display Type15" TFT LCD• Max. Resolution1024 x 768• Max Colors16.7M (8 bit/color)• Luminance (cd/m²)300 nits

- Viewing Angle 176° (H), 176° (V)
- Backlight LED
- Backlight MTBF (Hours) 70,000hrs

| Touchscreen (Resistive) | | | |
|-------------------------|-----------------------------------|--|--|
| • Туре | 5-Wire resistive | | |
| • Light Transmission | 80% ± 2% | | |
| • Lifetime | 100,000,000 keystrokes | | |
| | | | |
| Touchscreen (P-CAP) | | | |
| • Туре | Projected capacitive touch screen | | |
| Light Transmission | ≥88% | | |

Lifetime

Chapter 2

Hardware Information

DMNI-3155-UP

2.1 Dimensions





2.2 I/O Ports introduction



5V DC in



2.3 Jumpers and Connectors

Please refer to the table below for all of the system's jumpers and connectors that you can configure for your application

| Label | Function |
|-------|-------------------------|
| CN1 | RTC Battery |
| CN6 | USB 3.0 Micro Connector |
| CN7 | USB 2.0 1x10P Wafer |
| CN8 | USB Type A Connector 1 |
| CN9 | USB Type A Connector 2 |
| CN10 | RJ45 LAN Connector |
| CN11 | HDMI Connector |
| CN12 | HAT 40 GPIO Connector |
| CN14 | Reset Pin Header |
| CN30 | DC Jack |
| CN31 | MIPI DSI Connector |
| CN32 | MIPI CSI Connector |
| CN33 | Power Button Wafer |
| CN34 | Update CPLD Header |



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2.3.1 RTC Battery Wafer (CN1)



| Pin | Signal |
|-----|-------------|
| 1 | +V_COIN_BAT |
| 2 | GND |

2.3.2 USB 3.0 Micro Connector (CN6)



| Pin | Signal | Pin | Signal |
|-----|----------------|-----|----------------|
| 1 | USB_VCC | 8 | GND |
| 2 | USB2_D- | 9 | CROSSBAR_TX1_N |
| 3 | USB2_D+ | 10 | CROSSBAR_TX1_P |
| 4 | USB2_ID | 11 | GND |
| 5 | GND | 12 | GND |
| 6 | CROSSBAR_RX1_N | 13 | GND |
| 7 | CROSSBAR_RX1_P | 14 | GND |

2.3.3 USB 2.0 x 10P Wafer (CN7)



2.3.4 Dual USB Type A Connector 1 (CN8)



| Pin | Signal | Pin | Signal |
|-----|------------|-----|------------|
| 1 | USB_VCC | 5 | USB_VCC |
| 2 | USB2_P1_D- | 6 | USB2_P2_D- |
| 3 | USB2_P1_D+ | 7 | USB2_P2_D+ |
| 4 | GND | 8 | GND |

2.3.5 Dual USB Type A Connector 2 (CN9)



| Pin | Signal | Pin | Signal |
|-----|------------|-----|----------------|
| 1 | USB_VCC | 5 | USB_VCC |
| 2 | USB2_P3_D- | 6 | USB_HSIC_P2_D- |
| 3 | USB2_P3_D+ | 7 | USB_HSIC_P2_D+ |
| 4 | GND | 8 | GND |

2.3.6 RJ-45 LAN Connector (CN10)



| Pin | Signal | Pin | Signal |
|-----|------------|-----|---------------|
| R1 | LAN1_MDI0+ | R8 | LAN1_MDI2- |
| R2 | LAN1_MDI0- | R9 | LAN1_MDI3+ |
| R3 | LAN1_MDI1+ | R10 | LAN1_MDI3- |
| R4 | LAN1_MDI1- | L1 | LAN_ACTLEDP |
| R5 | LAN1_MDI2+ | L2 | LAN_ACTLEDN |
| R6 | LAN1_MDI2- | L3 | LAN_LINK100# |
| R7 | LAN1_MDI2+ | L4 | LAN_LINK1000# |

2.3.7 HDMI Connector (CN11)



| Pin | Signal | Pin | Signal |
|-----|-------------------|-----|-------------------|
| 1 | DDI2_TX0_HDMI_DP+ | 11 | GND |
| 2 | GND | 12 | DDI2_CLK_HDMI_DN- |
| 3 | DDI2_TX0_HDMI_DN- | 13 | HDMI_CEC_D |
| 4 | DDI2_TX1_HDMI_DP+ | 14 | NC |
| 5 | GND | 15 | DDI2_DDC_CLK |
| 6 | DDI2_TX1_HDMI_DN- | 16 | DDI2_DDC_DAT |
| 7 | DDI2_TX2_HDMI_DP+ | 17 | GND |
| 8 | GND | 18 | +5V_HDMI |
| 9 | DDI2_TX2_HDMI_DN- | 19 | DDI2_TYPE_C_HPD |
| 10 | DDI2_CLK_HDMI_DP+ | | |

2.3.8 HAT 40 GPIO Connector (CN12)

| | $ \begin{array}{c} 2\\ \bigcirc $ | 00000 | 40 0 0 0 0 0 0 0 0 0 0 0 0 39 |
|-----|---|-------|--|
| Pin | Signal | Pin | Signal |
| 1 | +3.3V | 2 | +5V |
| 3 | I2C1_SDA | 4 | +5V |
| 5 | I2C1_SCL | 6 | GND |
| 7 | ISH_GPIO0 | 8 | UART_TX |

| 9 | GND | 10 | UART_RX |
|----|-----------|----|--------------|
| 11 | SD2_CMD | 12 | I2S2_CLK |
| 13 | SD2_CLK | 14 | GND |
| 15 | SD2_SD0 | 16 | SD2_SD1 |
| 17 | +3.3V | 18 | SD2_SD2 |
| 19 | spi2_mosi | 20 | GND |
| 21 | SPI2_MISO | 22 | SD2_SD3 |
| 23 | SPI2_CLK | 24 | ISH_SPI2_CS0 |
| 25 | GND | 26 | ISH_SPI2_CS1 |
| 27 | I2C0_SDA | 28 | I2C0_SCL |
| 29 | ISH_GPIO2 | 30 | GND |
| 31 | ISH_GPIO3 | 32 | PWM0 |
| 33 | PWM1 | 34 | GND |
| 35 | I2S2_FRM | 36 | GPIO27 |
| 37 | GPIO13 | 38 | 12S2_RX |
| 39 | GND | 40 | 12S2_TX |

2.3.9 Reset Pin Header (CN14)

| Pin | Signal |
|-----|--------------|
| 1 | PMU_RSTBTN_N |
| 2 | GND |

2.3.10 DC Jack (CN30)



| Pin | Signal |
|-----|--------|
| 1 | +V5 |
| 2 | GND |
| 3 | GND |

2.3.11 MIPI DSI Connector (CN31)

| $40 \qquad 2$ $43 \qquad 41 \qquad 42$ $41 \qquad 42$ | | | | | |
|---|-----------------|-----|-------------|-----|------------------|
| Pin | Signal | Pin | Signal | Pin | Signal |
| 1 | MDSI_A_DATA1_DN | 16 | DDI0_TX0_DP | 31 | DDI0_HPD_CONN |
| 2 | MDSI_A_DATA1_DP | 17 | DDI0_TX0_DN | 32 | DDI0_BKLT_R_CTRL |
| 3 | GND | 18 | GND | 33 | DDI0_VDD_EN |
| 4 | MDSI_A_CLK_DN | 19 | DDI0_TX1_DP | 34 | DDI0_BKLT_EN |
| 5 | MDSI_A_CLK_DP | 20 | DDI0_TX1_DN | 35 | NC |
| 6 | GND | 21 | GND | 36 | NC |
| 7 | MDSI_A_DATA0_DN | 22 | DDI0_TX2_DP | 37 | +3.3V |
| 8 | MDSI_A_DATA0_DP | 23 | DDI0_TX2_DN | 38 | +3.3V |
| 9 | GND | 24 | GND | 39 | +3.3V |
| 10 | I2C2_3P3_SCL | 25 | DDI0_TX3_DP | 40 | +3.3V |

| 11 | I2C2_3P3_SDA | 26 | DDI0_TX3_DN | 41 | +3.3V |
|----|----------------|----|-------------|----|-------|
| 12 | GND | 27 | GND | 42 | GND |
| 13 | DDI1_DDC_C_CLK | 28 | DDI0_AUX_DP | 43 | GND |
| 14 | DDI1_DDC_C_DAT | 29 | DDI0_AUX_DN | | |
| 15 | GND | 30 | GND | | |

2.3.12 MIPI CSI Connector (CN32)



| | | - | |
|-----|-----------------|-----|---------------|
| Pin | Signal | Pin | Signal |
| 1 | GND | 13 | GND |
| 2 | MCSI_1_DATA1_DN | 14 | CAM_MCLK |
| 3 | MCSI_1_DATA1_DP | 15 | GND |
| 4 | GND | 16 | I2C2_SOC_SCL |
| 5 | MCSI_1_CLK_DN | 17 | I2C2_SOC_SDA |
| 6 | MCSI_1_CLK_DP | 18 | CAM_RST_N |
| 7 | GND | 19 | FLASH_RESET_N |
| 8 | MCSI_1_DATA0_DN | 20 | +2.8V |
| 9 | MCSI_1_DATA0_DP | 21 | GND_CAM |
| 10 | GND | 22 | GND |
| 11 | +1.2V | 23 | GND |
| 12 | +1.8V | | |

2.3.13 Power Button Wafer (CN33)



| Pin | Signal |
|-----|---------------|
| 1 | PWR_SW#_CTL_R |
| 2 | GND |

2.3.14 Update CPLD Header (CN34)



| Pin | Signal | Pin | Signal |
|-----|--------------|-----|----------|
| 1 | CHT_GPIO_TMS | 2 | CPLD_TMS |
| 3 | CHT_GPIO_TDI | 4 | CPLD_TDI |
| 5 | CHT_GPIO_TCK | 6 | CPLD_TCK |
| 7 | CHT_GPIO_TDO | 8 | CPLD_TDO |
| 9 | FAN_PWM | 10 | +1.8V |
| 11 | +5V | 12 | GND |

2.4 Mount the OMNI-3155-UP onto the wall

Step 1 - Glue the waterproof rubber along back side of the panel





Step 2 - Pull the Panel Mount clips out along the edges of front frame

Step 3 – use screws to secure the panel onto the wall with the wall-mount brackets



Chapter 2 – Hardware Information

2.5 P-CAP Touch Screen Operating



- 1. Always touch the screen with finger pads.
- 2. The force of finger should be lower than 10g.



Chapter 3

AMI BIOS Setup

OMNI-3155-UP

3.1 System Test and Initialization

The system uses certain routines to perform testing and initialization. If an error, fatal or non-fatal, is encountered, a few short beeps or an error message will be outputted. The board can usually continue the boot up sequence with non-fatal errors.

The system configuration verification routines check the current system configuration against the values stored in the CMOS memory. If they do not match, an error message will be outputted, in which case you will need to run the BIOS setup program to set the configuration information in memory.

There are three situations in which you will need to change the CMOS settings:

- You are starting your system for the first time
- You have changed your system's hardware
- The CMOS memory has lost power and the configuration information is erased

The system's CMOS memory uses a backup battery for data retention, which is to be replaced once emptied.

Industrial Modular Touch Panel PC

OMNI-3155-UF

3.2 AMI BIOS Setup

The AMI BIOS ROM has a pre-installed Setup program that allows users to modify basic system configurations, which is stored in the battery-backed CMOS RAM and BIOS NVRAM so that the information is retained when the power is turned off.

To enter BIOS Setup, press or <F2> immediately while your computer is powering up.

The function for each interface can be found below.

Main – Date and time can be set here. Press <Tab> to switch between date elements

Advanced - Enable/ Disable boot option for legacy network devices

Chipset - For hosting bridge parameters

Boot - Enable/ Disable quiet Boot Option

Security - The setup administrator password can be set here

Save & Exit – Save your changes and exit the program

3.3 Setup Submenu: Main

| Aptio Setup Utility – Copyright (C) 2018 American Megatrends, Inc. Main Advanced Chipset Security Boot Save & Exit | | | | |
|---|---|--|--|--|
| BIOS Information OMNI Series Project (UP) R1.0 ((| MUPAM10)(08/08/2018) | Set the Date. Use Tab to switch between Date elements. | | |
| BIOS Vendor Compliancy | American Megatrends UEFI 2.4; PI 1.3 | | | |
| System Date System Time | [Sun 01/01/2012] [00:12:10] | | | |
| Access Level | Administrator | | | |
| | | ++: Select Screen 11: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit | | |
| Version 2.18.1263. Co | pyright (C) 2018 American M | egatrends, Inc. | | |
3.4 Setup Submenu: Advanced

| Aptio Setup Utility – Copyright (C) 2018 American Main <mark>Advanced</mark> Chipset Security Boot Save & Exit | Megatrends, Inc. |
|--|----------------------------------|
| Main Advanced Chipset Security Boot Save & Exit CPU Configuration Trusted Computing USB Configuration Power Management | CPU Configuration Parameters |
| Vacion 0, 49, 4950, Convolute (P) 2049, American W | watpanda Tec |
| Version 2.10.1203. Copyright (C) 2010 Hillerical Ht | sgaurenus, Inc. |

3.4.1 Advanced: CPU Configuration

| CPU Configuration H Intel(R) Atom(TM) x5-28350 CPU @ 1.44GHz F CPU Signature 406c4 Microcode Patch 410 Max CPU Speed 1440 MHz Min CPU Speed 480 MHz Processor Cores 4 Intel HT Technology Not Supported Intel VT-x Technology Supported Intel Virtualization Technology [Enabled] FIST [Enabled] Turbo Mode [Enabled] | |
|---|--|
| Intel Virtualization Technology [Enabled] EIST [Enabled] – Turbo Mode [Enabled] – f Enabled] – f | When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology |
| F F F F | <pre>#: Select Screen f1: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre> |

| Intel Virtualization | Disabled | | |
|--------------------------------|----------------------------------|--------------------------------|--|
| Technology | Enabled | Optimal Default | |
| When enabled, a VM | M can utilize the additional har | dware capabilities provided by | |
| Vanderpool Technolo | Vanderpool Technology. | | |
| EIST | Disabled | | |
| | Enabled | Optimal Default | |
| Enable/Disable Intel SpeedStep | | | |
| Turbo Mode | Disabled | | |
| | Enabled | Optimal Default | |
| Turbo Mode | | | |

3.4.2 Advanced: Trusted Computing

| Aptio Setup Utility Advanced | – Copyright (C) 2018 Americ | can Megatrends, Inc. |
|--|---|--|
| Advanced TPM20 Device Found Security Device Support Active PCR banks Available PCR bank SHA-1 PCR Bank SHA256 PCR Bank Pending operation Platform Hierarchy Storage Hierarchy Endorsement Hierarchy TPM2.0 UEFI Spec Version Physical Presence Spec Version TPM 20 InterfaceType Device Select | [Enable] SHA-1 SHA-1,SHA256 [Enabled] [Disabled] [Enabled] [Enabled] [TCG_2] [1.2] [CRB] [Auto] | Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available. ++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit |
| | | |

| Security Device | Disabled | |
|------------------------|---|------------------------------|
| Support | Enabled | Optimal Default |
| Enable/Disable Secur | Enable/Disable Security Device. NOTE: Your Computer will reboot during restart in | |
| order to change State | e of the Device. | |
| SHA-1 PCR Bank | Disabled | |
| | Enabled | Optimal Default |
| Enable or Disable SH | A-1 PCR Bank | |
| SHA256 PCR Bank | Disabled | |
| | Enabled | Optimal Default |
| Enable or Disable SH | A256 PCR Bank | |
| Pending Operation | TPM Clear | |
| | None | Optimal Default |
| Schedule an Operation | on for the Security Device. NOT | E: Your Computer will reboot |
| during restart in orde | er to change State of Security D | evice. |
| Platform Hierarchy | Disabled | |
| | Enabled | Optimal Default |

| Enable or Disable P | latform Hierarchy | |
|------------------------|------------------------------|--|
| Storage Hierarchy | Disabled | |
| | Enabled | Optimal Default |
| Enable or Disable St | torage Hierarchy | |
| Endorsement | Disabled | |
| Hierarchy | Enabled | Optimal Default |
| Enable or Disable E | ndorsement Hierarchy | |
| TPM2.0 UEFI Spec | TCG_2 | |
| Version | TCG_1_2 | Optimal Default |
| Select the TCG2 Spe | ec Version Support, | |
| TCG_1_2: the Comp | atible mode for Win8/Win10 |), |
| TCG_2: Support nev | w TCG2 protocol and event f | ormat for Win10 or later |
| Physical presence | 1.3 | |
| Spec Version | 1.2 | Optimal Default |
| Select to Tell O.S. to | support PPI Spec Version 1.2 | 2 or 1.3. Note some HCK tests might not |
| support 1.3. | | |
| TPM 20 | TIS | |
| InterfaceType | CRB | Optimal Default |
| Select the Commun | nication Interface to TPM 20 | Device. |
| Device Select | Auto | Optimal Default |
| | TPM 1.2 | |
| | TPM 2.0 | |
| TPM 1.2 will restrict | support to TPM 1.2 devices, | TPM 2.0 will restrict support to TPM 2.0 |
| devices, Auto will su | upport both with the default | set to TPM 2.0 devices if not found, |
| TPM 1.2 devices will | be enumerated | |

3.4.3 Advanced: USB Configuration

| Advanced | Megatrends, Inc. |
|---|--|
| USB Configuration | |
| USB Devices: 1 Drive, 1 Keyboard, 1 Point, 1 Hub | ++: Select Screen 14: Select Item Enter: Select |
| | <pre>File. Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre> |

3.4.4 Advanced: Power Management

| Aptio Setu Advanced | up Utility – Copyright (C) 2018 Ame | erican Megatrends, Inc. |
|------------------------|-------------------------------------|---|
| Power Management | | |
| Power Mode | [AT Type] | |
| | | |
| | | |
| | | |
| | | |
| | | ++: Select Screen |
| | | †∔: Select Item Enter: Select |
| | | +/−: Change Opt. F1: General Help |
| | | F2: Previous Values F3: Optimized Defaults |
| | | F4: Save & Exit ESC: Exit |
| | | |
| | | |
| Version 2 | 2.18.1263. Copyright (C) 2018 Ameri | ican Megatrends, Inc. |

3.5 Setup submenu: Chipset

| Aptio Setup Utility – Copyright (C) 2018 American Main Advanced <mark>Chipset</mark> Security Boot Save & Exit | Megatrends, Inc. |
|---|---|
| ▶ North Bridge ▶ South Bridge | <pre>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>></pre> |
| Version 2.18.1263. Copyright (C) 2018 American Me | egatrends, Inc. |

3.5.1 Chipset: North Bridge



| IGD Turbo Enable | Auto | |
|----------------------|----------|-----------------|
| | Disabled | |
| | Enabled | Optimal Default |
| Enable or Disable IC | GD Turbo | |
| DVMT | 32M | |
| Pre-Allocated | 64M | |
| | 96M | |
| | 128M | |
| | 160M | |
| | 192M | |
| | 224M | |
| | 256M | |
| | 288M | |
| | 320M | |
| | 352M |] |
| | 384M | |

| | 416M | |
|---------------------|------------------------------|-----------------------------------|
| | 448M | |
| | 480M | |
| | 512M | Optimal Default |
| Select DVMT5.0 Pre | -Allocated(Fixed) Graphics M | emory size used by the Internal |
| Graphics Device | | |
| DVMT Total Gfx | 128M | |
| Mem | 256M | Optimal Default |
| | Max | |
| Select DVMT5.0 Tota | al Graphics Memory size use | d by the Internal Graphics Device |
| Aperture Size | 128M | |
| | 256M | Optimal Default |
| | 512M | |
| Select the Aperture | Size | |

3.5.1.1 North Bridge: LVDS Panel Configuration



| Backlight Level | 10% | |
|---------------------|--------------|-----------------|
| | 20% | |
| | 30% | |
| | 40% | |
| | 50% | |
| | 60% | |
| | 70% | |
| | 80% | Optimal Default |
| | 90% | |
| | 100% | |
| Select backlight co | ontrol level | |

3.5.2 Chipset: South Bridge

| Aptio Setup Utility - (Chipset | Copyright (C) 2018 American | Megatrends, Inc. |
|--|---------------------------------------|---|
| South Bridge eMMC: Hynix HCG4a2 8C76054 (58.2 GB |) | SCC eMMC Support Enable\Disable |
| SCC eMMC Support Onboard PCIe LAN UART Interface Selection | [ACPI mode] [Enabled] [Enabled] | |
| USB OTG Support | [PCI mode] | |
| | | |
| | | <pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre> |
| Version 2.18.1263. Co | pyright (C) 2018 American M | legatrends, Inc. |

| SCC eMMC | ACPI mode | Optimal Default |
|--------------------------------|--------------------------------|-----------------|
| Support | PCI mode | |
| | Disabled | |
| Enable/Disable SCC | eMMC Support | |
| Onboard PCIe LAN | Enabled | Optimal Default |
| | Disabled | |
| Enable or Disable th | e PCI Express Port 1 in the Ch | nipset |
| MUART Interface | Disabled | |
| Selection | Enabled | Optimal Default |
| Select which UART in | nterface to use | |
| USB OTG Support | Auto | |
| | ACPI mode | |
| | PCI mode | Optimal Default |
| | Disabled | |
| Enable/Disable USB OTG Support | | |

3.6 Setup submenu: Security

| Aptio Setu Main Advanced Chipset | o Utility – Copyright (C) 2018 American Security Boot Save & Exit | Megatrends, Inc. |
|---|--|--|
| Password Description | | Set Administrator Password |
| The password length must in the following range: Minimum length Maximum length | be 3 20 | |
| Administrator Password User Password | | |
| | | ++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit |
| Version 2 | .18.1263. Copyright (C) 2018 American M | egatrends, Inc. |

Change User/Administrator Password

You can set a User Password once an Administrator Password. The password will be required during boot up, or when the user enters the Setup utility. Please Note that a User Password does not provide access to many of the features in the Setup utility. Select the password you wish to set, press Enter to open a dialog box to enter your password (you can enter no more than six letters or numbers). Press Enter to confirm your entry, after which you will be prompted to retype your password for a final confirmation. Press Enter again after you have retyped it correctly.

Removing the Password

Highlight this item and type in the current password. At the next dialog box press Enter to disable password protection.

OMNI-3155-UP

3.7 Setup submenu: Boot

| Aptio Setup Utility Main Advanced Chipset Securit | – Copyright (C) 2018 Americar y <mark>Boot</mark> Save & Exit | Megatrends, Inc. |
|--|--|---|
| Boot Configuration Quiet Boot Network Stack | [Enabled] [Disabled] | Enables or disables Quiet Boot option |
| Boot Option Priorities Boot Option #1 Boot Option #2 | (Windows Boot Manager) (UEFI: JetFlashTrans) | |
| | | Hu Salast Career |
| | | 11: Select Item Enter: Select +/-: Change Opt. F1: General Help |
| | | F2: Previous values F3: Optimized Defaults F4: Save & Exit ESC: Exit |
| Version 2 18 1253 | Conumight (C) 2018 American W | Agration de Tor |

| Quiet Boot | Disabled | | |
|-----------------------------------|-----------------|-----------------|--|
| | Enabled | Optimal Default | |
| Enable or Disable Qui | iet Boot option | | |
| Network Stack | Disabled | Optimal Default | |
| | Enabled | | |
| Enable/Disable UEFI Network Stack | | | |

3.8 Setup submenu: Save & Exit

| Aptio Setup Utility – Copyright (C) 2018 American Main Advanced Chipset Security Boot <mark>Save & Exit</mark> | Megatrends, Inc. |
|---|---|
| Save Changes and Reset Discard Changes and Reset Restore Defaults | Reset the system after saving the changes. |
| | <pre>++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit</pre> |
| Version 2.18.1263. Copyright (C) 2018 American Me | egatrends, Inc. |

Chapter 4

Drivers Installation & Touchscreen Settings

DMNI-3155-UP

4.1 Product CD/DVD

The OMNI-3155-UP comes with a product DVD that contains all the drivers and utilities you need to setup your product. Insert the DVD and follow the steps in the autorun program to install the drivers.

In case the program does not start, follow the sequence below to install the drivers.

Step 1 – Install DriverPackage

- 1. Open the Step1–DriverPackage folder
- 2. Open the **Setup.exe** file
- 3. Follow the installation instructions
- 4. Drivers should be installed automatically
- 5. If the drivers don't install, copy the entire **Step1–DriverPackage** folder to

your computer's C: drive, open the **Setup.exe** file and follow the instructions. The drivers should then install.

Step 2 – Install TrustExecutionEngine Driver

- 1. Open the Step2-TrustExecutionEngine folder
- 2. Open the SetupTXE.exe file in the folder
- 3. Follow the instructions
- 4. Drivers will be installed automatically

Step 3 – Install USBHub Driver

- 1. Open the Step3–USBHub folder
- 2. Open the .exe file in the folder
- 3. Follow the instructions
- 4. Drivers will be installed automatically

Step 4 – Install LAN Drivers

- 1. Open the Step4-LAN folder and select your OS
- 2. Open the setup.exe file in the folder
- 3. Follow the instructions
- 4. Drivers will be installed automatically

Step 5 – Install PenMount Touch 6000 Series Driver (Resistive touchscreen only)

- 1. Open the Step5–PenMount Touch 6000 folder
- 2. Open the **Setup.exe** file
- 3. Follow the instructions
- 4. Drivers will be installed automatically
- * Projected Capacitive Touch does not require a driver to be installed.

4.2 PCAP Dual Monitor Touch Settings

When two panels are used, they can set to be the primary and secondary display. The instruction below shows how this can be done:

1. Go to Display Panel and choose your preferred primary display.

| ų. | Screen Resolution | | × |
|--------------------|-------------------------------|----------------------|---|
| € 🤿 🕤 🛉 🖉 | ≪ Displ → Screen Resoluti v 🖒 | Search Control Panel | Q |
| Change the ap | pearance of your displays | | ^ |
| | | Detect | |
| | | Identify | |
| | | | |
| Display: | 1. Mobile PC Display 🗸 | | |
| Resolution: | 1366 × 768 (Recommended) v | | |
| Orientation: | Landscape 🗸 | | |
| Multiple displays: | Extend these displays | | |
| | | | ~ |

2. Go to Tablet PC Settings in Control Panel. Under Display options, select the

primary display from step 1. Apply the changes and exit.

| | | _ _ × |
|--|---|---|
| 😌 🕤 - 🔜 🗩 | Control Panel > | • 9 ta x |
| Taskba Auto-hid Group sin Lock or u Turn tool What hap | r and Start Menu ethe taskbar ethe taskbar nlock the taskbar nlock the taskbar bars on the taskbar pared to the Quick Launch toolbar? | Tablet PC Settings |
| Tablet I Set tablet | PC Settings buttons to perform certain tasks | Display options |
| Perform a | eshooting ecommended maintenance tasks automatically nd fix audio recording problems | Display: 2. Dell S122201(Analog) Details: Touch Input Available |
| Change P | t Options row web pages are displayed in tabs | Calibrate |
| System View runn Device How to c | ning processes with Task Manager Manager hange the size of virtual memory | Choose the order in which your screen rotates. Go to Orientation |
| Notific: Customic Show or I Show or I Show or I Show or I Remove i | ation Area Icons e icons on the taskbar iide inactive icons on the taskbar iide the notification area on the taskbar iide volume (speaker) icon on the taskbar iide dock icon on the taskbar cons from notification area (system tray) on the desktop | OK Cancel Apply |

*Do NOT calibrate the screen on your own. Doing so might disrupt the device's factory

calibration

Chapter 4 – Driver Installation & Touchscreen Settings

Appendix A

I/O Information

DMNI-3155-UP

A.1 I/O Address Map

Note: There is no PS/2 interface on the OMNI-BT series, hence the exclamation marks

 Input/output (IO) [00000000000000000 - 0000000000006F] PCI Express Root Complex [000000000000000020 - 000000000000021] Programmable interrupt controller to otroller [00000000000024 - 0000000000000025] Programmable interrupt controller to otroller [00000000000028 - 0000000000000029] Programmable interrupt controller [000000000000002C - 0000000000002D] Programmable interrupt controller [00000000000000030 - 000000000000031] Programmable interrupt controller to nterrupt controller [00000000000000035] Programmable interrupt controller to otroller [000000000000038 - 00000000000000039] Programmable interrupt controller [000000000000003C - 0000000000003D] Programmable interrupt controller timer [0000000000000040 - 0000000000000043] System timer [000000000000004E - 0000000000004F] Motherboard resources timer [0000000000000050 - 0000000000000053] System timer [0000000000000061 - 000000000000061] Motherboard resources [0000000000000063 - 00000000000063] Motherboard resources [0000000000000065 - 00000000000065] Motherboard resources [000000000000067 - 00000000000067] Motherboard resources [00000000000000070 - 0000000000000070] Motherboard resources [0000000000000070 - 000000000000077] System CMOS/real time clock [0000000000000078 - 000000000000CF7] PCI Express Root Complex [0000000000000080 - 000000000008F] Motherboard resources [0000000000000092 - 000000000000092] Motherboard resources [000000000000080 - 00000000000081] Programmable interrupt controller [0000000000000082 - 0000000000083] Motherboard resources [0000000000000084 - 00000000000085] Programmable interrupt controller [0000000000000088 - 00000000000089] Programmable interrupt controller [0000000000000BC - 000000000000BD] Programmable interrupt controller [0000000000003F8 - 000000000003FF] Communications Port (COM1) [00000000000000400 - 00000000000047F] Motherboard resources [00000000000004D0 - 000000000004D1] Programmable interrupt controller

| | [000000000000030 - 00000000000031] Programmable interrupt controller |
|-----|--|
| | [00000000000034 - 00000000000035] Programmable interrupt controller |
| | [00000000000038 - 00000000000039] Programmable interrupt controller |
| | [0000000000003C - 0000000000003D] Programmable interrupt controller |
| | [000000000000040 - 00000000000043] System timer |
| | [0000000000004E - 0000000000004F] Motherboard resources |
| | [000000000000050 - 000000000000053] System timer |
| | [000000000000061 - 000000000000061] Motherboard resources |
| | [000000000000063 - 00000000000063] Motherboard resources |
| | [000000000000065 - 00000000000065] Motherboard resources |
| | [000000000000067 - 00000000000067] Motherboard resources |
| | [000000000000070 - 0000000000000070] Motherboard resources |
| | [000000000000070 - 00000000000077] System CMOS/real time clock |
| | [000000000000078 - 00000000000CF7] PCI Express Root Complex |
| | [000000000000080 - 0000000000008F] Motherboard resources |
| | [00000000000092 - 00000000000092] Motherboard resources |
| | [0000000000000A0 - 000000000000A1] Programmable interrupt controller |
| | [000000000000A4 - 00000000000A5] Programmable interrupt controller |
| | [000000000000A8 - 00000000000A9] Programmable interrupt controller |
| | [000000000000AC - 00000000000AD] Programmable interrupt controller |
| | [0000000000000B0 - 000000000000B1] Programmable interrupt controller |
| | [00000000000082 - 00000000000083] Motherboard resources |
| | [00000000000084 - 00000000000085] Programmable interrupt controller |
| | [00000000000088 - 00000000000089] Programmable interrupt controller |
| | [00000000000BC - 00000000000BD] Programmable interrupt controller |
| ļ | [000000000003F8 - 00000000003FF] Communications Port (COM1) |
| | [000000000000400 - 0000000000047F] Motherboard resources |
| | [0000000000004D0 - 000000000004D1] Programmable interrupt controller |
| | [000000000000500 - 000000000005FE] Motherboard resources |
| | [00000000000680 - 0000000000069F] Motherboard resources |
| | [000000000000000 - 00000000000FFFF] PCI Express Root Complex |
| Ţ | [0000000000000000 - 0000000000000000000 |
| | [0000000000000000 - 00000000000EFFF] PCI Express Root Port |
| Int | [000000000000F000 - 00000000000F03F] Intel(R) HD Graphics |

A.2 Memory Address Map

| m | Memory |
|---|--|
| - | To 1000000000000000000000000000000000000 |
| | 10000000000000000000000000000000000000 |
| | 10000000000000000000000000000000000000 |
| | [0000000020000000 - 00000000201EEEEE] Intel SST Audio Device (WDM) |
| | Topp00000020000000 - 0000000201FFFFF1 PCI Express Root Complex |
| | To 100000005ED00001 - 00000007ED000001 PCI Express Root Complex |
| | 1000000007EE00000 - 00000007EE00EEE1 Trusted Platform Module 2.0 |
| | [] [000000080000000 - 00000009EEEEEE] Intel(R) HD Graphics |
| | To 1000000080000000 - 00000000000000000000 |
| | [] [00000000A0000000 - 0000000A0FFFFF1 Intel(R) HD Graphics |
| | L [00000000A1000000 - 00000000A11FFFFF] USB Synopsys Controller |
| | I00000000A1200000 - 00000000A13FFFFF] Intel SST Audio Device (WDM) |
| | 00000000A1400000 - 00000000A14FFFFF] Intel(R) Trusted Execution Engine Interface |
| | 100000000A1500000 - 00000000A15FFFFF] Intel(R) Trusted Execution Engine Interface |
| | [00000000A1600000 - 00000000A1603FFF] Realtek PCIe GBE Family Controller |
| | E [00000000A1600000 - 00000000A16FFFF] PCI Express Root Port |
| | [00000000A1604000 - 00000000A1604FFF] Realtek PCIe GBE Family Controller |
| | [00000000A1700000 - 00000000A170FFF] Intel(R) USB 3.0 eXtensible Host Controller - 1.0 (Microsoft) |
| | |
| | The [00000000A1714000 - 00000000A1714FFF] Motherboard resources |
| | time [00000000A1715000 - 00000000A1715FFF] Intel(R) Serial IO I2C ES Controller |
| | to 00000000A1716000 - 00000000A1716FFF] Motherboard resources |
| | to [00000000A1717000 - 00000000A1717FFF] Intel(R) Serial IO I2C ES Controller |
| | to [00000000A1718000 - 00000000A1718FFF] Motherboard resources |
| | Tem [00000000A1719000 - 00000000A1719FFF] Intel(R) Serial IO I2C ES Controller |
| | to 1000000000000000000000000000000000000 |
| | to [00000000A171B000 - 00000000A171BFFF] Intel(R) Serial IO I2C ES Controller |
| | to 1000000000000000000000000000000000000 |
| | I [00000000A171D000 - 00000000A171DFFF] Intel SST Audio Device (WDM) |
| | to [00000000A171E000 - 00000000A171EFFF] Motherboard resources |
| | [00000000A171F000 - 00000000A171FFFF] Intel SD Host Controller |
| | to the sources [000000000000 - 0000000000000000000000 |
| | te [0000000E00000D0 - 0000000E00000DF] Intel(R) Sideband Fabric Device |

| | - | [00000000A1600000 - 00000000A1603FFF] F | Realtek PCIe GBE Family Controller |
|---|---|---|---|
| | | 00000000A1600000 - 00000000A16FFFFF] F | PCI Express Root Port |
| | P | 00000000A1604000 - 00000000A1604FFF] F | Realtek PCIe GBE Family Controller |
| | Ŷ | [00000000A1700000 - 00000000A170FFFF] | Intel(R) USB 3.0 eXtensible Host Controller - 1.0 (Microsoft) |
| ļ | Ż | [00000000A1710000 - 00000000A1713FFF] | Intel(R) Serial IO DMA Controller |
| | | [00000000A1714000 - 00000000A1714FFF] N | Motherboard resources |
| | | 00000000A1715000 - 00000000A1715FFF] I | Intel(R) Serial IO I2C ES Controller |
| | | 00000000A1716000 - 00000000A1716FFF] N | Motherboard resources |
| | | 00000000A1717000 - 00000000A1717FFF] I | Intel(R) Serial IO I2C ES Controller |
| | | 00000000A1718000 - 00000000A1718FFF] N | Motherboard resources |
| | | 00000000A1719000 - 00000000A1719FFF] I | Intel(R) Serial IO I2C ES Controller |
| | | 00000000A171A000 - 00000000A171AFFF] | Motherboard resources |
| | | 00000000A171B000 - 00000000A171BFFF] | Intel(R) Serial IO I2C ES Controller |
| | | 00000000A171C000 - 00000000A171CFFF] | Motherboard resources |
| | 1 | [00000000A171D000 - 00000000A171DFFF] | Intel SST Audio Device (WDM) |
| | | 00000000A171E000 - 00000000A171EFFF] N | Motherboard resources |
| | | [00000000A171F000 - 00000000A171FFFF] | Intel SD Host Controller |
| | | 000000000000000 - 00000000EFFFFFF] N | Notherboard resources |
| | | [0000000E0000D0 - 0000000E00000DF] | Intel(R) Sideband Fabric Device |
| | | 00000000FEA00000 - 00000000FEAFFFF] N | Motherboard resources |
| | | 00000000FED00000 - 00000000FED003FF] H | High precision event timer |
| | | 00000000FED01000 - 00000000FED01FFF] N | Motherboard resources |
| | | 00000000FED03000 - 00000000FED03FFF] N | Motherboard resources |
| | | 00000000FED06000 - 00000000FED06FFF] N | Motherboard resources |
| | | 00000000FED08000 - 00000000FED09FFF] N | Motherboard resources |
| | | [0000000FED1C000 - 0000000FED1CFFF] | Motherboard resources |
| | | [00000000FED80000 - 00000000FED87FFF] | Intel Serial IO GPIO Controller |
| | | [00000000FED80000 - 00000000FEDBFFFF] 1 | Motherboard resources |
| | | [00000000FED88000 - 00000000FED8FFFF] | Intel Serial IO GPIO Controller |
| | | [00000000FED90000 - 00000000FED97FFF] | Intel Serial IO GPIO Controller |
| | | [00000000FED98000 - 00000000FED9FFF] | Intel Serial IO GPIO Controller |
| | | [0000000FEDA0000 - 0000000FEDA7FF] | Intel Serial IO GPIO Controller |
| | | [00000000FEE00000 - 00000000FEEFFFF] N | Aotherboard resources |
| | | 00000000FF000000 - 00000000FFFFFFF] Lo | egacy device |

A.3 IRQ Mapping Chart

Interrupt request (IRQ) (ISA) 0x00000000 (00) System timer Communications Port (COM1) (ISA) 0x00000004 (04) ISA) 0x00000008 (08) High precision event timer Intel SST Audio Device (WDM) 1 (ISA) 0x00000018 (24) 1 (ISA) 0x00000019 (25) Intel SST Audio Device (WDM) 1 (ISA) 0x0000001A (26) Intel SST Audio Device (WDM) 1 (ISA) 0x0000001B (27) Intel SST Audio Device (WDM) 1 (ISA) 0x0000001C (28) Intel SST Audio Device (WDM) (ISA) 0x0000001D (29) Intel SST Audio Device (WDM) 1 (ISA) 0x00000022 (34) Intel(R) Serial IO I2C ES Controller Tal: (ISA) 0x00000024 (36) Intel(R) Serial IO I2C ES Controller ISA) 0x00000025 (37) Intel(R) Serial IO I2C ES Controller ISA) 0x00000026 (38) Intel(R) Serial IO I2C ES Controller (ISA) 0x0000002B (43) Intel(R) Serial IO DMA Controller (ISA) 0x0000002D (45) Intel SD Host Controller Intel Serial IO GPIO Controller ISA) 0x00000030 (48) ISA) 0x00000031 (49) Intel Serial IO GPIO Controller (ISA) 0x00000032 (50) Intel Serial IO GPIO Controller (ISA) 0x00000036 (54) Microsoft ACPI-Compliant System ISA) 0x00000037 (55) Microsoft ACPI-Compliant System ISA) 0x00000038 (56) Microsoft ACPI-Compliant System ISA) 0x00000039 (57) Microsoft ACPI-Compliant System ISA) 0x0000003A (58) Microsoft ACPI-Compliant System (ISA) 0x0000003B (59) Microsoft ACPI-Compliant System ISA) 0x000003C (60) Microsoft ACPI-Compliant System Tall (ISA) 0x0000003D (61) Microsoft ACPI-Compliant System Microsoft ACPI-Compliant System (ISA) 0x000003E (62) (ISA) 0x000003F (63) Microsoft ACPI-Compliant System ISA) 0x00000040 (64) Microsoft ACPI-Compliant System Tal: (ISA) 0x00000041 (65) Microsoft ACPI-Compliant System ISA) 0x00000042 (66) Microsoft ACPI-Compliant System Tal (ISA) 0x00000043 (67) Microsoft ACPI-Compliant System ISA) 0x00000044 (68) Microsoft ACPI-Compliant System

ISA) 0x00000045 (69) (ISA) 0x00000046 (70) ISA) 0x00000047 (71) ISA) 0x00000048 (72) (ISA) 0x00000049 (73) ISA) 0x0000004A (74) (ISA) 0x0000004B (75) ISA) 0x0000004C (76) (ISA) 0x0000004D (77) ISA) 0x0000004E (78) (ISA) 0x0000004F (79) (ISA) 0x00000050 (80) ISA) 0x00000051 (81) ISA) 0x00000052 (82) ISA) 0x00000053 (83) ISA) 0x00000054 (84) (ISA) 0x00000055 (85) ISA) 0x00000056 (86) (ISA) 0x00000057 (87) ISA) 0x00000058 (88) Test (ISA) 0x00000059 (89) (ISA) 0x0000005A (90) [ISA] 0x0000005B (91) (ISA) 0x0000005B (91) ISA) 0x0000005C (92) (ISA) 0x0000005D (93) (ISA) 0x0000005E (94) (ISA) 0x0000005F (95) Text (ISA) 0x0000060 (96) (ISA) 0x00000061 (97) ISA) 0x00000062 (98) ISA) 0x00000063 (99) ISA) 0x0000064 (100) (ISA) 0x00000065 (101)

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