# GENESYSM-ADN6

GENESYSM Compact Embedded System

User's Manual 1st Ed

*Last Updated: August 1, 2024*

## Copyright Notice

## Acknowledgements

All other products' name or trademarks are properties of their respective owners.

- Microsoft Windows is a registered trademark of Microsoft Corp.
- Intel Atom® is a registered trademark of Intel Corporation
- Intel® Core™ is a trademark of Intel Corporation
- Realtek is a trademark of Realtek Semiconductor Corporation.
- Ubuntu is a trademark of Canonical.

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## Packing List

Before setting up your product, please make sure the following items have been shipped:

| Item | Quantity |
| --- | --- |
| • GENESYSM-ADN6 | 1 |

If any of these items are missing or damaged, please contact your distributor or sales representative immediately.

## About this Document

This User's Manual contains all the essential information, such as detailed descriptions and explanations on the product's hardware and software features (if any), its specifications, dimensions, jumper/connector settings/definitions, and driver installation instructions (if any), to facilitate users in setting up their product.

Users may refer to the product page at AAEON.com for the latest version of this document.

## Safety Precautions

Please read the following safety instructions carefully. It is advised that you keep this manual for future references

1. All cautions and warnings on the device should be noted.
2. Make sure the power source matches the power rating of the device.
3. Position the power cord so that people cannot step on it. Do not place anything over the power cord.
4. Always completely disconnect the power before working on the system's hardware.
5. No connections should be made when the system is powered as a sudden rush of power may damage sensitive electronic components.
6. If the device is not to be used for a long time, disconnect it from the power supply to avoid damage by transient over-voltage.
7. Always disconnect this device from any AC supply before cleaning.
8. While cleaning, use a damp cloth instead of liquid or spray detergents.
9. Make sure the device is installed near a power outlet and is easily accessible.
10. Keep this device away from humidity.
11. Place the device on a solid surface during installation to prevent falls
12. Do not cover the openings on the device to ensure optimal heat dissipation.
13. Watch out for high temperatures when the system is running.
14. Do not touch the heat sink or heat spreader when the system is running
15. Never pour any liquid into the openings. This could cause fire or electric shock.
16. As most electronic components are sensitive to static electrical charge, be sure to ground yourself to prevent static charge when installing the internal components. Use a grounding wrist strap and contain all electronic components in any static-shielded containers.

17. If any of the following situations arises, please the contact our service personnel:
    i. Damaged power cord or plug
    ii. Liquid intrusion to the device
    iii. Exposure to moisture
    iv. Device is not working as expected or in a manner as described in this manual
    v. The device is dropped or damaged
    vi. Any obvious signs of damage displayed on the device
18. DO NOT LEAVE THIS DEVICE IN AN UNCONTROLLED ENVIRONMENT WITH TEMPERATURES BEYOND THE DEVICE'S PERMITTED STORAGE TEMPERATURES (SEE CHAPTER 1) TO PREVENT DAMAGE.

## FCC Statement


This device complies with Part 15 FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received including interference that may cause undesired operation.

*Caution:*

*There is a danger of explosion if the battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions and your local government's recycling or disposal directives.*

*Attention:*

*Il y a un risque d'explosion si la batterie est remplacée de façon incorrecte.*
*Ne la remplacer qu'avec le même modèle ou équivalent recommandé par le constructeur.*
*Recycler les batteries usées en accord avec les instructions du fabricant et les directives gouvernementales de recyclage.*

## China RoHS Requirements (CN)

### 产品中有毒有害物质或元素名称及含量

AAEON System QO4-381 Rev.A0

| 部件名称 | 有毒有害物质或元素 | | | | | |
|---|---|---|---|---|---|---|
| | 铅 (Pb) | 汞 (Hg) | 镉 (Cd) | 六价铬 (Cr(VI)) | 多溴联苯 (PBB) | 多溴二苯醚(PBDE) |
| **印刷电路板及其电子组件** | × | ○ | ○ | ○ | ○ | ○ |
| **外部信号连接器及线材** | × | ○ | ○ | ○ | ○ | ○ |
| **外壳** | ○ | ○ | ○ | ○ | ○ | ○ |
| **中央处理器与内存** | × | ○ | ○ | ○ | ○ | ○ |
| **硬盘** | × | ○ | ○ | ○ | ○ | ○ |
| **液晶模块** | × | ○ | ○ | ○ | ○ | ○ |
| **光驱** | × | ○ | ○ | ○ | ○ | ○ |
| **触控模块** | × | ○ | ○ | ○ | ○ | ○ |
| **电源** | × | ○ | ○ | ○ | ○ | ○ |
| **电池** | × | ○ | ○ | ○ | ○ | ○ |

本表格依据 SJ/T 11364 的规定编制。

○：表示该有毒有害物质在该部件所有均质材料中的含量均在 GB/T 26572 标准规定的限量要求以下。

×：表示该有害物质的某一均质材料超出了 GB/T 26572 的限量要求，然而该部件仍符合欧盟指令 2011/65/EU 的规范。

备注：

一、此产品所标示之环保使用期限，系指在一般正常使用状况下。

二、上述部件物质中央处理器、内存、硬盘、光驱、电源为选购品。

三、上述部件物质液晶模块、触控模块仅一体机产品适用。

## China RoHS Requirement (EN)

# Hazardous and Toxic Materials List

AAEON System QO4-381 Rev.A0

| Component Name | Hazardous or Toxic Materials or Elements | | | | | |
|---|---|---|---|---|---|---|
| | Lead (Pb) | Mercury (Hg) | Cadmium (Cd) | Hexavalent Chromium (Cr(VI)) | Polybrominated biphenyls (PBBs) | Polybrominated diphenyl ethers (PBDEs) |
| PCB and Components | X | O | O | O | O | O |
| Wires & Connectors for Ext.Connections | X | O | O | O | O | O |
| Chassis | O | O | O | O | O | O |
| CPU & RAM | X | O | O | O | O | O |
| HDD Drive | X | O | O | O | O | O |
| LCD Module | X | O | O | O | O | O |
| Optical Drive | X | O | O | O | O | O |
| Touch Control Module | X | O | O | O | O | O |
| PSU | X | O | O | O | O | O |
| Battery | X | O | O | O | O | O |

This form is prepared in compliance with the provisions of SJ/T 11364.

O: The level of toxic or hazardous materials present in this component and its parts is below the limit specified by GB/T 26572.

X: The level of toxic of hazardous materials present in the component exceed the limits specified by GB/T 26572, but is still in compliance with EU Directive 2011/65/EU (RoHS 2).

Notes:

1. The Environment Friendly Use Period indicated by labelling on this product is applicable only to use under normal conditions.
2. Individual components including the CPU, RAM/memory, HDD, optical drive, and PSU are optional.
3. LCD Module and Touch Control Module only applies to certain products which feature these components.

# Table of Contents

# Chapter 1

Product Specifications

## 1.1 Specifications

| System | |
|---|---|
| Form Factor | 3.5" SubCompact System |
| CPU | Intel® Processor N Series, Intel® Core™ i3-N305<br>Processors:<br>Intel® Core™ i3-N305 (8C/8T, up to 3.8 GHz, 15W)<br>Intel® Processor N97 (4C/4T, up to 3.6 GHz, 12W) |
| Chipset | Integrated with Intel® SoC |
| Memory Type | DDR5 4800, Single-Channel SODIMM x 1, Max. 16GB,<br>Non-ECC |
| BIOS | UEFI |
| Wake on LAN | Yes |
| Watchdog Timer | 255 Levels |
| Security | TPM 2.0 |
| RTC Battery | Lithium Battery 3V/240mAh |
| Dimension | 7.00" x 5.28" x 1.69" (178mm x 134mm x 43mm) |
| OS Support | Windows 10 (64-bit)<br>Ubuntu 22.04/Kernel 5.15.0-25-generic |

| Power | |
|---|---|
| Power Requirement | +9V ~ 36V |
| Power Supply Type | ATX |
| Connector | DC Jack Connector |
| Power Consumption | Intel® Core™ i3-N305, DDR5 16GB x 1, 3.80A @+12V<br>(Typical)<br>Intel® Core™ i3-N305, DDR5 16GB x 1, 5.48A @+12V<br>(Max) |

## Display

| | |
|---|---|
| Controller | Intel® UHD Graphics |
| LVDS/eDP | — |
| Display Interface | HDMI 1.4 x 1, up to 3840 x 2160 @30Hz |
| | VGA x 1, up to 1920 x 1080 |
| Multiple Display | Up to 2 Simultaneous Displays |

## Audio

| | |
|---|---|
| Codec | Realtek ALC897 |
| Audio Interface | Line-In/Line-Out/Mic |
| Speaker | — |

## External I/O

| | |
|---|---|
| Ethernet | Intel® Ethernet Controller I226 2.5GbE, RJ-45 x 3 |
| USB | USB 3.2 Gen 2 (Type-A) x 2 |
| | USB 2.0 x 4 |
| Serial Port | COM 1, COM 2 (RS-232/422/485, supports 5V/12V/RI) |
| | COM 3, COM 4 (RS-232 x 2, optional) |
| Video | HDMI 1.4 x 1 |
| | VGA x 1 |

## Internal I/O

| | |
|---|---|
| USB | — |
| Serial Port | — |
| Video | — |
| SATA | SATA 6Gb/s x 1 |
| | +5V SATA Power Connector x 1 |
| Audio | Audio Header x 1 |

## Internal I/O

| | |
|---|---|
| DIO/GPIO | 8-bit GPIO |
| SMBus/I2C | SMBus/I2C x 1 (Default: SMBus) |
| Touch | — |
| FAN | — |
| SIM | Nano SIM x 1 |
| Front Panel | HDD LED, PWR LED, Power Button, Buzzer, Reset |
| Others | — |

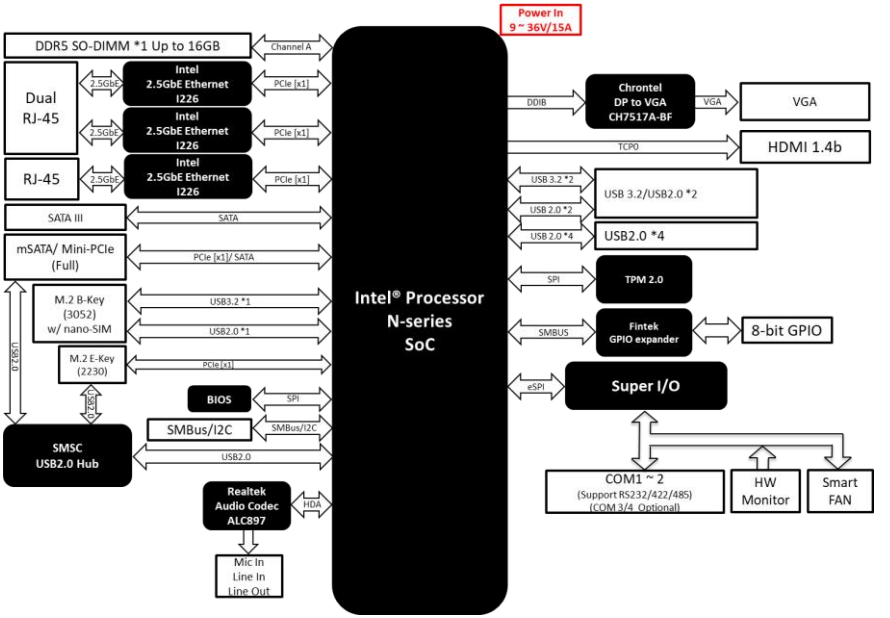
## Expansion

| | |
|---|---|
| Mini PCIe/mSATA | Full-size mSATA/mPCIe x 1 (Default: mSATA, select by BIOS) |
| M.2 | M.2 2230 E-Key x 1 (PCIe 3.0 [x1] + USB 2.0) |
| | M.2 3052 B-Key x 1 (USB 3.2 Gen 2 + USB 2.0) |

## Environmental

| | |
|---|---|
| Operating Temperature | 14°F ~ 140°F (-10°C ~ 60°C) 0.5m/s air flow |
| Storage Temperature | -40°F ~ 185°F (-40°C ~ 85°C) |
| Operating Humidity | 0% ~ 90% relative humidity, non-condensing |
| MTBF (Hours) | 337,963 |
| EMC | CE/FCC Class A |

## 1.2 Block Diagram

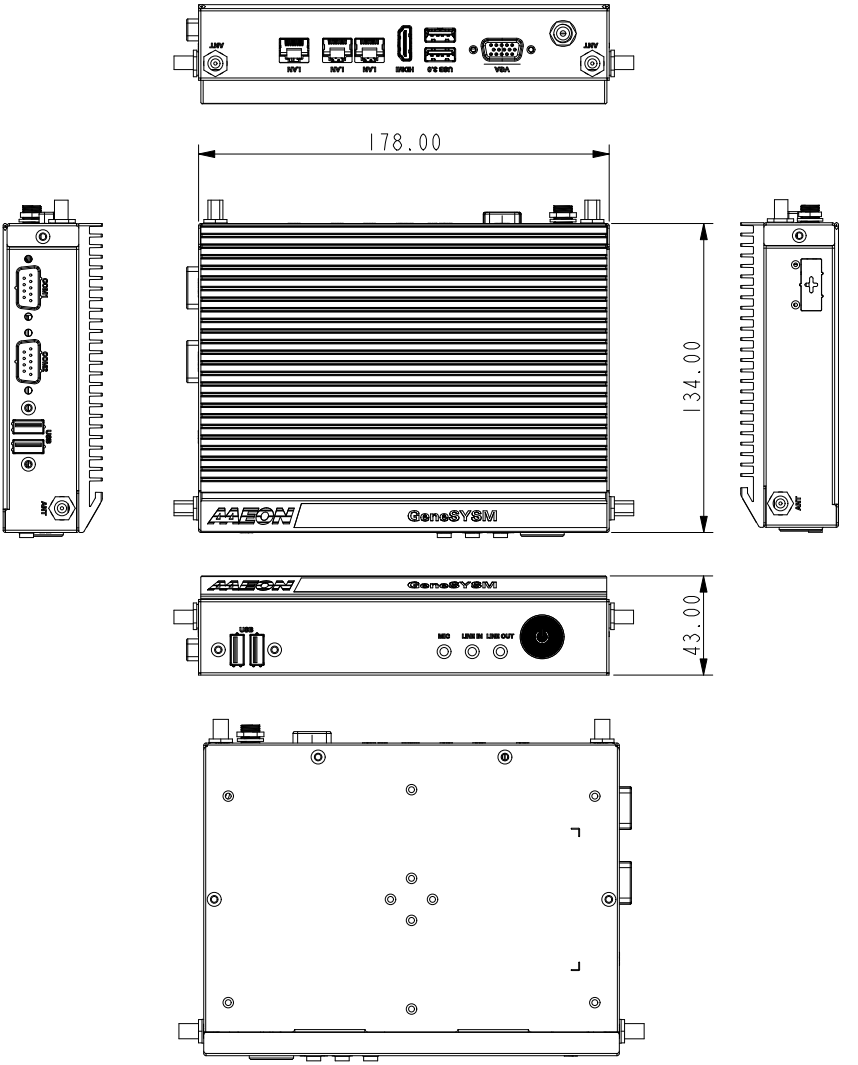


# Chapter 2

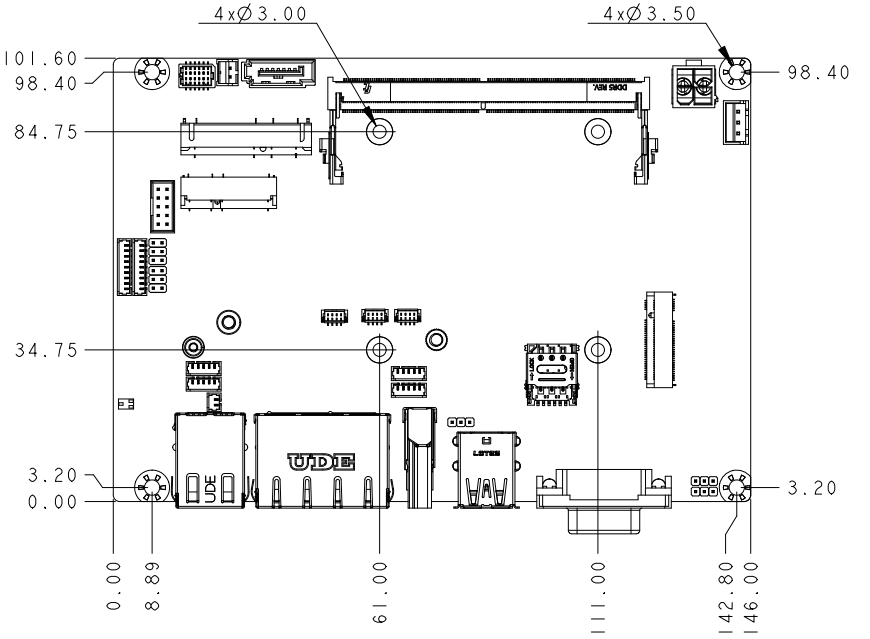
Hardware Information

## 2.1 Dimensions

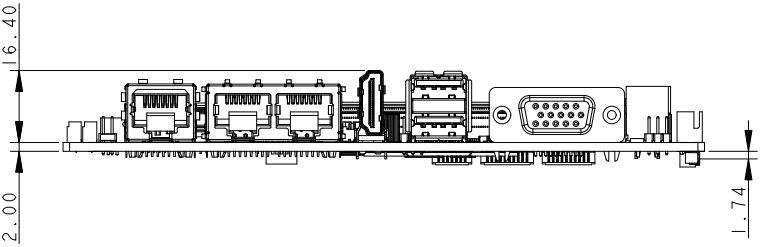
### System Dimensions

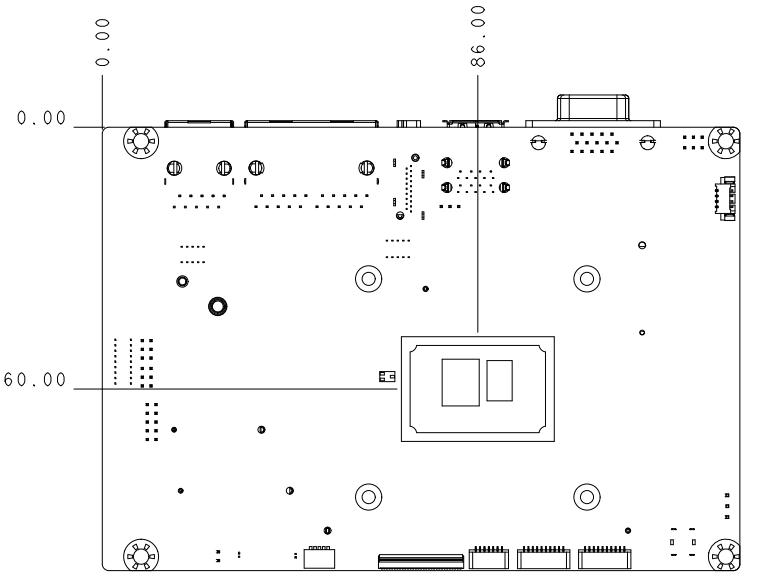


## Board Dimensions




Component Side

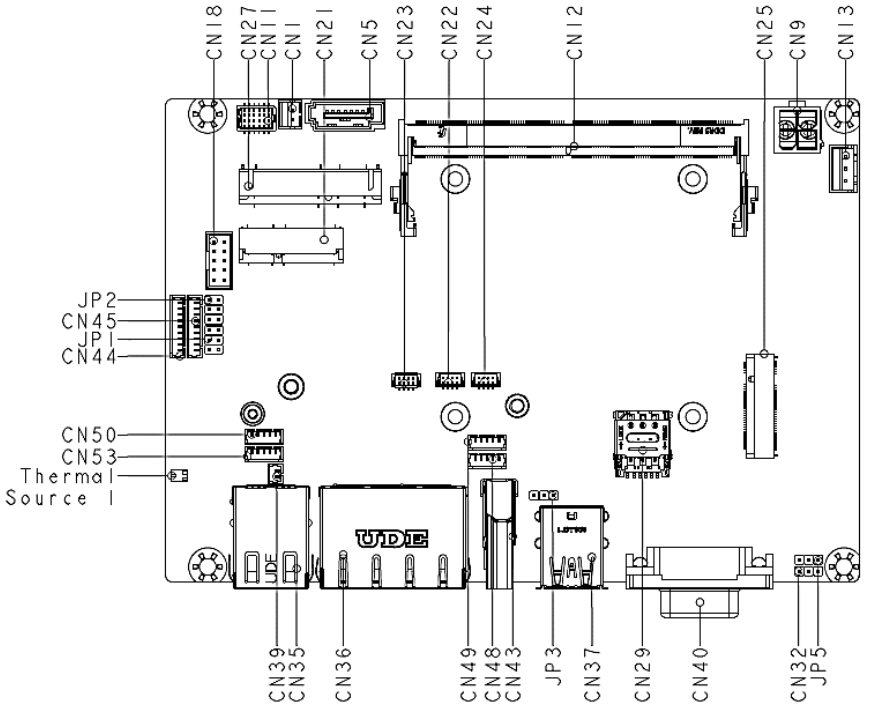




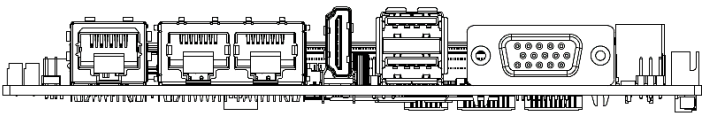

Solder Side

## 2.2 Jumpers and Connectors

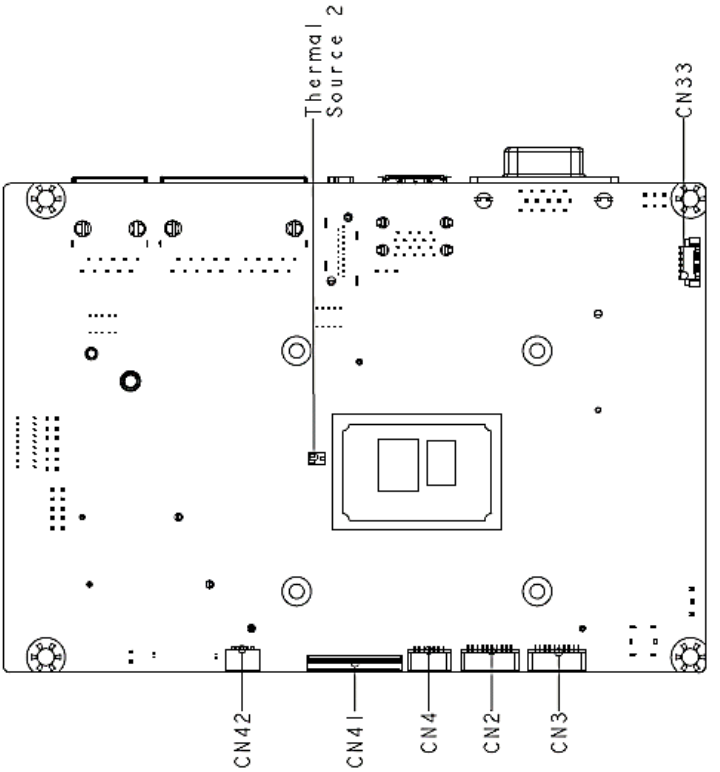
Top View




Front I/O View



Bottom View

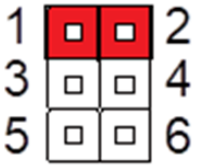


## 2.3 List of Jumpers

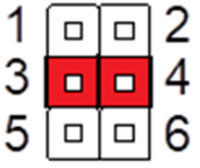
Please refer to the table below for all of the board's jumpers that you can configure for your application

| Label | Function |
|---|---|
| JP1 | COM 2 Pin 9 Function Selection |
| JP2 | COM 1 Pin 9 Function Selection |
| JP3 | Auto Power Button Enable/Disable Selection |
| JP5 | Clear CMOS Jumper |

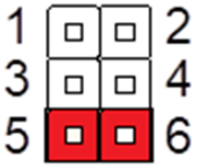
### 2.3.1 COM 2 Pin 9 Function Selection (JP1)




+12V

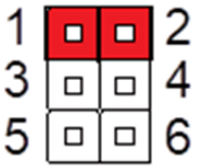



Ring (Default)

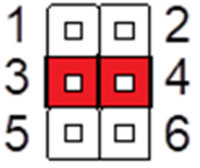



+5V

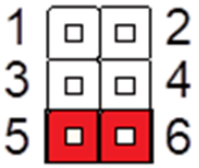
### 2.3.2 COM 1 Pin 9 Function Selection (JP2)




+12V

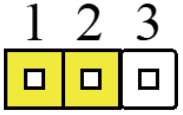



Ring (Default)

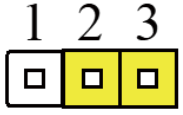



+5V

### 2.3.3 Auto Power Button Enable/Disable Selection (JP3)

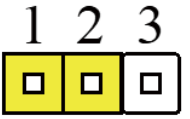



Disable Auto Power Button (Default)

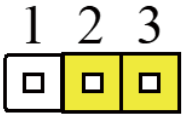



Enable Auto Power Button

### 2.3.4 Clear CMOS Jumper (JP5)




Normal
(Default)




Clear CMOS

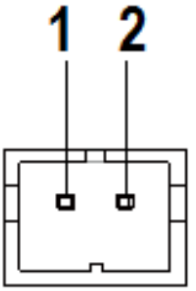
## 2.4 List of Connectors

Please refer to the table below for all of the board's connectors that you can configure for your application

| Label | Function |
|---|---|
| CN1 | +5V Output for SATA HDD |
| CN2 | Front Panel |
| CN3 | Port 80 Debug Port Connector |
| CN4 | SPI Flash Programming Port |
| CN5 | SATA Port |
| CN8 | External +12V Input (Optional) |
| CN9 | External Power Input |
| CN11 | Audio I/O Port |
| CN12 | DDR5 SODIMM |
| CN13 | External +5VSB Input |
| CN18 | GPIO Port |
| CN21 | M.2 2230 E-Key |
| CN22 | LAN Port 1 LED Connector |
| CN23 | LAN Port 3 LED Connector |
| CN24 | LAN Port 2 LED Connector |
| CN25 | M.2 3052 B-Key |
| CN27 | Full-size Mini Card Slot/mSATA |
| CN29 | Nano SIM Card Socket |
| CN33 | 4-pin FAN Connector |
| CN35 | RJ-45 LAN Port 3 |
| CN36 | RJ-45 LAN Port 1/Port 2 |
| CN37 | USB 3.2/USB 2.0 Port 1/Port 2 |
| CN39 | RTC Battery Connector |
| CN40 | CRT Connector |
| CN42 | I2C/SMBus Connector |
| CN43 | HDMI Connector |
| CN44 | COM Port 1 |
| CN45 | COM Port 2 |

| Label | Function |
|---|---|
| CN48 | USB 2.0 Port 3 |
| CN49 | USB 2.0 Port 4 |
| CN50 | USB 2.0 Port 5 |
| CN53 | USB 2.0 Port 6 |

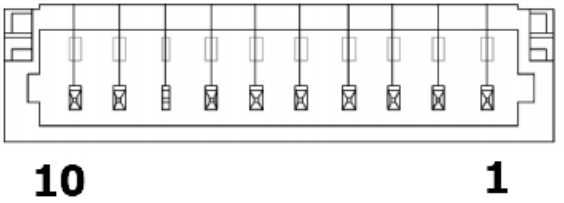
### 2.4.1 +5V Output for SATA HDD (CN1)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | +V5S | PWR | +5V |
| 2 | GND | GND | - |

**Note:** The driving current of +V5S supports up to 2A.

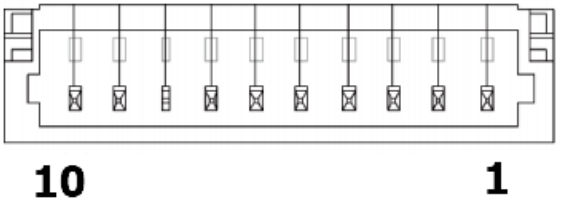
### 2.4.2 Front Panel (CN2)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | GND | GND | GND |
| 2 | EXT_PWRBTN# | IN | |
| 3 | SATA_LED- | OUT | |
| 4 | SATA_LED+ | OUT | |
| 5 | BUZZER- | OUT | |
| 6 | BUZZER+ | OUT | |

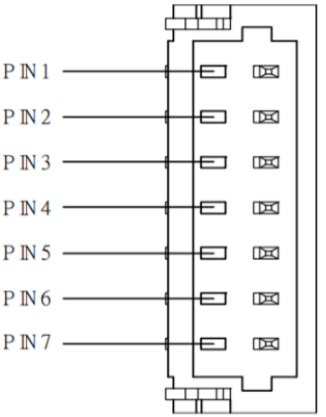
| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 7 | GND | GND | GND |
| 8 | PWR_LED+ | OUT | |
| 9 | GND | GND | GND |
| 10 | HWRST# | IN | |

### 2.4.3 Port 80 Debug Port Connector (CN3)



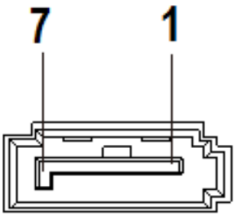

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | ESP_IO0 | I/O | +1.8V |
| 2 | ESP_IO1 | I/O | +1.8V |
| 3 | ESP_IO2 | I/O | +1.8V |
| 4 | ESP_IO3 | I/O | +1.8V |
| 5 | +V3P3S | PWR | +3.3V |
| 6 | ESPI_CS | IN | |
| 7 | ESPI_RST | OUT | +3.3V |
| 8 | GND | GND | GND |
| 9 | ESPI_CLK | OUT | +1.8V |
| 10 | +V3P3A | PWR | +3.3V |

### 2.4.4 SPI Flash Programming Port (CN4)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | SPI_MISO | OUT | |
| 2 | GND | GND | GND |
| 3 | SPI_CLK | IN | |
| 4 | +V3P3A_SPI | PWR | +3.3V |
| 5 | SPI_MOSI | IN | |
| 6 | SPI_CS | IN | |
| 7 | NC | | |

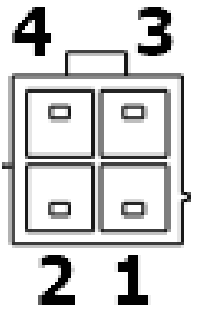
### 2.4.5 SATA Port (CN5)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | GND | GND | GND |
| 2 | SATA_TX+ | DIFF | |

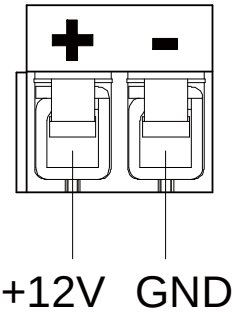
| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 3 | SATA_TX- | DIFF | |
| 4 | GND | GND | GND |
| 5 | SATA_RX- | DIFF | |
| 6 | SATA_RX+ | DIFF | |
| 7 | GND | GND | GND |

### 2.4.6 External +12V Input (Optional) (CN8)



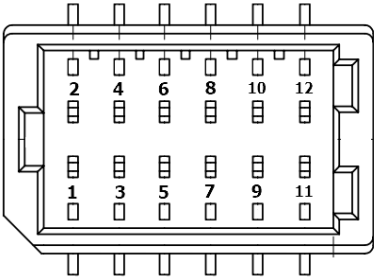

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | GND | GND | GND |
| 2 | GND | GND | GND |
| 3 | +12V | PWR | +12V |
| 4 | +12V | PWR | +12V |

## 2.4.7 External Power Input (CN9)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | +12V | PWR | +12V |
| 2 | GND | GND | GND |

## 2.4.8 Audio I/O Port (CN11)



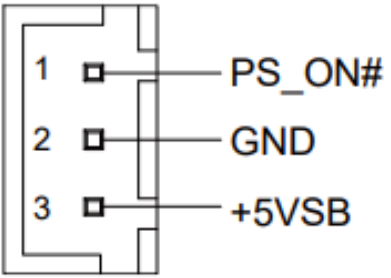

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | RIGHT_OUT | OUT | |
| 2 | MIC_R | IN | |
| 3 | LEFT_OUT | OUT | |

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 4 | MIC_L | IN | |
| 5 | JD_LOUT | IN | |
| 6 | JD_MIC | IN | |
| 7 | GND_AUDIO | GND | |
| 8 | GND_AUDIO | GND | |
| 9 | JD_LIN | IN | |
| 10 | LINE_R_IN | IN | |
| 11 | +5V_AUDIO | PWR | +5V |
| 12 | LINE_L_IN | IN | |

### 2.4.9 DDR5 SO-DIMM Channel (CN12)

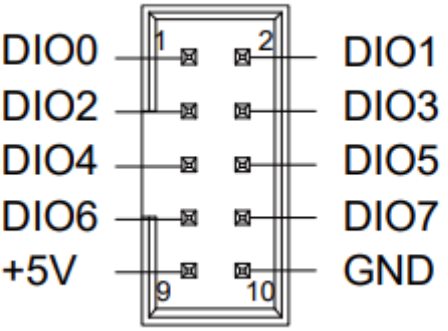
Standard Specifications.

### 2.4.10 External +5VSB Input (CN13)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | PS_ON# | OUT | +5V |
| 2 | GND | GND | |
| 3 | +5VSB | PWR | +5V at 2A |

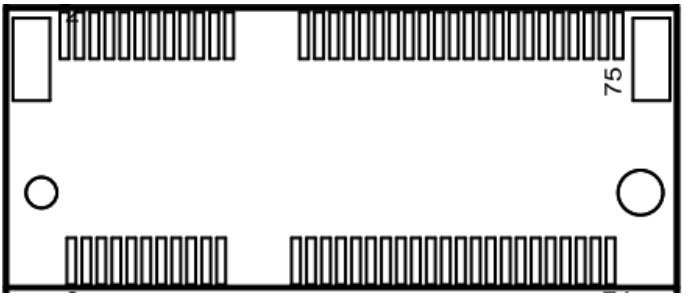
### 2.4.11 GPIO Port (CN18)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | GPIO_0 | IN/OUT | |
| 2 | GPIO_1 | IN/OUT | |
| 3 | GPIO_2 | IN/OUT | |
| 4 | GPIO_3 | IN/OUT | |
| 5 | GPIO_4 | IN/OUT | |
| 6 | GPIO_5 | IN/OUT | |
| 7 | GPIO_6 | IN/OUT | |
| 8 | GPIO_7 | IN/OUT | |
| 9 | +V5S | PWR | +5V |
| 10 | GND | GND | GND |

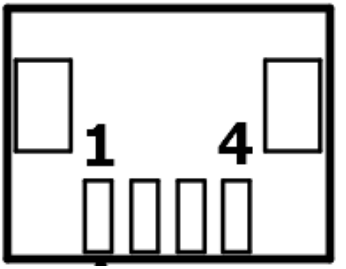
**Note:** The driving current of +V5S supports up to 0.5A.

### 2.4.12 M.2 2230 E-Key (CN21)



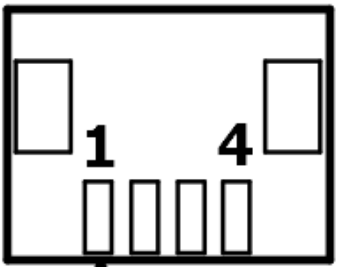

Note: Standard specifications.

### 2.4.13 LAN Port 1 LED Connector (CN22)



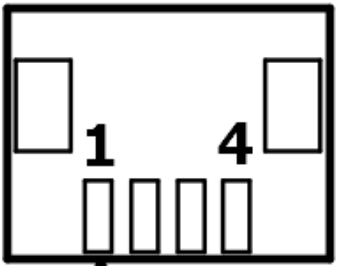

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | +V3P3A | PWR | +3.3V |
| 2 | LAN1_1000# | I/O | |
| 3 | LAN1_ACT# | I/O | |
| 4 | LAN1_2500# | I/O | |

### 2.4.14 LAN Port 3 LED Connector (CN23)



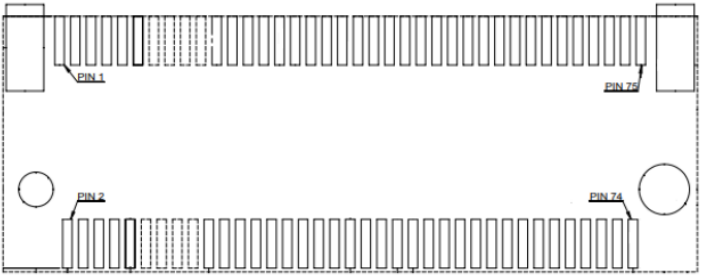

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | +V3P3A | PWR | +3.3V |
| 2 | LAN3_1000# | I/O | |
| 3 | LAN3_ACT# | I/O | |
| 4 | LAN3_2500# | I/O | |

### 2.4.15 LAN Port 2 LED Connector (CN24)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | +V3P3A | PWR | +3.3V |
| 2 | LAN2_1000# | I/O | |
| 3 | LAN2_ACT# | I/O | |
| 4 | LAN2_2500# | I/O | |

## 2.4.16 M.2 3052 B-Key (CN25)




**Note:** Standard specifications.

## 2.4.17 Full Size Mini Card Slot/mSATA (CN27)

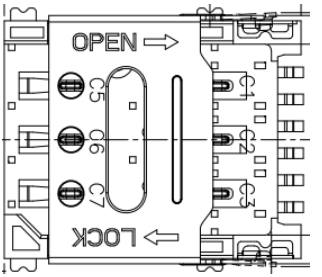
| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | PCIE_WAKE# | IN | |
| 2 | +3.3V | PWR | +3.3V |
| 3 | NC | NC | NC |
| 4 | GND | GND | GND |
| 5 | NC | NC | NC |
| 6 | +1.5V | PWR | +1.5V |
| 7 | PCIE_CLK_REQ# | IN | |
| 8 | NC | NC | NC |
| 9 | GND | GND | GND |
| 10 | NC | NC | NC |
| 11 | PCIE_REF_CLK- | DIFF | |
| 12 | NC | NC | NC |
| 13 | PCIE_REF_CLK+ | DIFF | |
| 14 | NC | NC | NC |
| 15 | GND | GND | GND |
| 16 | NC | NC | NC |
| 17 | NC | NC | NC |
| 18 | GND | GND | GND |

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 19 | NC | NC | NC |
| 20 | W_DISABLE# | OUT | +3.3V |
| 21 | GND | GND | GND |
| 22 | PCIE_RST# | OUT | +3.3V |
| 23 | PCIE_RX-/SATA_RX+ | DIFF | GND |
| 24 | +3.3V | PWR | +3.3V |
| 25 | PCIE_RX+/SATA_RX- | DIFF | |
| 26 | GND | GND | GND |
| 27 | GND | GND | GND |
| 28 | +1.5V | PWR | +1.5V |
| 29 | GND | GND | GND |
| 30 | SMB_CLK | I/O | +3.3V |
| 31 | PCIE_TX-/SATA_TX- | DIFF | |
| 32 | SMB_DATA | I/O | +3.3V |
| 33 | PCIE_TX+/SATA_TX+ | DIFF | |
| 34 | GND | GND | GND |
| 35 | GND | GND | GND |
| 36 | USB_D- | DIFF | |
| 37 | GND | GND | GND |
| 38 | USB_D+ | DIFF | |
| 39 | +3.3V | PWR | +3.3V |
| 40 | GND | GND | GND |
| 41 | +3.3V | PWR | +3.3V |
| 42 | NC | NC | NC |
| 43 | GND | GND | GND |
| 44 | NC | NC | NC |
| 45 | NC | NC | NC |
| 46 | NC | NC | NC |
| 47 | NC | NC | NC |
| 48 | +1.5V | PWR | +1.5V |
| 49 | NC | | |
| 50 | GND | GND | GND |
| 51 | NC | NC | NC |

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 52 | +3.3V | PWR | +3.3V |

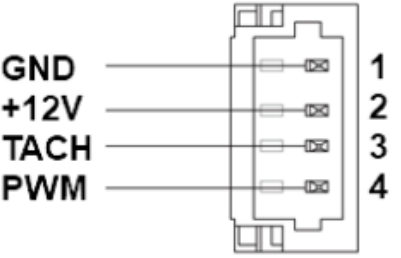
**Note:** Mini Card/mSATA function can be set by BIOS. Default is mSATA.

### 2.4.18 Nano SIM Card Socket (CN29)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | UIM_PWR | PWR | |
| 2 | UIM_RST | IN | |
| 3 | UIM_CLK | IN | |
| 4 | N/A | N/A | |
| 5 | GND | GND | GND |
| 6 | N/A | N/A | |
| 7 | UIM_DATA | I/O | |
| 8 | N/A | N/A | |

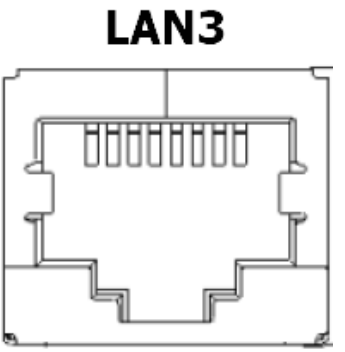
### 2.4.19 4-pin FAN Connector (CN33)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | GND | GND | GND |
| 2 | FAN_POWER | PWR | +12V |
| 3 | FAN_TAC | IN | |
| 4 | FAN_CTL | | |

Note: The driving current of FAN_POWER supports up to 1A.

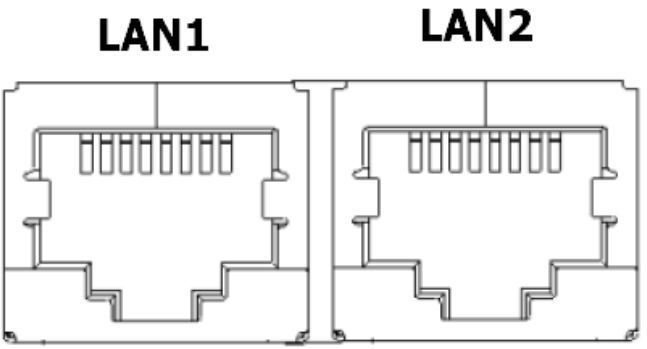
### 2.4.20 RJ-45 LAN Port 3 (CN35)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | MDI0+ | DIFF | |
| 2 | MDI0- | DIFF | |
| 3 | MDI1+ | DIFF | |

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 4 | MDI2+ | DIFF | |
| 5 | MDI2- | DIFF | |
| 6 | MDI1- | DIFF | |
| 7 | MDI3+ | DIFF | |
| 8 | MDI3- | DIFF | |

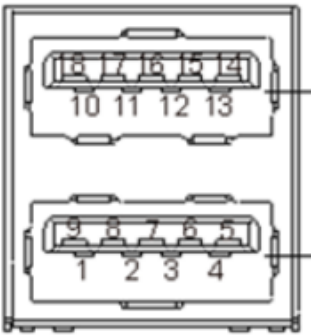
### 2.4.21 RJ-45 LAN Port 1/Port 2 (CN36)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1P1 | LAN2_MDI0_P | DIFF | |
| 1P2 | LAN2_MDI0_N | DIFF | |
| 1P3 | LAN2_MDI1_P | DIFF | |
| 1P4 | LAN2_MDI1_N | DIFF | |
| 1P7 | LAN2_MDI2_P | DIFF | |
| 1P8 | LAN2_MDI2_N | DIFF | |
| 1P9 | LAN2_MDI3_P | DIFF | |
| 1P10 | LAN2_MDI3_N | DIFF | |
| 2P1 | LAN1_MDI0_P | DIFF | |
| 2P2 | LAN1_MDI0_N | DIFF | |
| 2P3 | LAN1_MDI1_P | DIFF | |
| 2P4 | LAN1_MDI1_N | DIFF | |
| 2P7 | LAN1_MDI2_P | DIFF | |
| 2P8 | LAN1_MDI2_N | DIFF | |

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 2P9 | LAN1_MDI3_P | DIFF | |
| 2P10 | LAN1_MDI3_N | DIFF | |

### 2.4.22 USB 3.2/USB 2.0 Port 1/Port 2 (CN37)

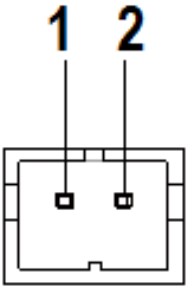



| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | +5VSB | PWR | +5V |
| 2 | USB2_1_DN | DIFF | |
| 3 | USB2_1_DP | DIFF | |
| 4 | GND | GND | GND |
| 5 | USB3_1_RXN | DIFF | |
| 6 | USB3_1_RXP | DIFF | |
| 7 | GND | GND | GND |
| 8 | USB3_1_TXN | DIFF | |
| 9 | USB3_1_TXP | DIFF | |
| 10 | +5VSB | PWR | +5V |
| 11 | USB2_2_DN | DIFF | |
| 12 | USB2_2_DP | DIFF | |
| 13 | GND | GND | GND |
| 14 | USB3_2_RXN | DIFF | |
| 15 | USB3_2_RXP | DIFF | |
| 16 | GND | GND | GND |
| 17 | USB3_2_TXN | DIFF | |

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 18 | USB3_2_TXP | DIFF | |

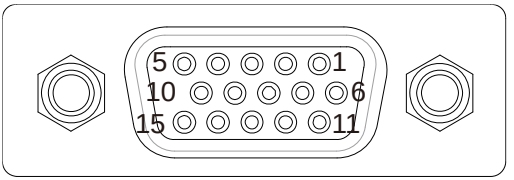
Note: The driving current of +5VSB supports up to 0.9A/Port.

### 2.4.23 RTC Battery Connector (CN39)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | +3.3V | PWR | +3.3V |
| 2 | GND | GND | GND |

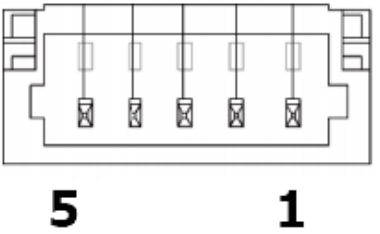
### 2.4.24 CRT Connector (CN40)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | RED | OUT | |
| 2 | GREEN | OUT | |
| 3 | BLUE | OUT | |
| 4 | NC | NC | |
| 5 | GND | GND | |
| 6 | RED_GND_RTN | GND | |

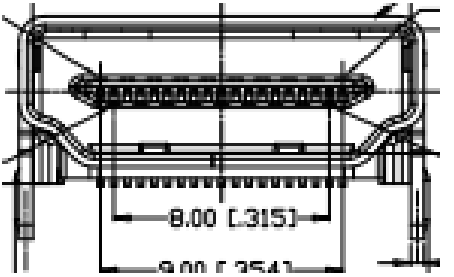
| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 7 | GREEN_GND_RTN | GND | |
| 8 | BLUE_GND_RTN | GND | |
| 9 | +5V | PWR | +5V |
| 10 | GND | GND | |
| 11 | NC | NC | |
| 12 | DDC_DATA | I/O | +5V |
| 13 | HSYNC | OUT | |
| 14 | VSYNC | OUT | |
| 15 | DDC_CLK | I/O | +5V |

### 2.4.25 I2C/SMBus Connector (CN42)



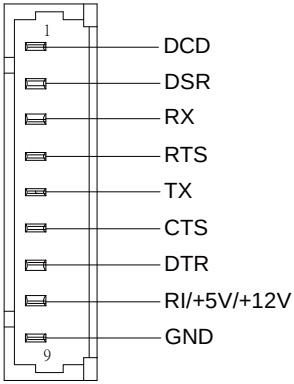

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | +V3P3A/ +V3P3S | PWR | +3.3V |
| 2 | SMB_CLK/ I2C_CLK/TIME_SYNC0 | OUT | |
| 3 | SMB_DAT/ I2C_DAT/TIME_SYNC1 | I/O | |
| 4 | SMB_ALERT/ INT_SERIRQ/LAN_SDP | IN | |
| 5 | GND | GND | GND |

### 2.4.26 HDMI Connector (CN43)



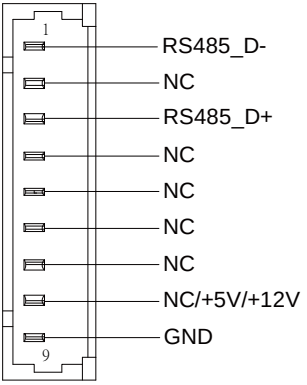

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | HDMI_TX2+ | DIFF | |
| 2 | GND | GND | GND |
| 3 | HDMI_TX2- | DIFF | |
| 4 | HDMI_TX1+ | DIFF | |
| 5 | GND | GND | GND |
| 6 | HDMI_TX1- | DIFF | |
| 7 | HDMI_TX0+ | DIFF | |
| 8 | GND | GND | GND |
| 9 | HDMI_TX0- | DIFF | |
| 10 | HDMI_CLK+ | DIFF | |
| 11 | GND | GND | GND |
| 12 | HDMI_CLK- | DIFF | |
| 13 | N/A | N/A | N/A |
| 14 | N/A | N/A | N/A |
| 15 | DDC_CLK | I/O | |
| 16 | DDC_DATA | I/O | |
| 17 | GND | GND | GND |
| 18 | +V5S | PWR | +5V |
| 19 | HDMI_HPD | IN | |

### 2.4.27 COM Port 1 (CN44)



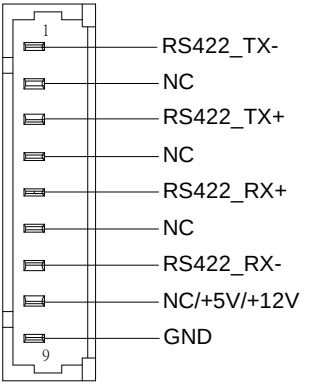

| RS-232 | | | |
|---|---|---|---|
| Pin | Pin Name | Signal Type | Signal Level |
| 1 | DCD | IN | |
| 2 | DSR | IN | |
| 3 | RX | IN | |
| 4 | RTS | OUT | ±9V |
| 5 | TX | OUT | ±9V |
| 6 | CTS | IN | |
| 7 | DTR | OUT | ±9V |
| 8 | RI/+5V/+12V | IN/ PWR | +5V/+12V |
| 9 | GND | GND | |

RS-485




| RS-485 | | | |
|---|---|---|---|
| Pin | Pin Name | Signal Type | Signal Level |
| 1 | RS485_D- | I/O | ±5V |
| 2 | NC | | |
| 3 | RS485_D+ | I/O | ±5V |
| 4 | NC | | |
| 5 | NC | | |
| 6 | NC | | |
| 7 | NC | | |
| 8 | NC/+5V/+12V | PWR | +5V/+12V |
| 9 | GND | GND | |

RS-422

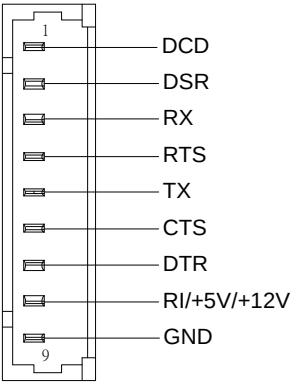



| RS-422 | | | |
|---|---|---|---|
| Pin | Pin Name | Signal Type | Signal Level |
| 1 | RS422_TX- | OUT | ±5V |
| 2 | NC | | |
| 3 | RS422_TX+ | OUT | ±5V |
| 4 | NC | | |
| 5 | RS422_RX+ | IN | |
| 6 | NC | | |
| 7 | RS422_RX- | IN | |
| 8 | NC/+5V/+12V | PWR | +5V/+12V |
| 9 | GND | GND | |

Note: COM 1 RS-232/422/485 can be set by BIOS setting. Default is RS-232.

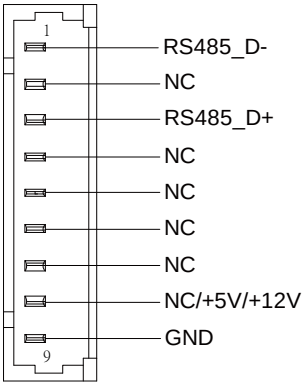
Note: Pin 8 function can be set by JP2.

## 2.4.28 COM Port 2 (CN45)



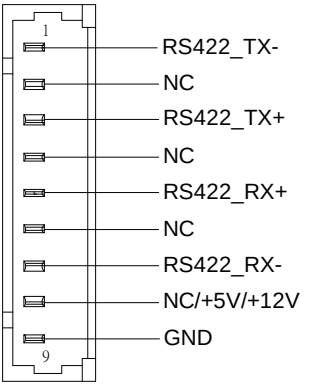

| RS-232 | | | |
|---|---|---|---|
| Pin | Pin Name | Signal Type | Signal Level |
| 1 | DCD | IN | |
| 2 | DSR | IN | |
| 3 | RX | IN | |
| 4 | RTS | OUT | ±9V |
| 5 | TX | OUT | ±9V |
| 6 | CTS | IN | |
| 7 | DTR | OUT | ±9V |
| 8 | RI/+5V/+12V | IN/ PWR | +5V/+12V |
| 9 | GND | GND | |

RS-485




| RS-485 | | | |
|---|---|---|---|
| Pin | Pin Name | Signal Type | Signal Level |
| 1 | RS485_D- | I/O | ±5V |
| 2 | NC | | |
| 3 | RS485_D+ | I/O | ±5V |
| 4 | NC | | |
| 5 | NC | | |
| 6 | NC | | |
| 7 | NC | | |
| 8 | NC/+5V/+12V | PWR | +5V/+12V |
| 9 | GND | GND | |

RS-422

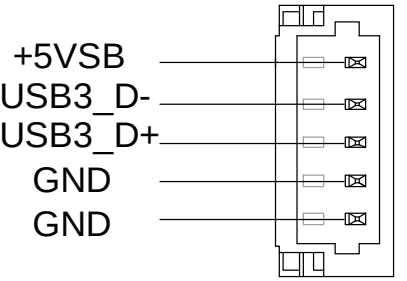



| RS-422 | | | |
|---|---|---|---|
| Pin | Pin Name | Signal Type | Signal Level |
| 1 | RS422_TX- | OUT | ±5V |
| 2 | NC | | |
| 3 | RS422_TX+ | OUT | ±5V |
| 4 | NC | | |
| 5 | RS422_RX+ | IN | |
| 6 | NC | | |
| 7 | RS422_RX- | IN | |
| 8 | NC/+5V/+12V | PWR | +5V/+12V |
| 9 | GND | GND | |

**Note:** COM 2 RS-232/422/485 can be set by BIOS setting. Default is RS-232.

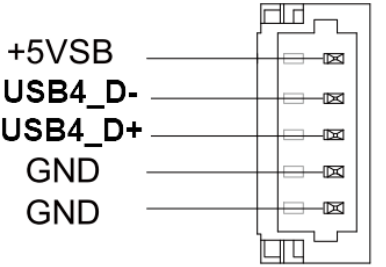
**Note:** Pin 8 function can be set by JP1.

### 2.4.29 USB 2.0 Port 3 (CN48)



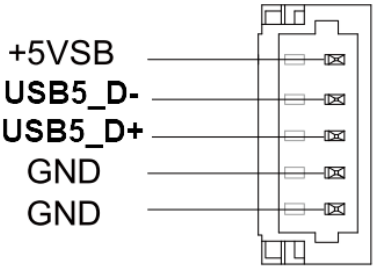

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | +5VSB | PWR | +5V |
| 2 | USB_D- | DIFF | |
| 3 | USB_D+ | DIFF | |
| 4 | GND | GND | |
| 5 | GND | GND | |

### 2.4.30 USB 2.0 Port 4 (CN49)



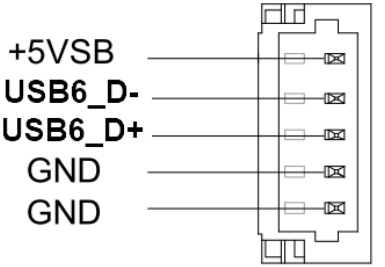

| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | +5VSB | PWR | +5V |
| 2 | USB_D- | DIFF | |
| 3 | USB_D+ | DIFF | |
| 4 | GND | GND | |
| 5 | GND | GND | |

### 2.4.31 USB 2.0 Port 5 (CN50)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | +5VSB | PWR | +5V |
| 2 | USB_D- | DIFF | |
| 3 | USB_D+ | DIFF | |
| 4 | GND | GND | |
| 5 | GND | GND | |

### 2.4.32 USB 2.0 Port 6 (CN53)




| Pin | Pin Name | Signal Type | Signal Level |
|---|---|---|---|
| 1 | +5VSB | PWR | +5V |
| 2 | USB_D- | DIFF | |
| 3 | USB_D+ | DIFF | |
| 4 | GND | GND | |
| 5 | GND | GND | |

## 2.5 Hardware Installation

This section details the steps needed to install various hardware components for the GENESYSM-ADN6. It is recommended that you read through each step before beginning installation and to make sure you have all necessary tools and components.

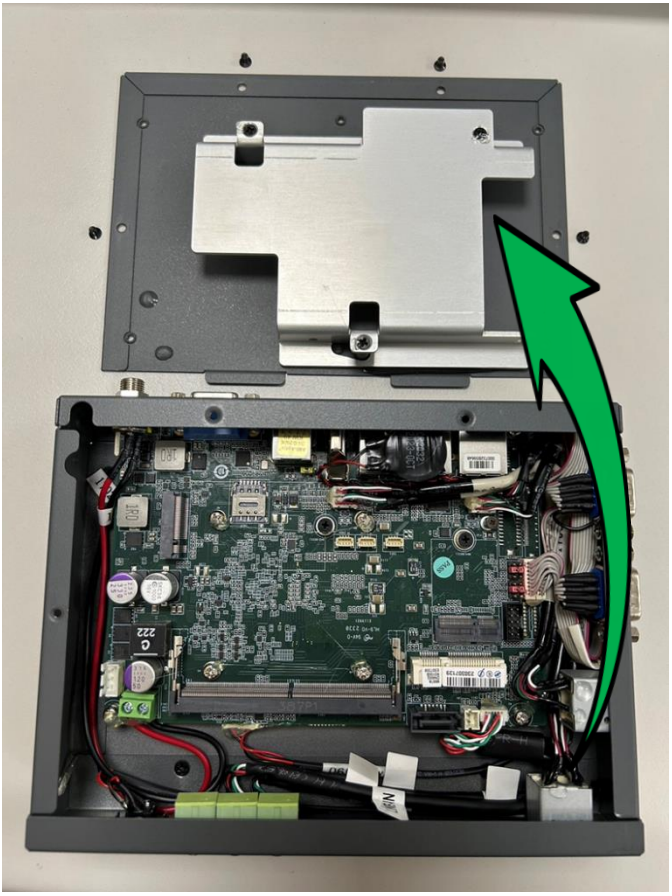
### 2.5.1 Accessing the Bottom Panel

For this process you will need a Phillips head screwdriver.

**Step 1:** Remove the four black screws securing the bottom panel.

**Step 2:** The bottom panel will lift up and swing toward the rear panel of the system, then can be removed.

**Note:** Be careful when opening if you have previously installed a 2.5" SATA Drive.



## 2.5.2 RAM Module Installation

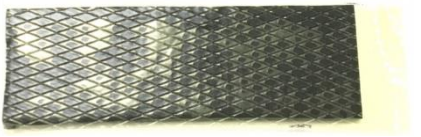
Before you begin, make sure you have the RAM module(s) you wish to install, along with thermal pads for each.

**Memory Slot**
**CN12**

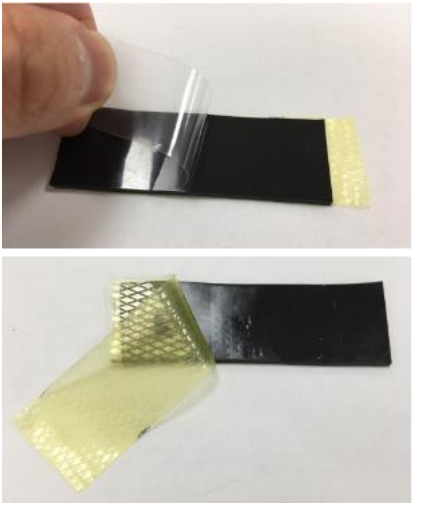
Prior to RAM module installation, please ensure a thermal pad of the appropriate type* (Size 57mm x 23mm x 1mm) is applied to the module, as shown.

*Note: TH5ADN6030, (TF).Thermal PAD.XR-HL.57*23*1mm.GENESYSM-ADN6.SUPA.3L1057002301000.(RAM)

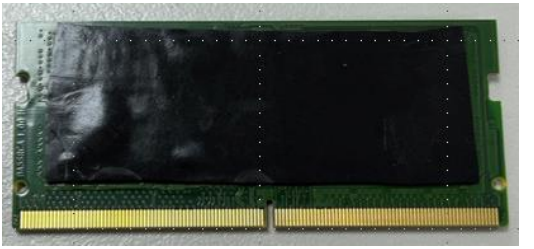
Thermal pad to be used for CN12 module installation



**Step 1:** Remove the protective film of the upper and lower layers of the thermal pad.

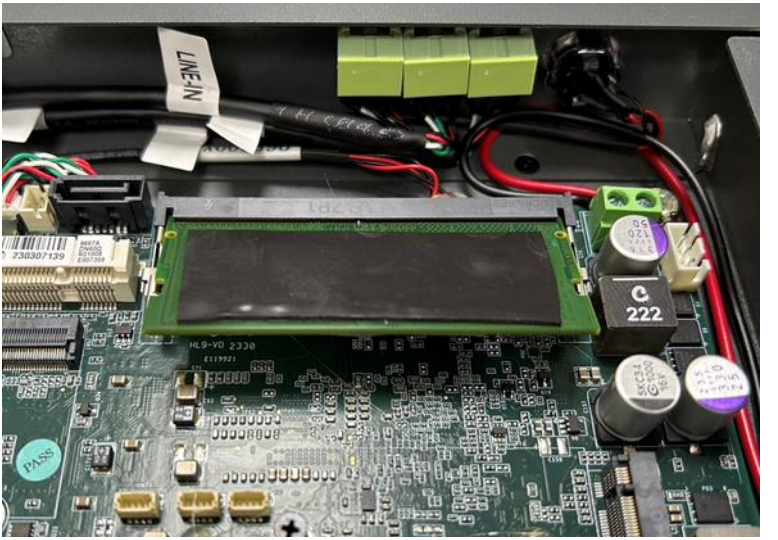


**Step 2**: Apply thermal pad to RAM module.



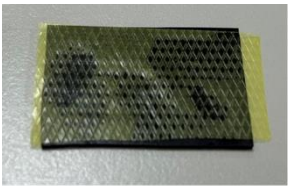
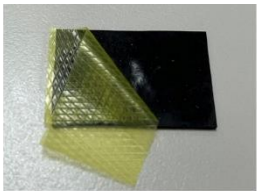
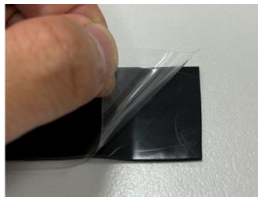
Step 3: Insert the RAM module with the thermal pad affixed into the DDR SO-DIMM Slot (CN12) at approximately a 45° angle, then gently press down until it is secured by the tabs.

RAM module installation is complete.



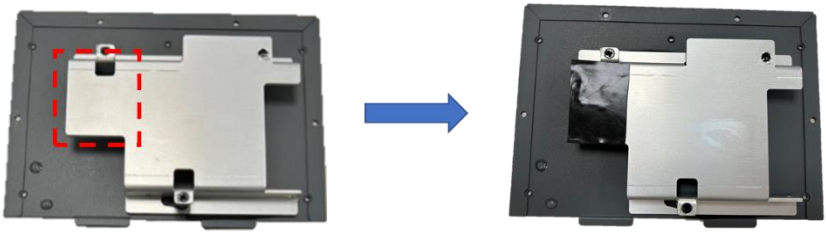
### 2.5.3 Mini Card Installation

**Step 1**: Remove the protective film of the upper and lower layers of the thermal pad.

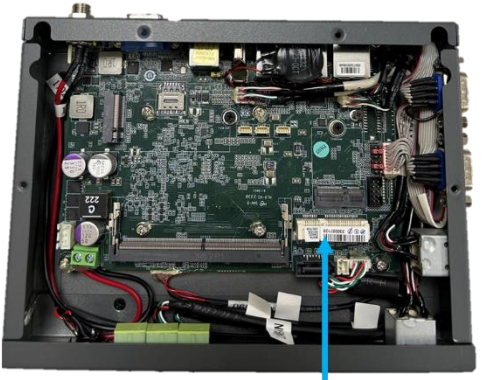




**Note**: Prior to Mini Card installation, please ensure a thermal pad of the appropriate type (Size 44mm x 30mm x 1.5mm) is applied to the module, as shown

**Step 2**: Apply thermal pad to HDD cover (red dotted area).



Step 3: Insert the mSATA module into Full-size Mini Card Slot (CN27) at approximately a 45° angle, then gently press down until it is secured.



**Full Size Mini Card Slot/mSATA (CN27)**

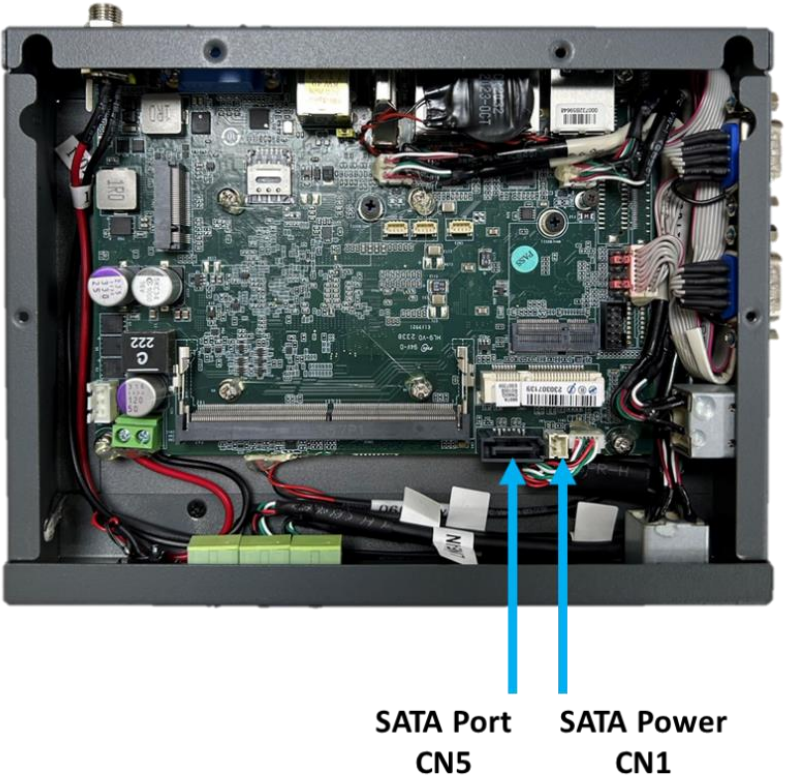
Memory and Mini Card installation is complete.

## 2.5.4 2.5" SATA Drive Installation

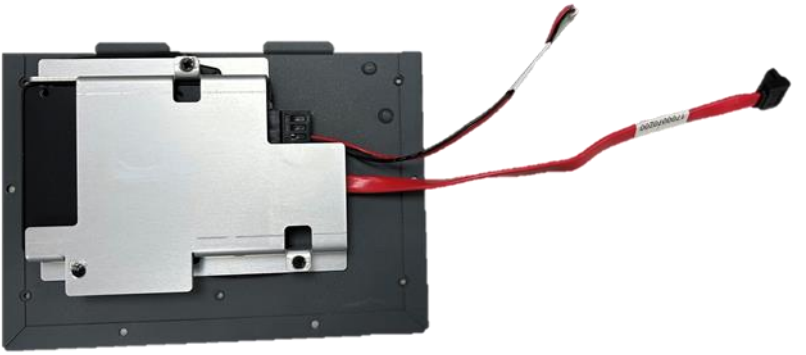
Before beginning, make sure you have the following prepared:

- SATA Drive x 1
- Black Screws x 4 (to mount 2.5" drive)
- SATA Cable x 1
- SATA Power Cable x 1
- Phillips head screwdriver

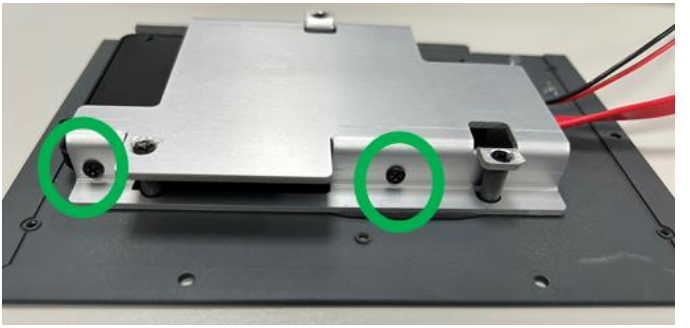
Please note the location of the SATA Port (CN5) and SATA Power Connector (CN1)

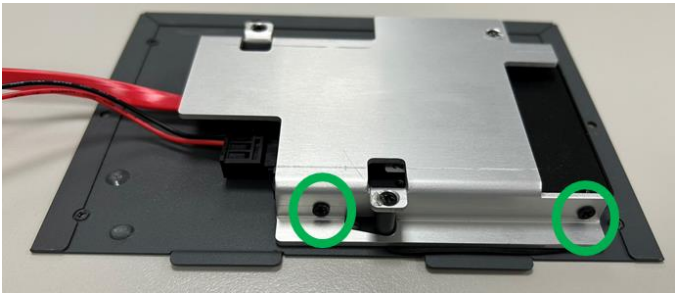


**Step 1:** Connect the SATA and SATA Power cables to the 2.5" drive.

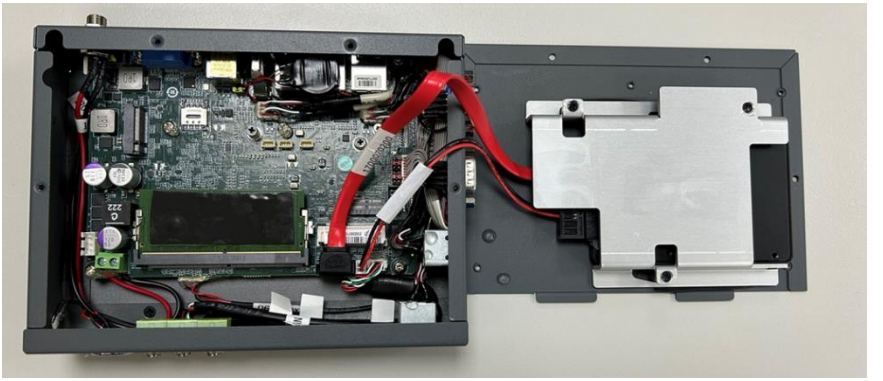


**Step 2**: Lock the SSD to the drive with two (2) screws on each side of the drive (four (4) screws total).

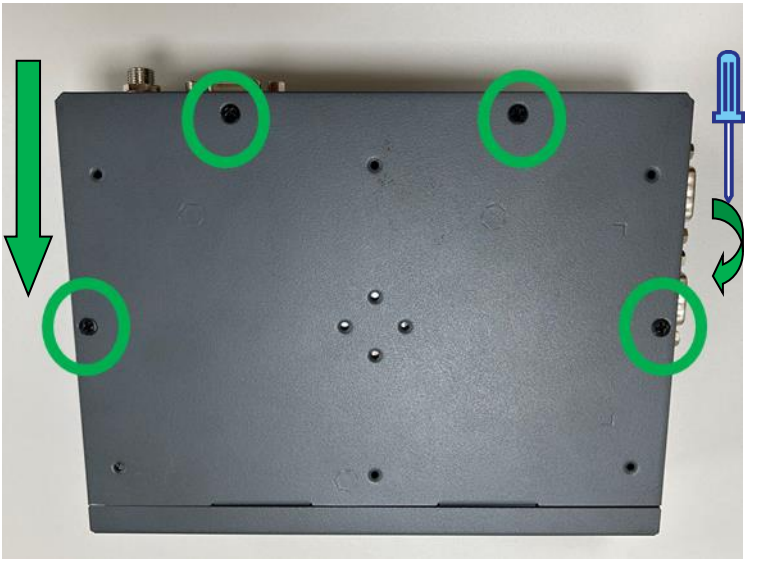




**Step 3:** Insert the SATA Cable into CN5 and SATA Power Cable into CN1.



If you are finished with hardware installation, replace the back panel and secure with four black screws.

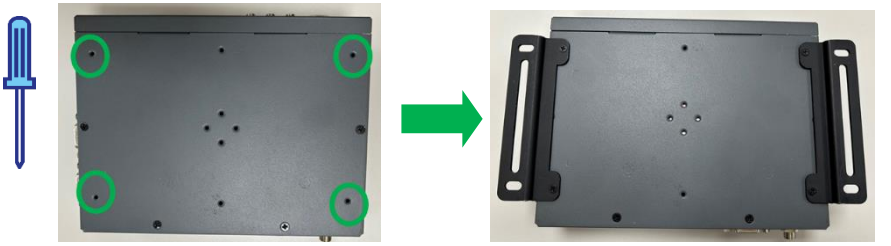


### 2.5.5 Wall Mount Assembly

Before beginning, ensure all panels on the system are secured. Then make sure you have to following components ready:

- Wallmount brackets x 2
- Black screws x 4 (two for each bracket)
- Phillips head screwdriver

Line up the brackets with the four open holes on the bottom panel of the system as shown, then secure brackets with the four screws.

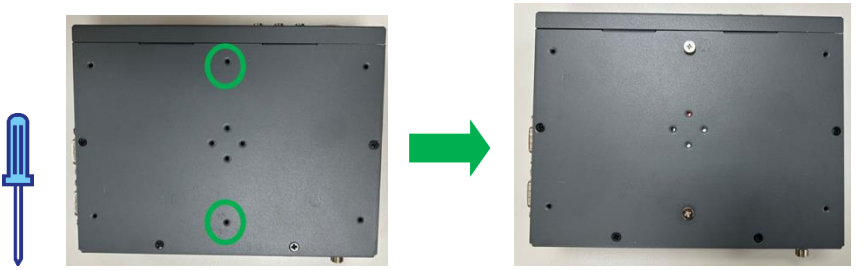


## 2.5.6 VESA Mount Installation

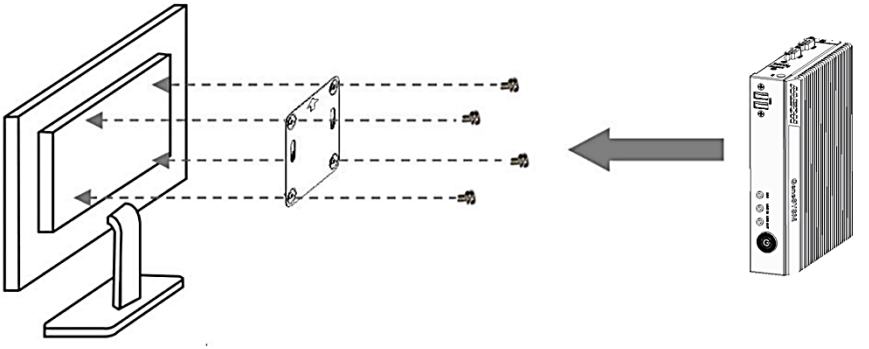
Before beginning, ensure all panels on the system are secured. Then make sure you have to following components ready:

- VESA mount screws x 2 (head is flat)
- VESA mount x 1
- Phillips head screwdriver

Insert VESA mount screws into the two empty holes on the bottom panel of the system as shown.



The system can now be attached to the VESA bracket. Mount the bracket to a surface or the back of a display/monitor with VESA attachment points. Use the four silver screws with washers to secure the bracket to the back of the monitor with the arrow pointing up. Then, attach the system to the VESA mount.



### 2.5.7 DIN Rail Mount Installation

Before beginning, make sure you have the following prepared:

- DIN rail x 1
- DIN rail screws x 2
- Phillips head screwdriver

For DIN Rail assembly, affix the DIN Rail bracket to the chassis using the two (2) screws provided.

Note that there are four orientations in which the DIN Rail bracket can be affixed, with four chassis holes accordingly.

# Chapter 3

AMI BIOS Setup

## 3.1 System Test and Initialization

These routines check the current system configuration stored in the CMOS memory and BIOS NVRAM. If system configuration is not found or system configuration data error is detected, system will load optimized default and re-boot with this default system configuration automatically.

There are four situations in which you will need to setup system configuration:

1. You are starting your system for the first time
2. You have changed the hardware attached to your system
3. The system configuration is reset by Clear-CMOS jumper
4. The CMOS memory has lost power and the configuration information has been erased.

The EPIC-ADN9 CMOS memory has an integral lithium battery backup for data retention. However, you will need to replace the complete unit when it finally runs down.

## 3.2 AMI BIOS Setup

AMI BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed CMOS RAM and BIOS NVRAM so that it retains the Setup information when the power is turned off.

Entering Setup

Power on the computer and press <Del>or <ESC> immediately. This will allow you to enter Setup.

### Main

Set the date, use tab to switch between date elements.

### Advanced

Enable disable boot option for legacy network devices.

### Chipset

Host bridge parameters.

### Boot

Enables/disable quiet boot option.

### Security

Set setup administrator password.

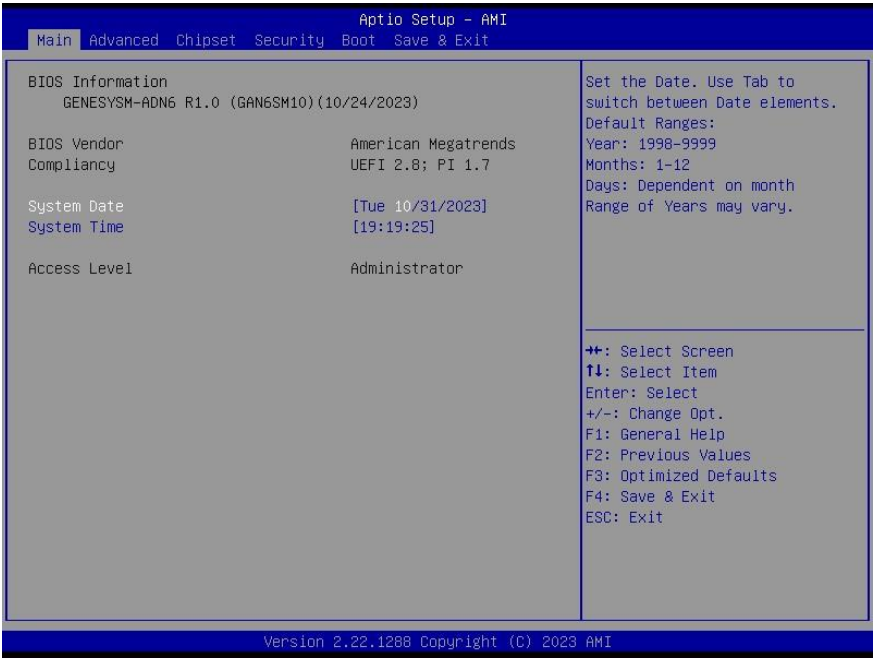
### Save & Exit

Exit system setup after saving the changes.

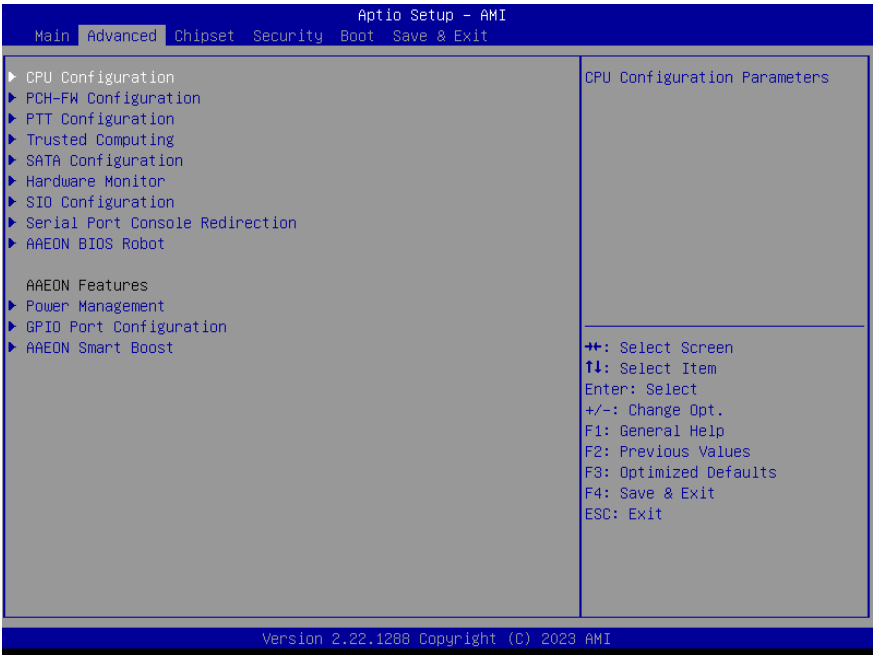
### Intel® AMT Configuration

Configure user content preferences.

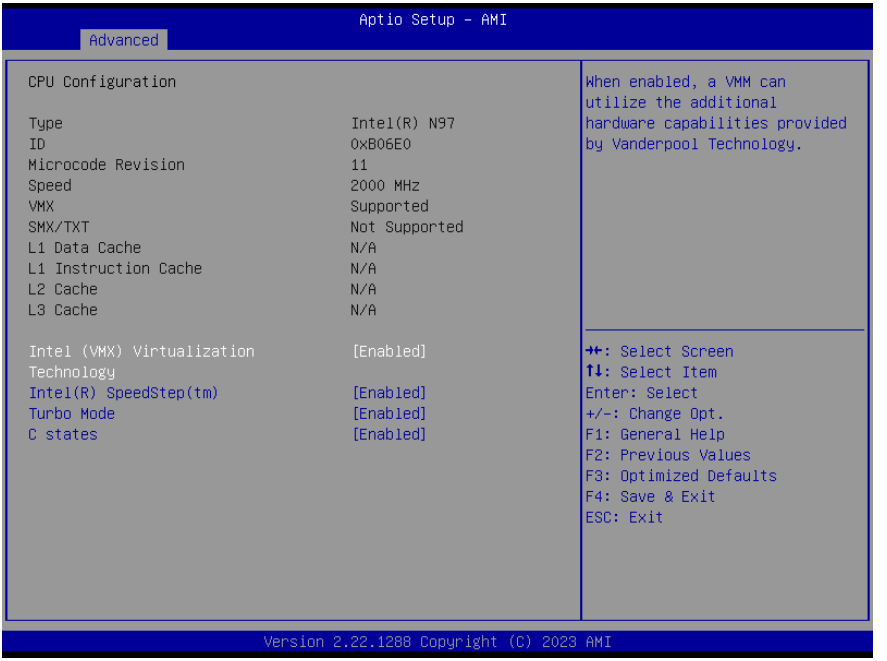
## 3.3 Setup Submenu: Main



## 3.4 Setup Submenu: Advanced

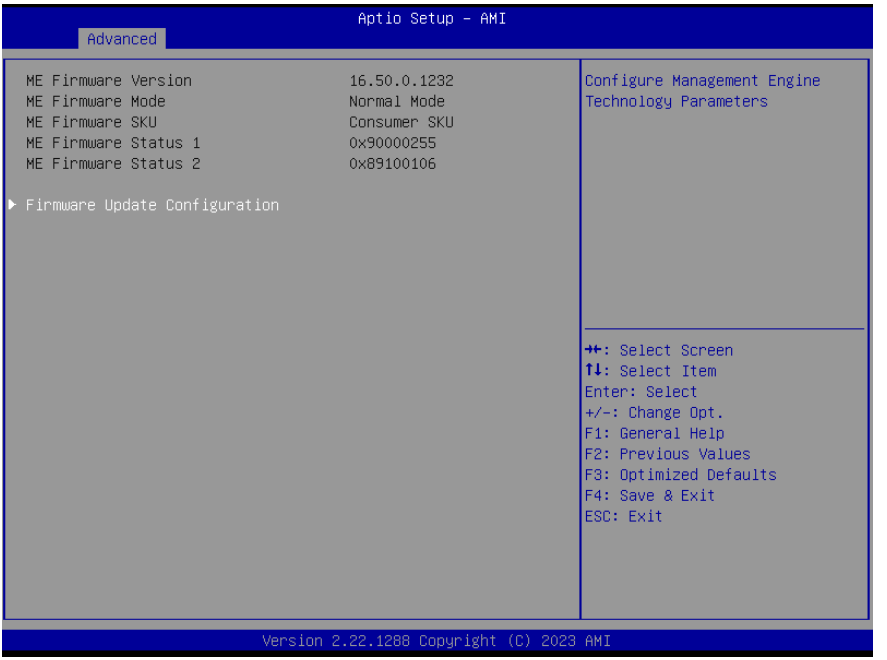


### 3.4.1 CPU Configuration

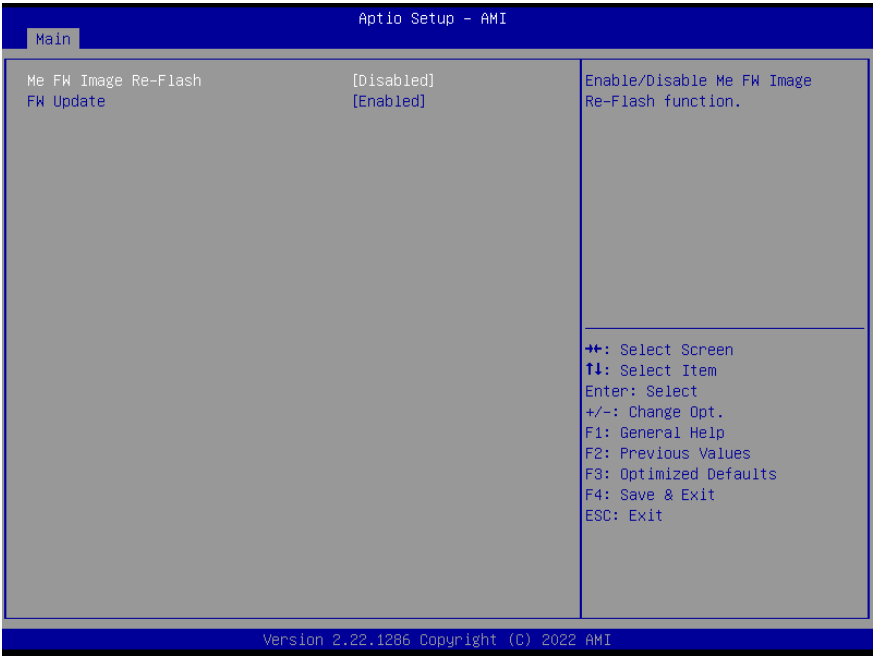



| Options Summary | | |
|---|---|---|
| Intel (VMX) Virtualization Technology | Disabled | |
| | Enabled | Optimal Default, Failsafe Default |
| When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology. | | |
| Intel® SpeedStep™ | Disabled | |
| | Enabled | Optimal Default, Failsafe Default |
| Allows more than two frequency ranges to be supported. | | |
| Turbo Mode | Disabled | |
| | Enabled | Optimal Default, Failsafe Default |
| Enable or Disable processor Turbo Mode (requires EMTTM enable too). AUTO means enabled. | | |
| C states | Disabled | |
| | Enabled | Optimal Default, Failsafe Default |
| Enable/Disable CPU Power Management. Allows CPU to go to C states when it's not 100 utilized. | | |

## 3.4.2 PCH-FW Configuration

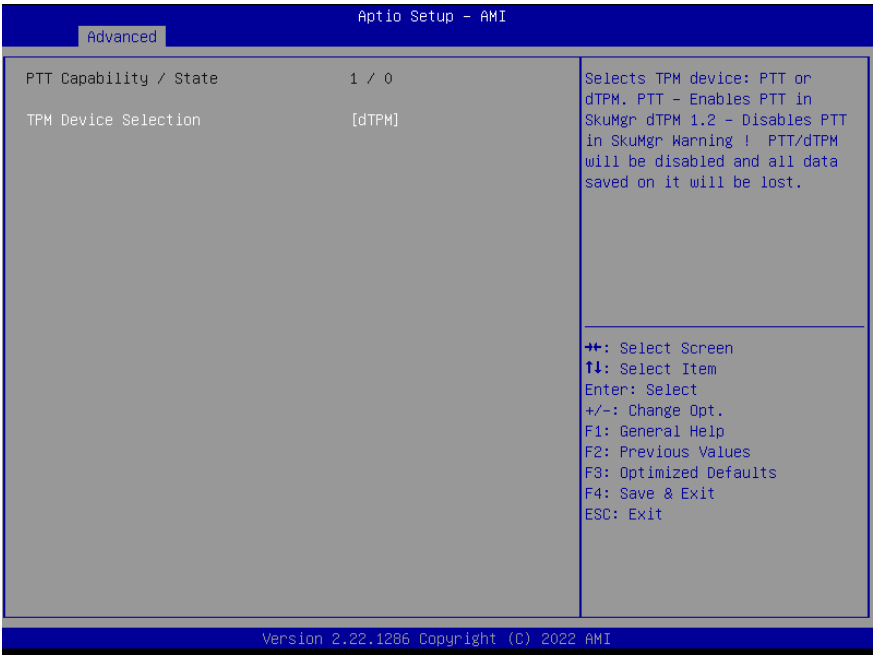


### 3.4.3 Firmware Update Configuration



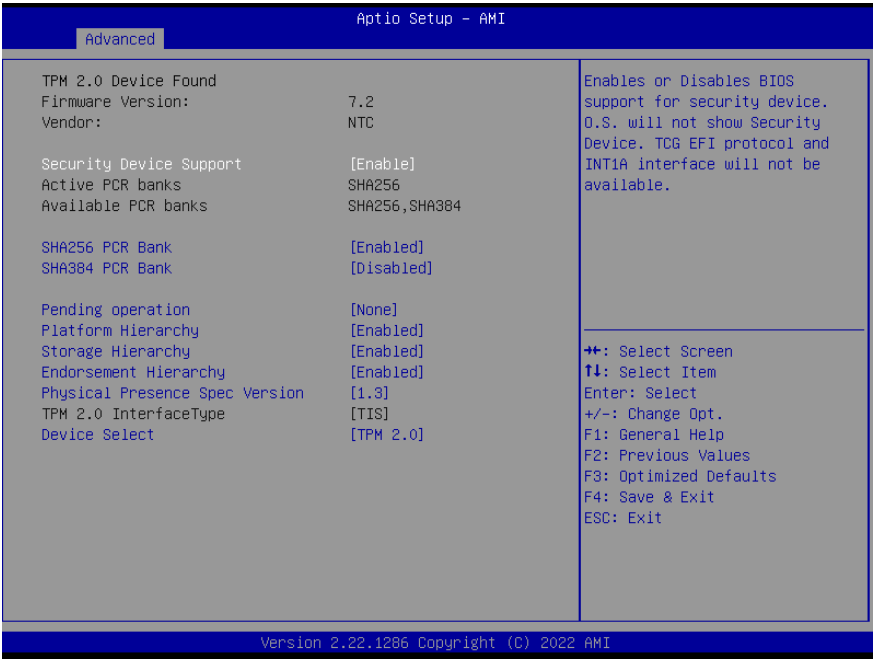

| Options Summary | | |
|---|---|---|
| Me FW Image Re-Flash | Enabled | |
| | Disabled | Optimal Default, Failsafe Default |
| Enable/Disable Me FW Image Re-Flash function. | | |
| FW Update | Disabled | |
| | Enabled | Optimal Default, Failsafe Default |
| Enable/Disable ME FW Update function. | | |

### 3.4.4 PTT Configuration




| Options Summary | | |
|---|---|---|
| TPM Device Selection | dTPM | Optimal Default, Failsafe Default |
| | PTT | |
| Selects TPM device: PTT or discrete TPM.<br>PTT - enables PTT in SkuMgr.<br>dTPM - disables PTT is SkuMgr.<br>**Warning!** PTT/dTPM will be disabled and all data saved on it will be lost. | | |

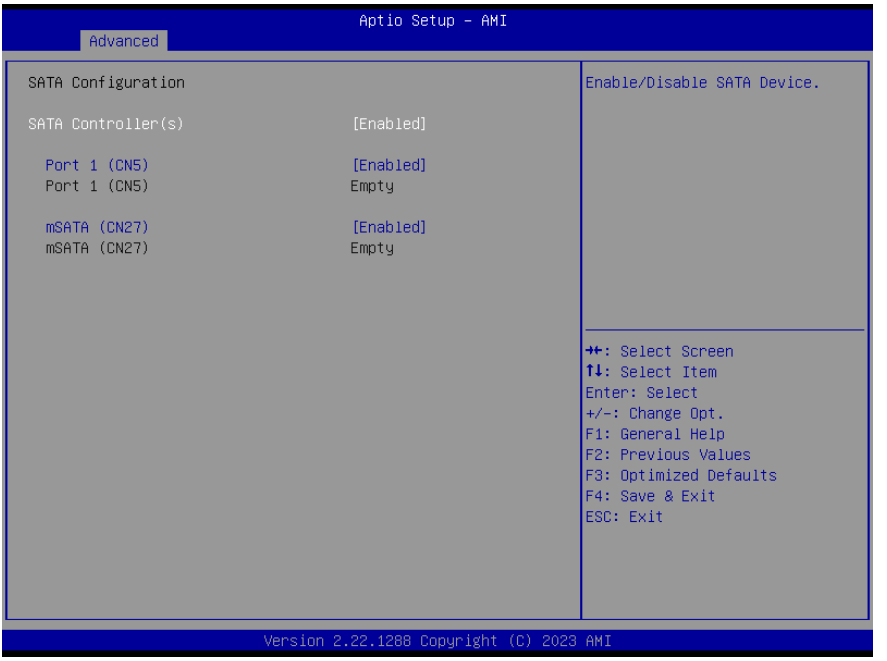
## 3.4.5 Trusted Computing




| Options Summary | | |
|---|---|---|
| **Security Device Support** | Enable | Optimal Default, Failsafe Default |
| | Disable | |
| Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available. | | |
| **SHA256 PCR Bank** | Enabled | Optimal Default, Failsafe Default |
| | Disabled | |
| Enable or Disable SHA256 PCR Bank. | | |
| **SHA384 PCR Bank** | Enabled | Optimal Default, Failsafe Default |
| | Disabled | |
| Enable or Disable SHA384 PCR Bank. | | |
| **Pending operation** | None | Optimal Default, Failsafe Default |
| | TPM Clear | |
| Schedule an Operation for the Security Device.<br>**NOTE**: Your Computer will reboot during restart in order to change State of Security Device. | | |

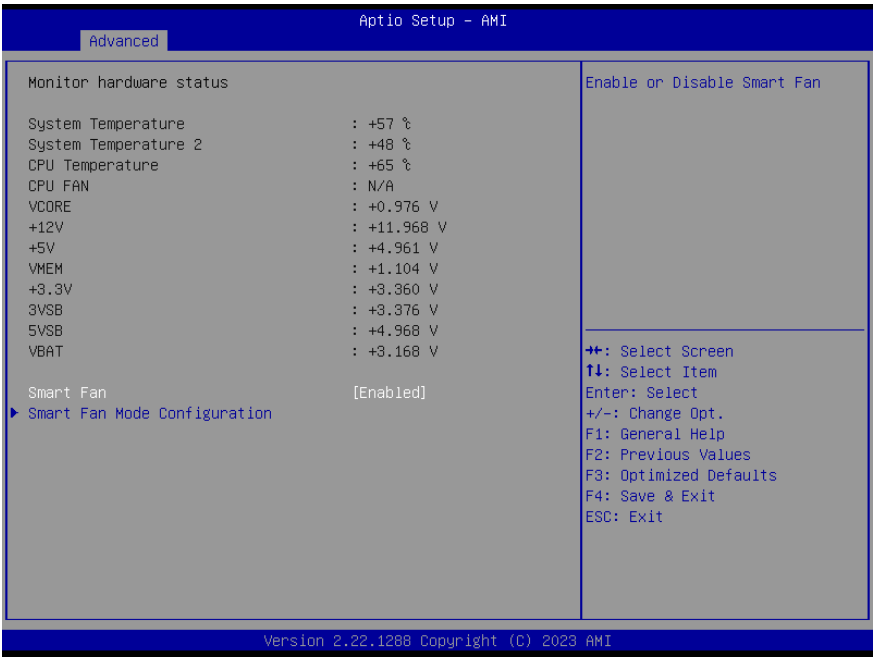
| Options Summary | | |
|---|---|---|
| Platform Hierarchy | Enabled | Optimal Default, Failsafe Default |
| | Disabled | |
| Enable or Disable Platform Hierarchy. | | |
| Storage Hierarchy | Enabled | Optimal Default, Failsafe Default |
| | Disabled | |
| Enable or Disable Storage Hierarchy. | | |
| Endorsement Hierarchy | Enabled | Optimal Default, Failsafe Default |
| | Disabled | |
| Enable or Disable Endorsement Hierarchy. | | |
| Physical Presence Spec Version | 1.3 | Optimal Default, Failsafe Default |
| | 1.2 | |
| Select to Tell O.S. to support PPI Spec Version 1.2 or 1.3. Note some HCK tests might not support 1.3. | | |
| Physical Presence Spec Version | Auto | |
| | TPM 1.2 | |
| | TPM 2.0 | Optimal Default, Failsafe Default |
| TPM 1.2 will restrict support to TPM 1.2 devices.<br>TPM 2.0 will restrict support to TPM 2.0 devices.<br>Auto will support both with the default set to TPM 2.0 devices if not found.<br>TPM 1.2 devices will be enumerated. | | |

## 3.4.6 SATA Configuration



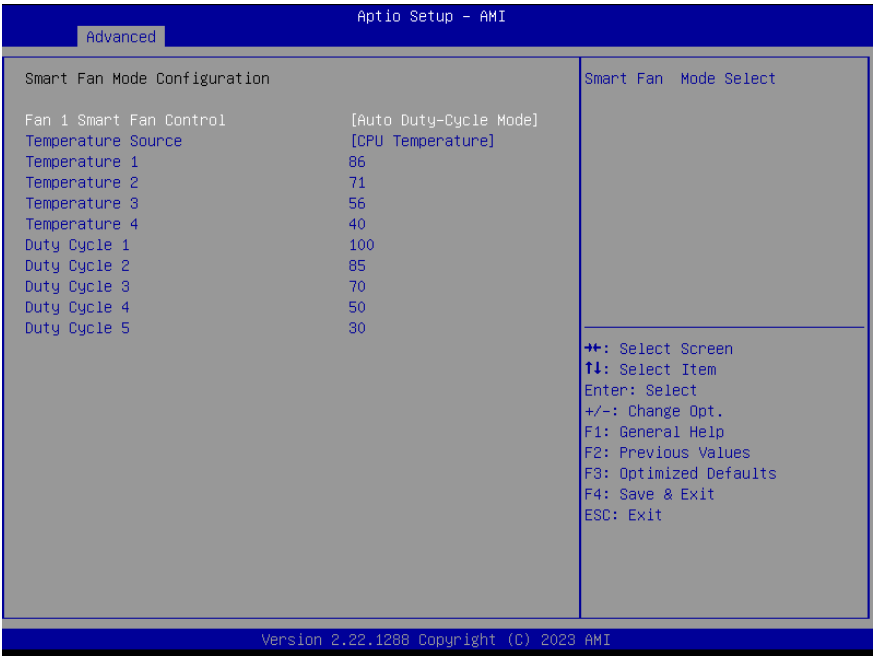

| Options Summary | | |
|---|---|---|
| SATA Controller(s) | Enabled | Optimal Default, Failsafe Default |
| | Disabled | |
| Enable/Disable SATA Device. | | |
| Port 1(CN5) | Enabled | Optimal Default, Failsafe Default |
| | Disabled | |
| Enable or Disable SATA Port. | | |
| mSATA (CN27) | Enabled | Optimal Default, Failsafe Default |
| | Disabled | |
| Enable or Disable SATA Port. | | |

## 3.4.7 Hardware Monitor




| Options Summary | | |
|---|---|---|
| Smart Fan | Disable | |
| | Enable | Optimal Default, Failsafe Default |
| Enables or Disables Smart Fan. | | |

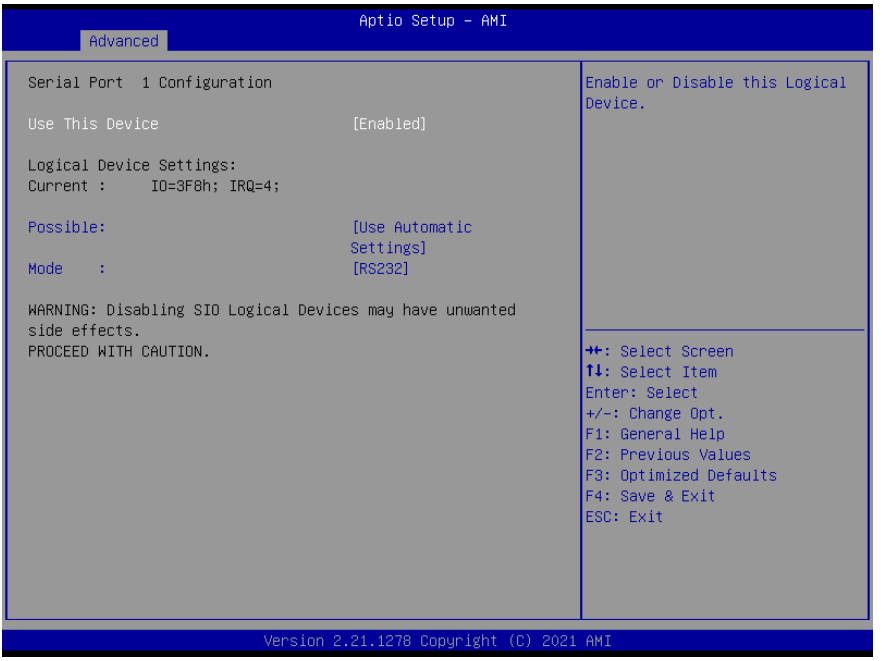
### 3.4.7.1 Smart Fan Mode Configuration




| Options Summary | | |
|---|---|---|
| **Fan 1 Smart Fan Control** | Manual Duty Mode | |
| | Auto Duty-Cycle Mode | Optimal Default, Failsafe Default |
| Smart Fan Mode Select. | | |
| **Temperature Source** | CPU Temperature | Optimal Default, Failsafe Default |
| | System Temperature | |
| | System Temperature 2 | |
| Select the monitored temperature source for this fan. | | |
| **Temperature 1** | 86 | |
| **Duty Cycle 1** | 100 | |
| Auto fan speed control. Fan speed will follow different temperature by different duty cycle 1-100. | | |

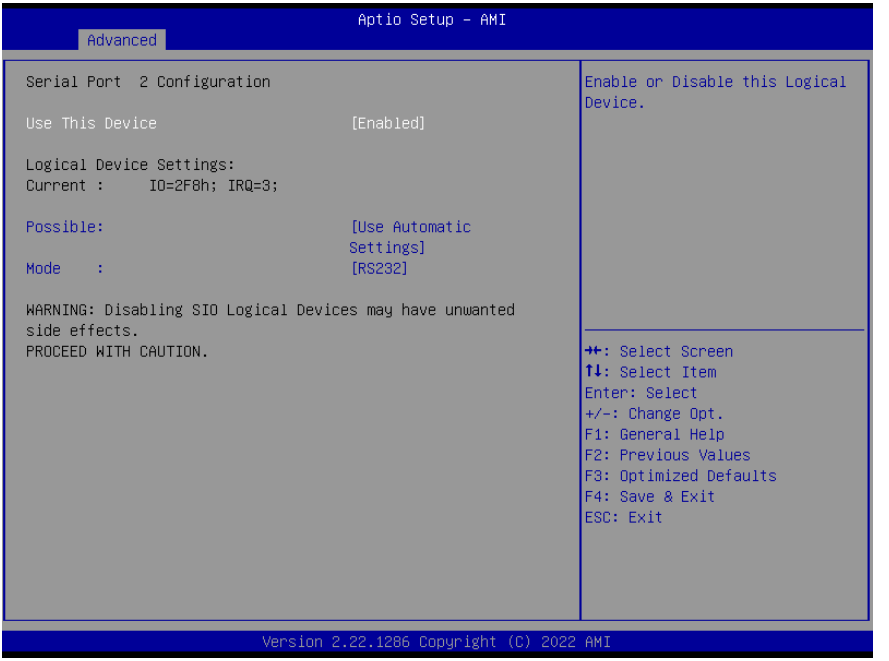
### 3.4.8 SIO Configuration

### 3.4.8.1 Serial Port 1 Configuration



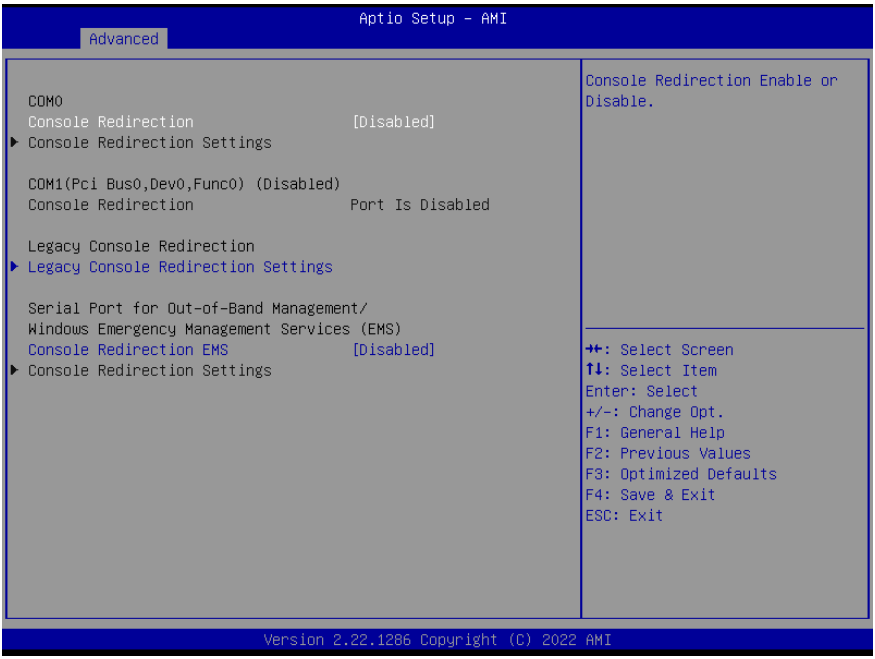

| Options Summary | | |
|---|---|---|
| **Use This Device** | Disable | |
| | Enable | Optimal Default, Failsafe Default |
| Enable or Disable this Logical Device. | | |
| **Possible:** | Use Automatic Settings | Optimal Default, Failsafe Default |
| | IO=3F8h; IRQ=4 | |
| | IO=2F8h; IRQ=3 | |
| Allows user to change Device's Resource settings. New settings will be reflected on This Setup Page after System restarts. | | |
| **Mode:** | RS232 | Optimal Default, Failsafe Default |
| | RS422 | |
| | RS485 | |
| UART RS232, 422, 485 selection. | | |

### 3.4.8.2 Serial Port 2 Configuration




| Options Summary | | |
|---|---|---|
| Use This Device | Disable | |
| | Enable | Optimal Default, Failsafe Default |
| Enable or Disable this Logical Device. | | |
| Possible: | Use Automatic Settings | Optimal Default, Failsafe Default |
| | IO=2F8h; IRQ=3 | |
| | IO=3F8h; IRQ=4 | |
| Allows user to change Device's Resource settings. New settings will be reflected on This Setup Page after System restarts. | | |
| Mode: | RS232 | Optimal Default, Failsafe Default |
| | RS422 | |
| | RS485 | |
| UART RS232, 422, 485 selection. | | |

## 3.4.9 Serial Port Console Redirection

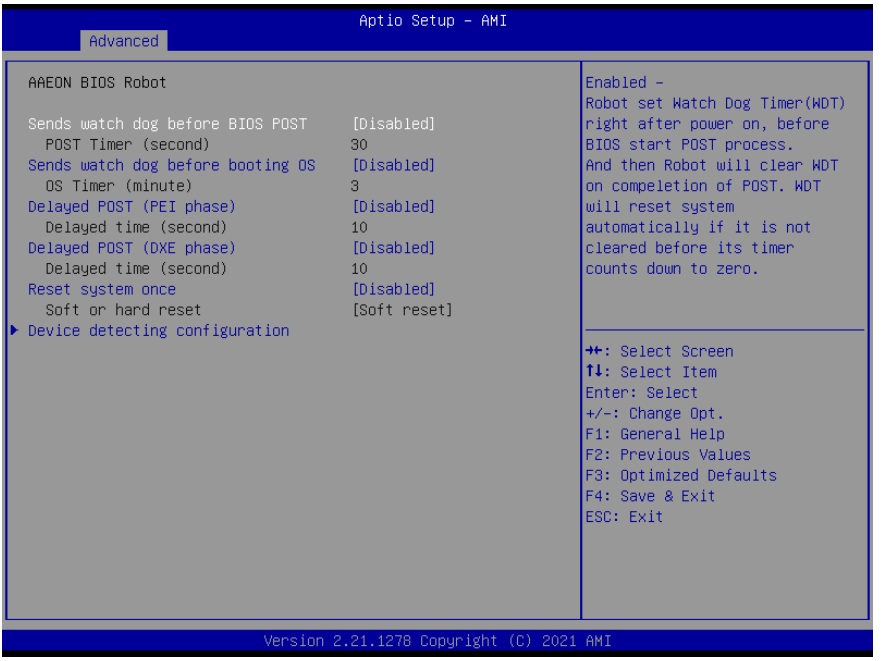



| Options Summary | | |
|---|---|---|
| Console Redirection | Disabled | Optimal Default, Failsafe Default |
| | Enabled | |
| Console Redirection Enable or Disable. | | |
| Console Redirection EMS | Disabled | Optimal Default, Failsafe Default |
| | Enabled | |
| Console Redirection Enable or Disable. | | |

## 3.4.10 Legacy Console Redirection Settings


| Options Summary | | |
|---|---|---|
| Redirection COM port | COM0 | Optimal Default, Failsafe Default |
| | COM1(Pci Bus0, Dev0, Func0) (Disabled) | |
| Select a COM Port to display redirection of Legacy OS and Legacy OPROM message. | | |
| Resolution | 80x24 | Optimal Default, Failsafe Default |
| | 80x25 | |
| On Legacy OS, the number of Rows and Columns supported redirection. | | |
| Redirect After POST | Always Enable | Optimal Default, Failsafe Default |
| | BootLoader | |
| When Bootloader is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When Always Enable is selected, then Legacy Console Redirection is enabled for legacy OS. Default setting for this option is set to Always Enable. | | |

## 3.4.11 AAEON BIOS Robot



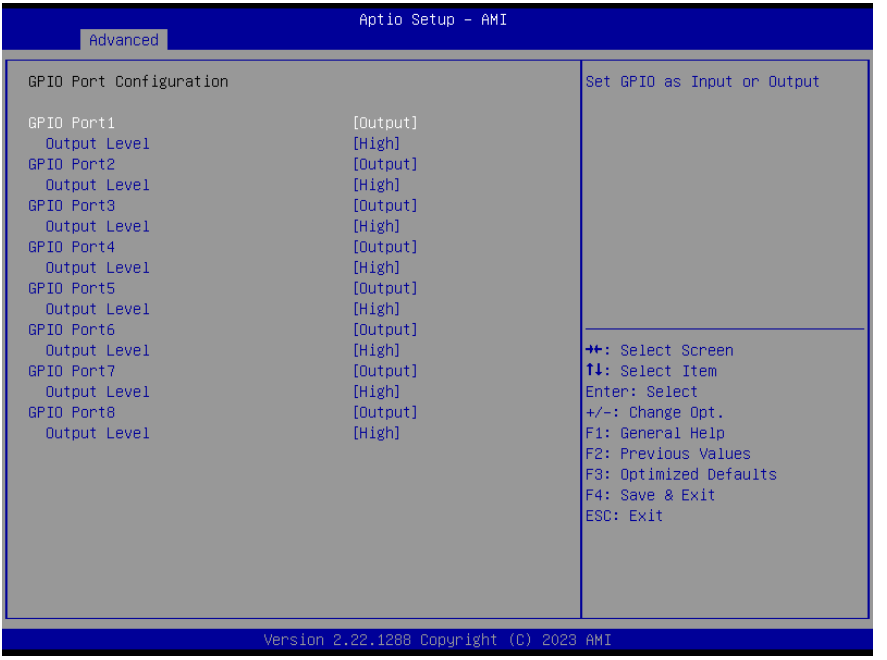

| Options Summary | | |
|---|---|---|
| Sends watch dog before BIOS POST | Disabled | Optimal Default, Failsafe Default |
| | Enabled | |
| Enabled - Robot set Watch Dog Timer (WDT) right after power on, before BIOS start POST process. And then Robot will clear WDT on completion of POST. WDT will reset system automatically if it is not cleared before its timer counts down to zero. | | |
| POST Timer (second) | 30 | Optimal Default, Failsafe Default |
| Timer count set to Watch Dog Timer for POST. **WARNING**: Do not set to a value equal or shorter than normal POST time, otherwise system may never complete POST unless clearing BIOS settings. More than 2x normal POST time is suggested. | | |
| Sends watch dog before booting OS | Disabled | Optimal Default, Failsafe Default |
| | Enabled | |
| Enabled - Robot set Watch Dog Timer (WDT) after POST completion, before BIOS transfer control to OS. **WARNING**: Before enabling this function, a program in OS must be in responsible for clearing WDT. Also, this function should be disabled if OS is going to update itself. | | |

| Options Summary | | |
|---|---|---|
| OS Timer (minute) | 3 | Optimal Default, Failsafe Default |
| Timer count set to Watch Dog Timer for OS loading. | | |
| Delayed POST (PEI phase) | Disabled | Optimal Default, Failsafe Default |
| | Enabled | |
| Enabled - Robot holds BIOS from starting POST, right after power on. This allows BIOS POST to start with stable power or start after system is physically warmed-up.<br>Note: Robot does this before 'Sends watch dog'. | | |
| Delayed time (second) | 10 | Optimal Default, Failsafe Default |
| Period of time for Robot to hold BIOS from POST. | | |
| Delayed POST (DXE phase) | Disabled | Optimal Default, Failsafe Default |
| | Enabled | |
| Enabled - Robot holds BIOS before POST completion. This allows BIOS POST to start with stable power or start after system is physically warmed-up.<br>Note: Robot does this after 'Sends watch dog before BIOS POST'. | | |
| Delayed time (second) | 10 | Optimal Default, Failsafe Default |
| Period of time for Robot to hold BIOS from POST. | | |
| Reset system once | Disabled | Optimal Default, Failsafe Default |
| | Enabled | |
| Enabled - Robot resets system for one time on each boot. This will send a soft or hard reset to onboard devices, thus puts devices to more stable state. | | |
| Soft or hard reset | Soft reset | Optimal Default, Failsafe Default |
| | Hard reset" | |
| Select reset type robot should send on each boot. | | |

## 3.4.12 Power Management

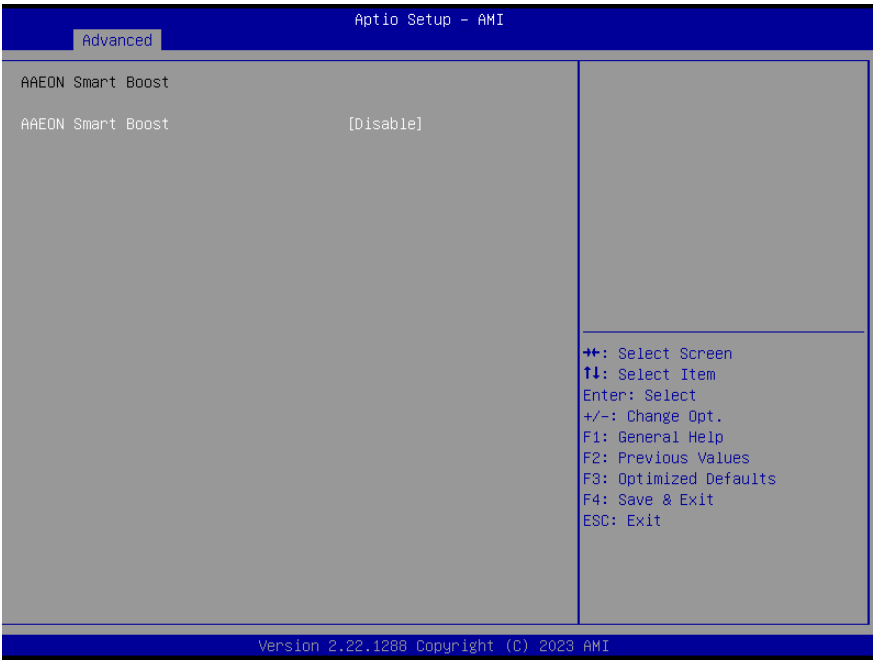

| Options Summary | | |
|---|---|---|
| Power Mode | ATX Type | Optimal Default, Failsafe Default |
| | AT Type | |
| Select power supply mode. | | |
| Restore AC Power Loss | Last State | Optimal Default, Failsafe Default |
| | Always On | |
| | Always Off | |
| Select power state when power is re-applied after a power failure. | | |
| Soft-Off (S5) Wake On RTC | Disable | Optimal Default, Failsafe Default |
| | By Date | |
| | By Weekday | |
| | Bypass | |
| By Date: System will wake on the with hr::min::sec specified. By Weekday: System will wake on the enabled weekday with hr::min::sec specified. Bypass: BIOS will not control RTC wake function. | | |

### 3.4.13 GPIO Port Configuration




| Options Summary | | |
|---|---|---|
| GPIO Port* | Output | |
| | Input | |
| Set GPIO as Input or Output. | | |
| Output Level | High | |
| | Low | |
| Set output level when GPIO pin is Output. | | |

### 3.4.14 AAEON Smart Boost



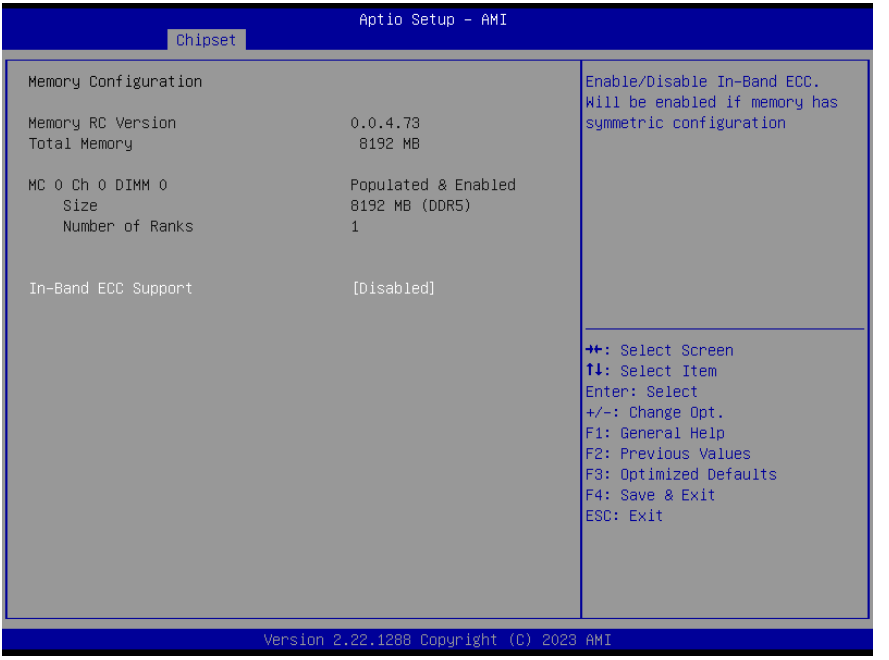

| Options Summary | | |
|---|---|---|
| AAEON Smart Boost | Smart Boost | |
| | Maximum Performance | |
| | Good Stability | |
| | Disabled | Optimal Default, Failsafe Default |
| | | |

## 3.5 Setup Submenu: Chipset

### 3.5.1 System Agent (SA) Configuration


| Options Summary | | |
|---|---|---|
| VT-d | Disabled | Optimal Default, Failsafe Default |
| | Enabled | |
| VT-d capability. | | |

## 3.5.2 Memory Configuration

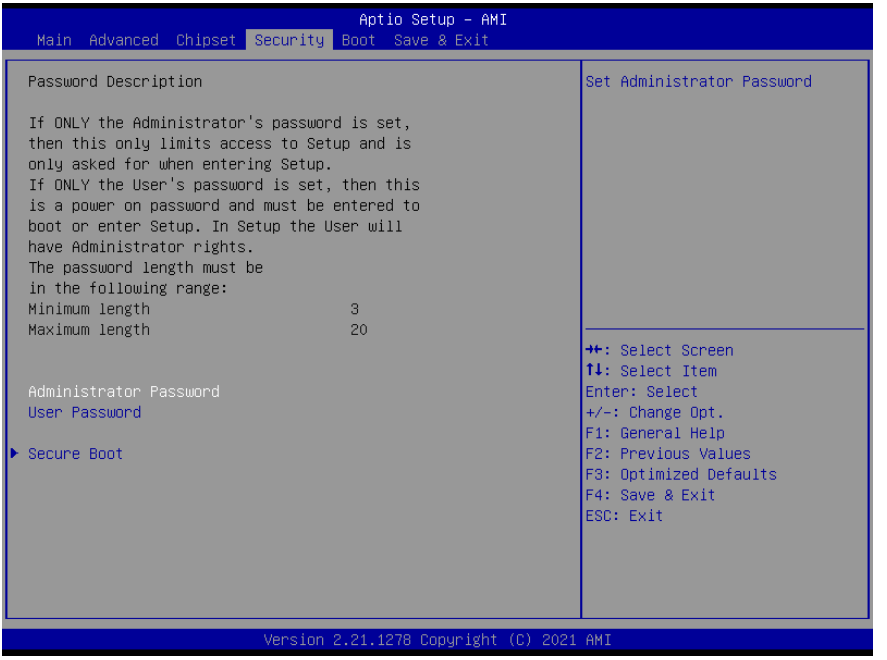



| Options Summary | | |
|---|---|---|
| In-Band ECC Support | Disabled | Optimal Default, Failsafe Default |
| | Enabled | |
| Enable/Disable In-Band ECC. Will be enabled if memory has symmetric configuration. | | |

### 3.5.3 PCH-IO Configuration


| Options Summary | | |
|---|---|---|
| HD Audio | Disabled | |
| | Enabled | Optimal Default, Failsafe Default |
| Control Detection of the HD-Audio device. Disable = HAD will be unconditionally disabled Enable = HAD will be unconditionally enabled. | | |
| Full-Mini Card Slot Function (CN27) | SATA | Optimal Default, Failsafe Default |
| | PCIe | |
| Select function enabled for Full-Mini Card (CN27) slot. | | |

## 3.6 Setup Submenu: Security




### Change User/Supervisor Password

You can install a Supervisor password, and if you install a supervisor password, you can then install a user password. A user password does not provide access to many of the features in the Setup utility.

If you highlight these items and press Enter, a dialog box appears which lets you enter a password. You can enter no more than six letters or numbers. Press Enter after you have typed in the password. A second dialog box asks you to retype the password for confirmation. Press Enter after you have retyped it correctly. The password is required at boot time, or when the user enters the Setup utility.

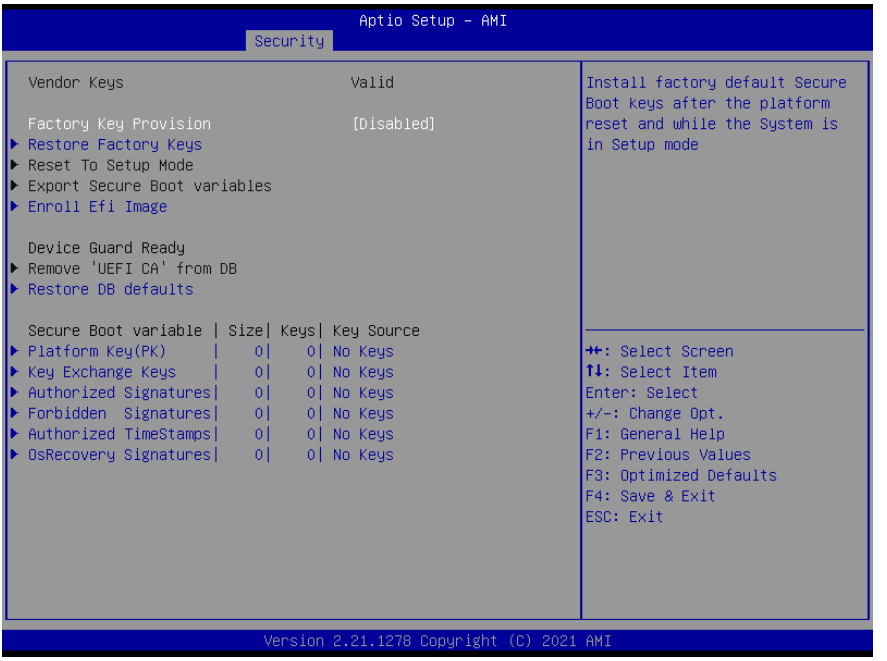
### Removing the Password

Highlight this item and type in the current password. At the next dialog box press Enter to disable password protection.

### 3.6.1 Secure Boot


| Options Summary | | |
|---|---|---|
| Secure Boot | Disabled | Optimal Default, Failsafe Default |
| | Enabled | |
| Secure Boot feature is Active if Secure Boot is Enabled, Platform Key (PK) is enrolled and the System is in User mode. The mode change requires platform reset. | | |
| Secure Boot Mode | Custom | Optimal Default, Failsafe Default |
| | Standard | |
| Secure Boot mode options: Standard or Custom.<br>In Custom mode, Secure Boot Policy variables can be configured by a physically present user without full authentication. | | |
| Restore Factory Keys | | |
| Force System to User Mode. Install factory default Secure Boot key databases. | | |
| Reset to Setup Mode | | |
| Delete all Secure Boot key databases from NVRAM. | | |

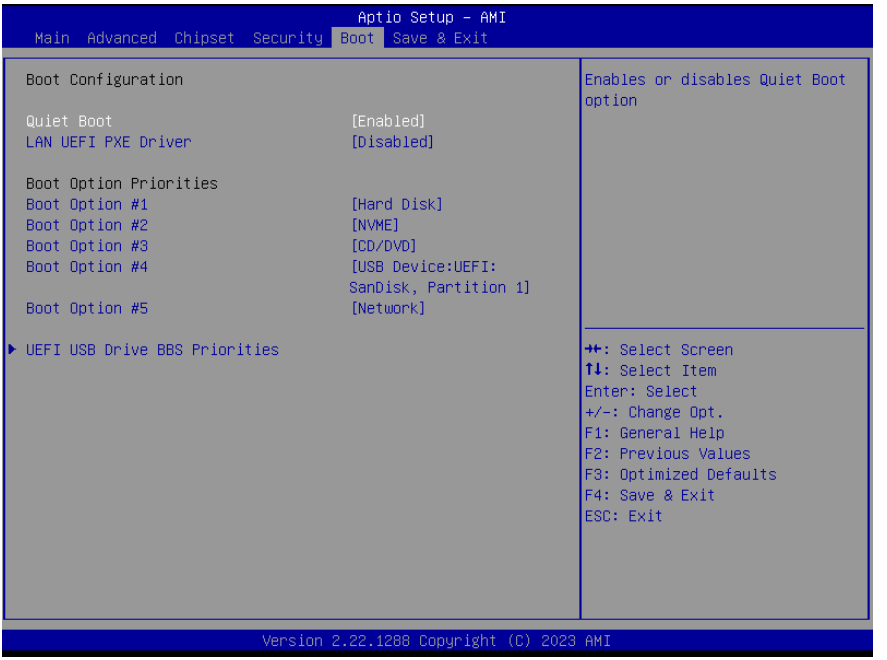
## 3.6.2 Key Management




| Options Summary | | |
|---|---|---|
| Factory Key Provision | Disabled | Optimal Default, Failsafe Default |
| | Enabled | |
| Secure Boot feature is Active if Secure Boot is Enabled, Platform Key (PK) is enrolled and the System is in User mode.<br>The mode change requires platform reset. | | |
| Restore Factory Keys | | |
| Force System to User Mode. Install factory default Secure Boot key databases. | | |
| Reset to Setup Mode | | |
| Delete all Secure Boot key databases from NVRAM. | | |
| Export Secure Boot variables | | |
| Copy NVRAM content of Secure Boot variables to files in a root folder on a file system device. | | |
| Enroll Efi Image | | |
| Allow the image to run in Secure Boot mode.<br>Enroll SHA256 Hash certificate of a PE image into Authorized Signature Database (db). | | |

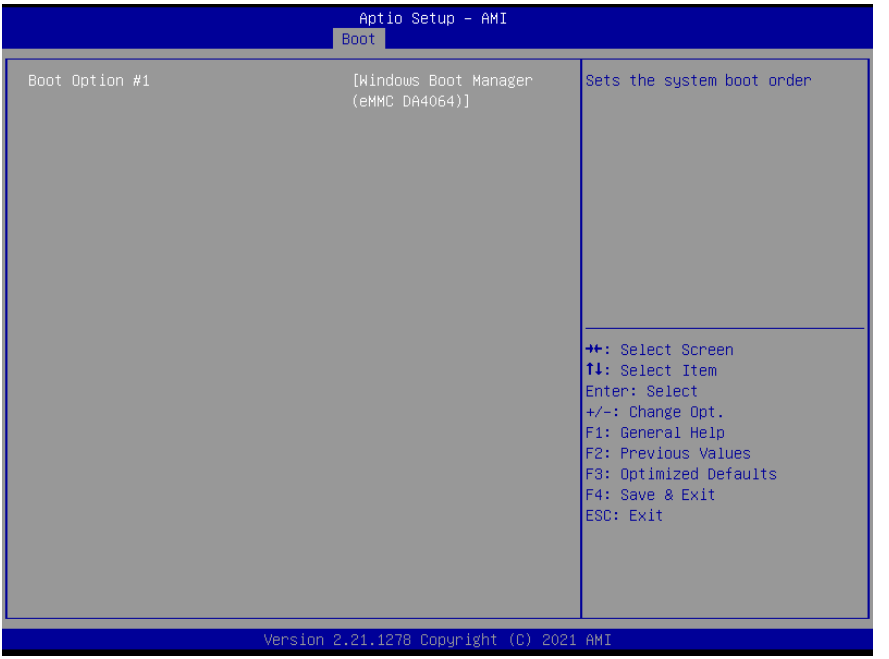
| Options Summary | |
|---|---|
| Remove 'UEFI CA' from DB | |
| Device Guard ready system must not list 'Microsoft UEFI CA' Certificate in Authorized Signature database (db). | |
| Restore DB defaults | |
| Restore DB variable to factory defaults. | |
| Platform Key (PK) | Details |
| | Export |
| | Update |
| | Delete |
| Key Exchange Keys | Details |
| | Export |
| | Update |
| | Append |
| | Delete |
| Authorized Signatures | Details |
| | Export |
| | Update |
| | Append |
| | Delete |
| Forbidden Signatures | Details |
| | Export |
| | Update |
| | Append |
| | Delete |
| Authorized TimeStamps | Update |
| | Append |
| OsRecovery Signatures | Update |
| | Append |
| Enroll Factory Defaults or load certificates from a file:<br>1.Public Key Certificate:<br>a) EFI_SIGNATURE_LIST<br>b) EFI_CERT_X509 (DER)<br>c) EFI_CERT_RSA2048 (bin)<br>d) EFI_CERT_SHAXXX<br>2.Authenticated UEFI Variable<br>3.EFI PE/COFF Image (SHA256)<br>Key Source: Factory, External, Mixed | |

## 3.7 Setup Submenu: Boot

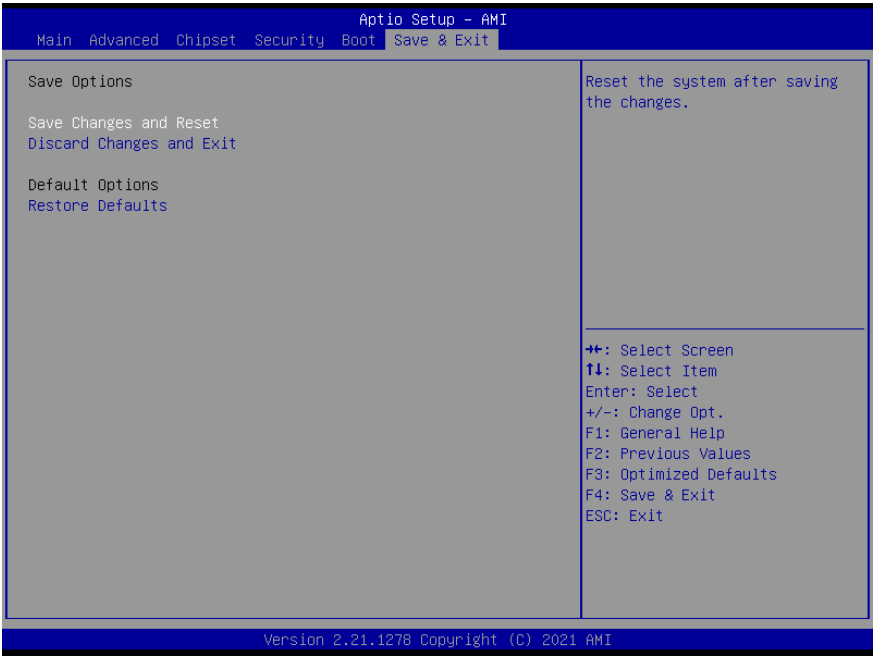



| Options Summary | | |
|---|---|---|
| Quiet Boot | Disabled | |
| | Enabled | Optimal Default, Failsafe Default |
| Enable or Disable Quiet Boot option. | | |
| UEFI PXE Support | Disabled | Optimal Default, Failsafe Default |
| | Enabled | |
| Enable/Disable UEFI Network Stack. | | |
| FIXED BOOT ORDER Priorities | | |
| Sets the system boot order. | | |

### 3.7.1 BBS Priorities



## 3.8 Setup Submenu: Save & Exit




| Options Summary | |
|---|---|
| Save Changes and Reset | Reset the system after saving the changes. |
| Discard Changes and Exit | Exit system setup without saving any changes. |
| Restore Defaults | Restore/Load Default values for all the setup options. |

# Chapter 4

Driver Installation

## 4.1 Driver Download/Installation

Drivers for the GENESYSM-ADN6 can be downloaded from the product page on the AAEON website by following this link:

https://www.aaeon.com/en/

Download the driver(s) you need and follow the steps below to install them.

### Chipset Driver

1. Open the folder where you unzipped the **Chipset Drivers.**
2. Run the **SetupChipset.exe** file in the folder.
3. Follow the instructions.
4. Drivers will be installed automatically.

### Graphics Driver

1. Open the **Graphics Driver** folder
2. Run the **Installer.exe** file in the folder.
3. Follow the instructions.
4. Drivers will be installed automatically.
5. Refer to the ReadMe.txt for any assistance.

### LAN Drivers

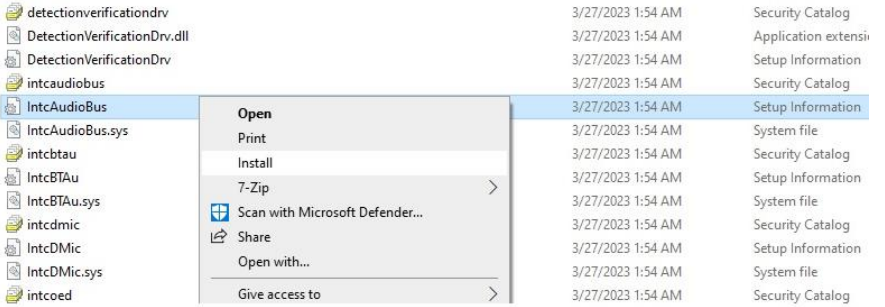
1. Open the **LAN** folder
2. Open the **.exe** file in the folder
3. Follow the instructions
4. Drivers will be installed automatically

## Install Intel Smart Sound Driver

1. Open the **Intel Smart Sound** folder
2. Navigate the folder as follows: **Production** > **Driver**, then follow the below instructions to install the BUS Driver (IntcAudioBus.inf) and OED Driver (IntcOED.inf).

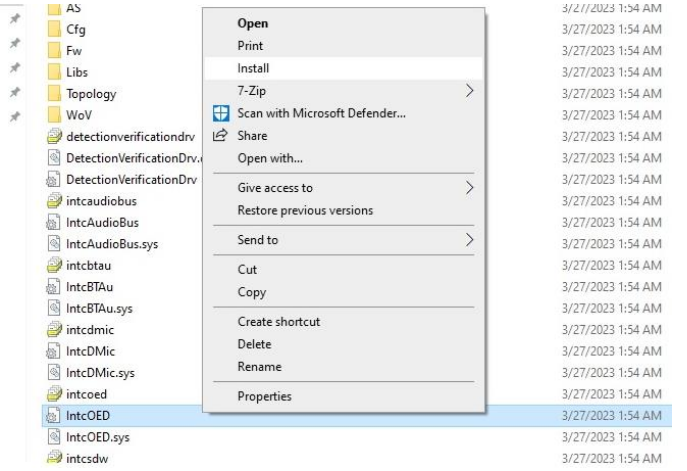
## Install BUS driver (IntcAudioBus.inf)

a. Press Right Key -> Install




## Install OED driver (IntcOED.inf)

b. Press Right Key -> Install



### Install Windows Audio Driver

1. Open the **Windows Audio** folder followed by **Setup.exe**
2. Follow the instructions
3. Drivers will be installed automatically

### Install Peripheral Driver

1. Open the **Peripheral Driver** folder
2. Open the **SetupSerialIO.exe** file
3. Follow the instructions
4. Drivers will be installed automatically

### Install SIO Driver

1. Open the **SIO** folder and select your OS
2. Open the **FintekSerial.exe** file
3. Follow the instructions
4. Drivers will be installed automatically

### Install Intel CSME Driver

1. Open the **Intel CSME** folder and select your OS
2. Open the **SetupME.exe** file
3. Follow the instructions
4. Drivers will be installed automatically

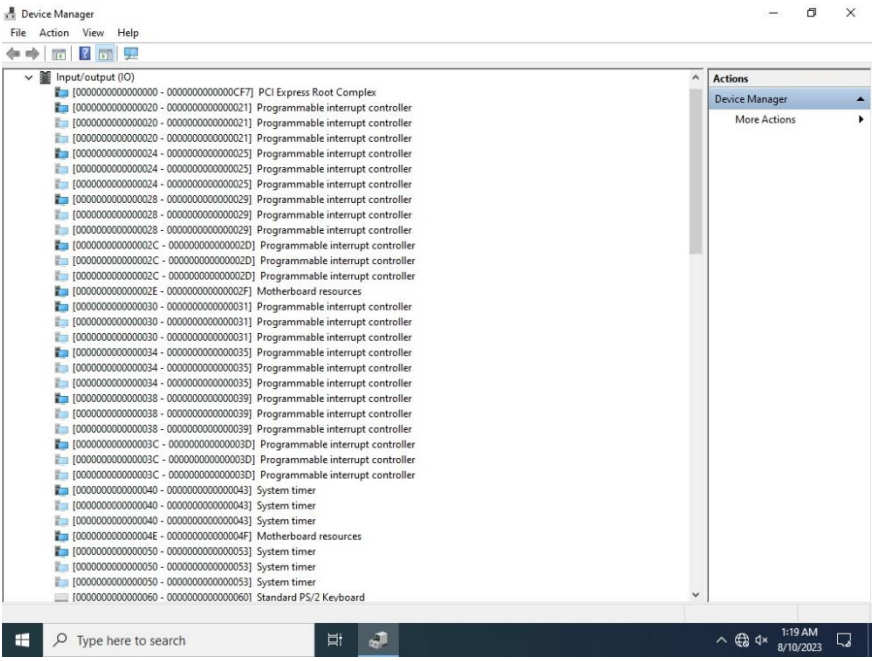
### Install Camera Driver

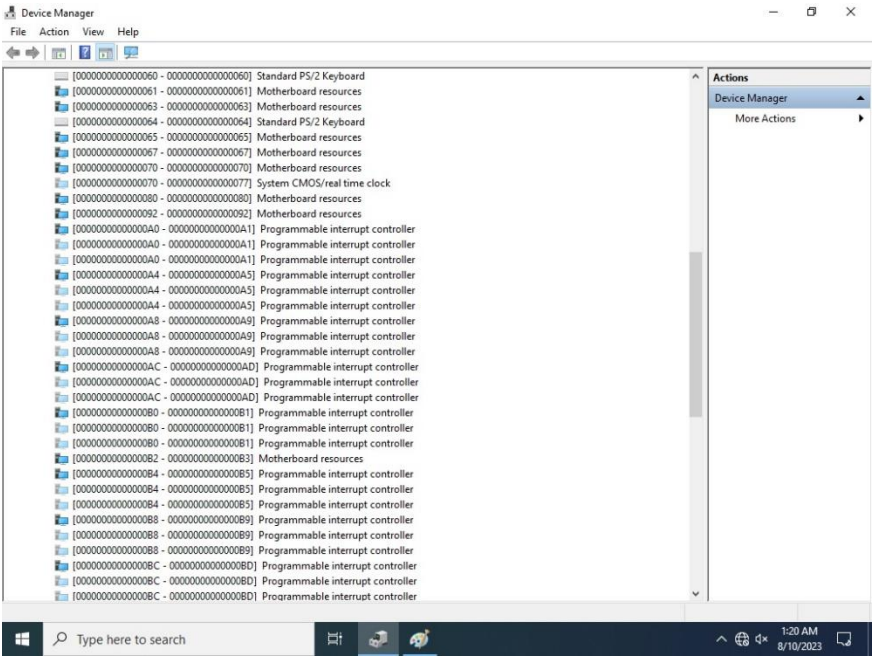
1. Open the **Camera Driver** folder
2. Follow the instructions provided in the **Documentation** subfolder to manually install drivers

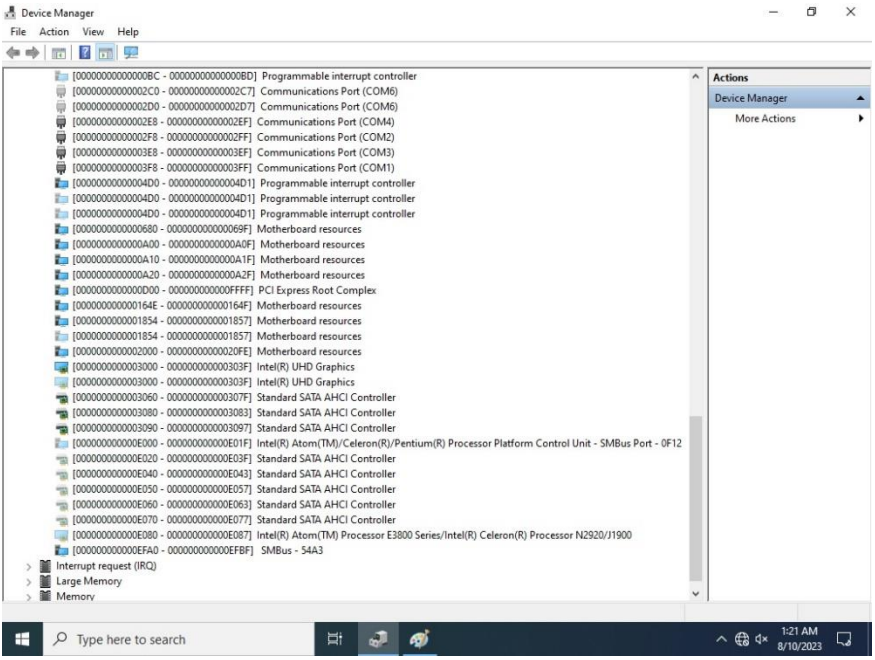
# Appendix A

I/O Information

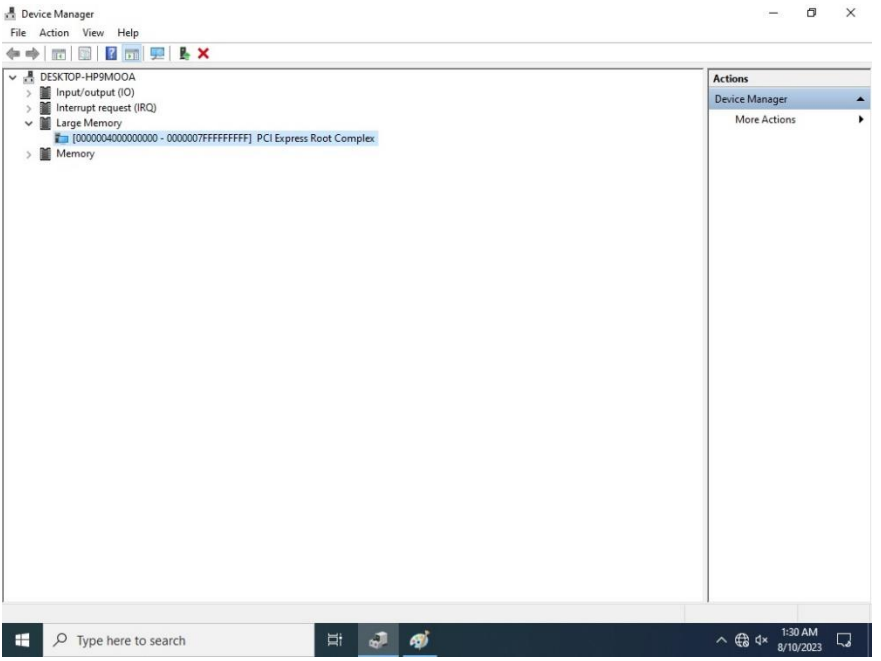
## A.1 I/O Address Map

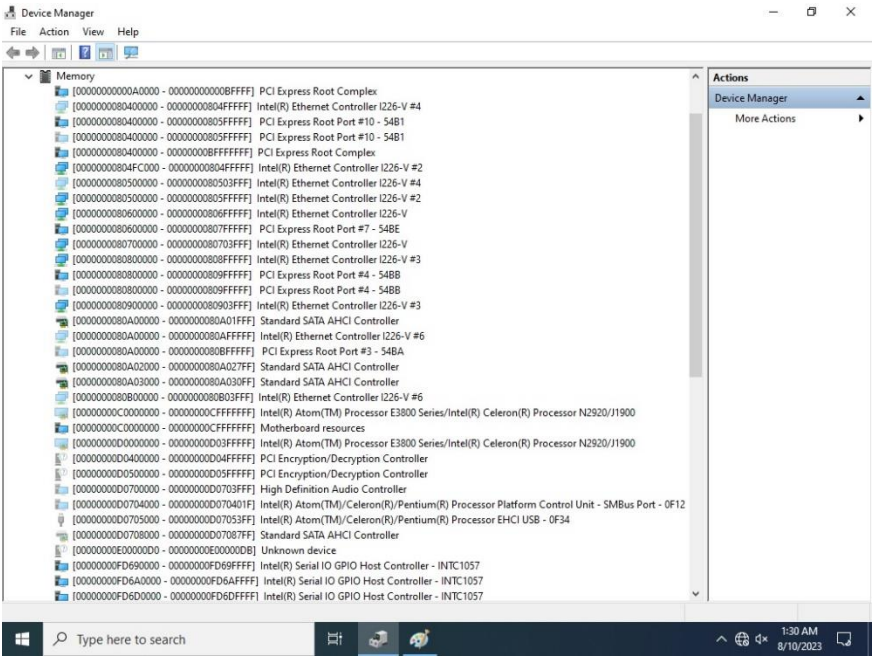



Device Manager
File Action View Help
[0000000000000060 - 0000000000000060] Standard PS/2 Keyboard
[0000000000000061 - 0000000000000061] Motherboard resources
[0000000000000063 - 0000000000000063] Motherboard resources
[0000000000000064 - 0000000000000064] Standard PS/2 Keyboard
[0000000000000065 - 0000000000000065] Motherboard resources
[0000000000000067 - 0000000000000067] Motherboard resources
[0000000000000070 - 0000000000000070] Motherboard resources
[0000000000000070 - 0000000000000077] System CMOS/real time clock
[0000000000000080 - 0000000000000080] Motherboard resources
[0000000000000092 - 0000000000000092] Motherboard resources
[00000000000000A0 - 00000000000000A1] Programmable interrupt controller
[00000000000000A0 - 00000000000000A1] Programmable interrupt controller
[00000000000000A0 - 00000000000000A1] Programmable interrupt controller
[00000000000000A4 - 00000000000000A5] Programmable interrupt controller
[00000000000000A4 - 00000000000000A5] Programmable interrupt controller
[00000000000000A4 - 00000000000000A5] Programmable interrupt controller
[00000000000000A8 - 00000000000000A9] Programmable interrupt controller
[00000000000000A8 - 00000000000000A9] Programmable interrupt controller
[00000000000000A8 - 00000000000000A9] Programmable interrupt controller
[00000000000000AC - 00000000000000AD] Programmable interrupt controller
[00000000000000AC - 00000000000000AD] Programmable interrupt controller
[00000000000000AC - 00000000000000AD] Programmable interrupt controller
[00000000000000B0 - 00000000000000B1] Programmable interrupt controller
[00000000000000B0 - 00000000000000B1] Programmable interrupt controller
[00000000000000B0 - 00000000000000B1] Programmable interrupt controller
[00000000000000B2 - 00000000000000B3] Motherboard resources
[00000000000000B4 - 00000000000000B5] Programmable interrupt controller
[00000000000000B4 - 00000000000000B5] Programmable interrupt controller
[00000000000000B4 - 00000000000000B5] Programmable interrupt controller
[00000000000000B8 - 00000000000000B9] Programmable interrupt controller
[00000000000000B8 - 00000000000000B9] Programmable interrupt controller
[00000000000000B8 - 00000000000000B9] Programmable interrupt controller
[00000000000000BC - 00000000000000BD] Programmable interrupt controller
[00000000000000BC - 00000000000000BD] Programmable interrupt controller
[00000000000000BC - 00000000000000BD] Programmable interrupt controller
Actions
Device Manager
More Actions
Type here to search
1:20 AM
8/10/2023


Device Manager
File Action View Help
[00000000000000BC - 00000000000000BD] Programmable interrupt controller
[00000000000002C0 - 00000000000002C7] Communications Port (COM6)
[00000000000002D0 - 00000000000002D7] Communications Port (COM6)
[00000000000002E8 - 00000000000002EF] Communications Port (COM4)
[00000000000002F8 - 00000000000002FF] Communications Port (COM2)
[00000000000003E8 - 00000000000003EF] Communications Port (COM3)
[00000000000003F8 - 00000000000003FF] Communications Port (COM1)
[00000000000004D0 - 00000000000004D1] Programmable interrupt controller
[00000000000004D0 - 00000000000004D1] Programmable interrupt controller
[00000000000004D0 - 00000000000004D1] Programmable interrupt controller
[0000000000000680 - 000000000000069F] Motherboard resources
[0000000000000A00 - 0000000000000A0F] Motherboard resources
[0000000000000A10 - 0000000000000A1F] Motherboard resources
[0000000000000A20 - 0000000000000A2F] Motherboard resources
[0000000000000D00 - 0000000000000FFFF] PCI Express Root Complex
[000000000000164E - 000000000000164F] Motherboard resources
[0000000000001854 - 0000000000001857] Motherboard resources
[0000000000001854 - 0000000000001857] Motherboard resources
[0000000000002000 - 000000000000020FE] Motherboard resources
[0000000000003000 - 000000000000303F] Intel(R) UHD Graphics
[0000000000003000 - 000000000000303F] Intel(R) UHD Graphics
[0000000000003060 - 000000000000307F] Standard SATA AHCI Controller
[0000000000003080 - 0000000000003083] Standard SATA AHCI Controller
[0000000000003090 - 0000000000003097] Standard SATA AHCI Controller
[000000000000E000 - 000000000000E01F] Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor Platform Control Unit - SMBus Port - 0F12
[000000000000E020 - 000000000000E03F] Standard SATA AHCI Controller
[000000000000E040 - 000000000000E043] Standard SATA AHCI Controller
[000000000000E050 - 000000000000E057] Standard SATA AHCI Controller
[000000000000E060 - 000000000000E063] Standard SATA AHCI Controller
[000000000000E070 - 000000000000E077] Standard SATA AHCI Controller
[000000000000E080 - 000000000000E087] Intel(R) Atom(TM) Processor E3800 Series/Intel(R) Celeron(R) Processor N2920/J1900
[000000000000EFA0 - 000000000000EFBF] SMBus - 54A3
Interrupt request (IRQ)
Large Memory
Memory
Actions
Device Manager
More Actions
Type here to search
1:21 AM
8/10/2023

## A.2 Memory Address Map




Device Manager
File Action View Help
Memory
[00000000000A0000 - 00000000000BFFFF] PCI Express Root Complex
[0000000080400000 - 0000000080804FFFFF] Intel(R) Ethernet Controller I226-V #4
[0000000080400000 - 0000000080805FFFFF] PCI Express Root Port #10 - 54B1
[0000000080400000 - 0000000080805FFFFF] PCI Express Root Port #10 - 54B1
[0000000080400000 - 00000000BFFFFFFFF] PCI Express Root Complex
[0000000080400000 - 0000000080804FC000] Intel(R) Ethernet Controller I226-V #2
[0000000080500000 - 0000000080503FFFF] Intel(R) Ethernet Controller I226-V #4
[0000000080500000 - 0000000080805FFFFF] Intel(R) Ethernet Controller I226-V #2
[0000000080600000 - 0000000080806FFFFF] Intel(R) Ethernet Controller I226-V
[0000000080600000 - 0000000080807FFFFF] PCI Express Root Port #7 - 54BE
[0000000080700000 - 0000000080703FFFF] Intel(R) Ethernet Controller I226-V
[0000000080800000 - 0000000080808FFFFF] Intel(R) Ethernet Controller I226-V #3
[0000000080800000 - 0000000080809FFFFF] PCI Express Root Port #4 - 54BB
[0000000080800000 - 0000000080809FFFFF] PCI Express Root Port #4 - 54BB
[0000000080900000 - 0000000080903FFFF] Intel(R) Ethernet Controller I226-V #3
[000000080A00000 - 000000080A01FFF] Standard SATA AHCI Controller
[000000080A00000 - 000000080AFFFFF] Intel(R) Ethernet Controller I226-V #6
[000000080A00000 - 000000080BFFFFF] PCI Express Root Port #3 - 54BA
[000000080A02000 - 000000080A027FF] Standard SATA AHCI Controller
[000000080A03000 - 000000080A030FF] Standard SATA AHCI Controller
[000000080B00000 - 000000080B03FFF] Intel(R) Ethernet Controller I226-V #6
[00000000C0000000 - 00000000CFFFFFFF] Intel(R) Atom(TM) Processor E3800 Series/Intel(R) Celeron(R) Processor N2920//J1900
[00000000C0000000 - 00000000CFFFFFFF] Motherboard resources
[00000000D0000000 - 00000000D03FFFFF] Intel(R) Atom(TM) Processor E3800 Series/Intel(R) Celeron(R) Processor N2920//J1900
[00000000D0400000 - 00000000D04FFFFF] PCI Encryption/Decryption Controller
[00000000D0500000 - 00000000D05FFFFF] PCI Encryption/Decryption Controller
[00000000D0700000 - 00000000D0703FFF] High Definition Audio Controller
[00000000D0704000 - 00000000D070401F] Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor Platform Control Unit - SMBus Port - 0F12
[00000000D0705000 - 00000000D07053FF] Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor EHCI USB - 0F34
[00000000D0708000 - 00000000D07087FF] Standard SATA AHCI Controller
[00000000E00000D0 - 00000000E00000DB] Unknown device
[00000000FD690000 - 00000000FD69FFFF] Intel(R) Serial IO GPIO Host Controller - INTC1057
[00000000FD6A0000 - 00000000FD6AFFFF] Intel(R) Serial IO GPIO Host Controller - INTC1057
[00000000FD6D0000 - 00000000FD6DFFFF] Intel(R) Serial IO GPIO Host Controller - INTC1057
Actions
Device Manager
More Actions
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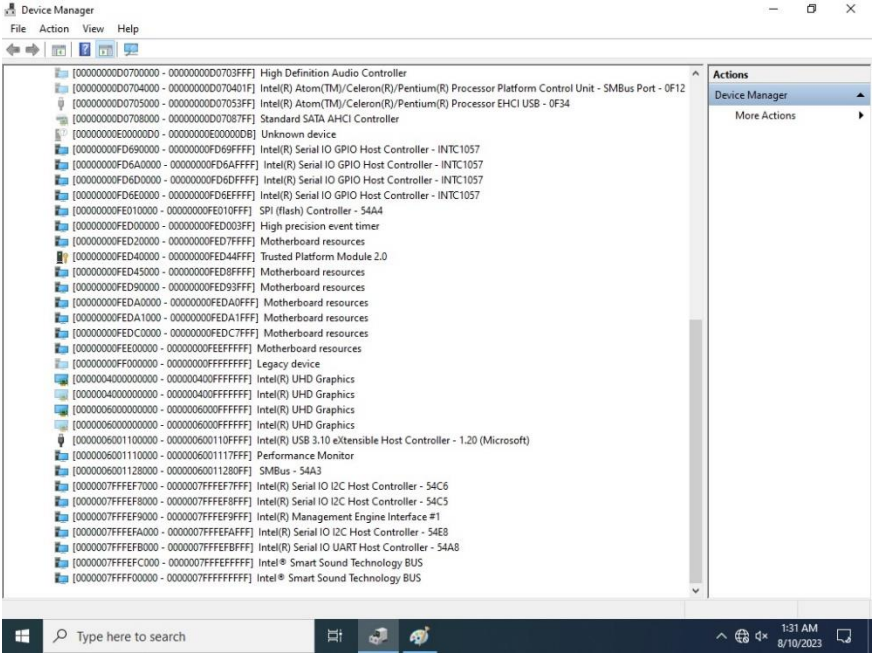
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File Action View Help
[00000000D0700000 - 00000000D0703FFF] High Definition Audio Controller
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[00000000D0705000 - 00000000D07053FF] Intel(R) Atom(TM)/Celeron(R)/Pentium(R) Processor EHCI USB - 0F34
[00000000D0708000 - 00000000D07087FF] Standard SATA AHCI Controller
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[00000000FE010000 - 00000000FE010FFF] SPI (flash) Controller - 54A4
[00000000FED00000 - 00000000FED003FF] High precision event timer
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[00000000FED40000 - 00000000FED44FFF] Trusted Platform Module 2.0
[00000000FED45000 - 00000000FED8FFFF] Motherboard resources
[00000000FED90000 - 00000000FED93FFF] Motherboard resources
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[00000000FEDA1000 - 00000000FEDA1FFF] Motherboard resources
[00000000FEDC0000 - 00000000FEDC7FFF] Motherboard resources
[00000000FEE00000 - 00000000FEEFFFFF] Motherboard resources
[00000000FF000000 - 00000000FFFFFFFF] Legacy device
[0000004000000000 - 000000400FFFFFFF] Intel(R) UHD Graphics
[0000004000000000 - 000000400FFFFFFF] Intel(R) UHD Graphics
[0000006000000000 - 000000600FFFFFFF] Intel(R) UHD Graphics
[0000006000000000 - 000000600FFFFFFF] Intel(R) UHD Graphics
[0000006001100000 - 000000600110FFFF] Intel(R) USB 3.10 eXtensible Host Controller - 1.20 (Microsoft)
[0000006001110000 - 000000600117FFFF] Performance Monitor
[0000006001128000 - 000000600128 0FF] SMBus - 54A3
[0000007FFFEF7000 - 0000007FFFEF7FFF] Intel(R) Serial IO I2C Host Controller - 54C6
[0000007FFFEF8000 - 0000007FFFEF8FFF] Intel(R) Serial IO I2C Host Controller - 54C5
[0000007FFFEF9000 - 0000007FFFEF9FFF] Intel(R) Management Engine Interface #1
[0000007FFFEFA000 - 0000007FFFEFAFFF] Intel(R) Serial IO I2C Host Controller - 54E8
[0000007FFFEFB000 - 0000007FFFEFBFFF] Intel(R) Serial IO UART Host Controller - 54A8
[0000007FFFEFC000 - 0000007FFFFFFFFF] Intel® Smart Sound Technology BUS
[0000007FFFF00000 - 0000007FFFFFFFFF] Intel® Smart Sound Technology BUS
Actions
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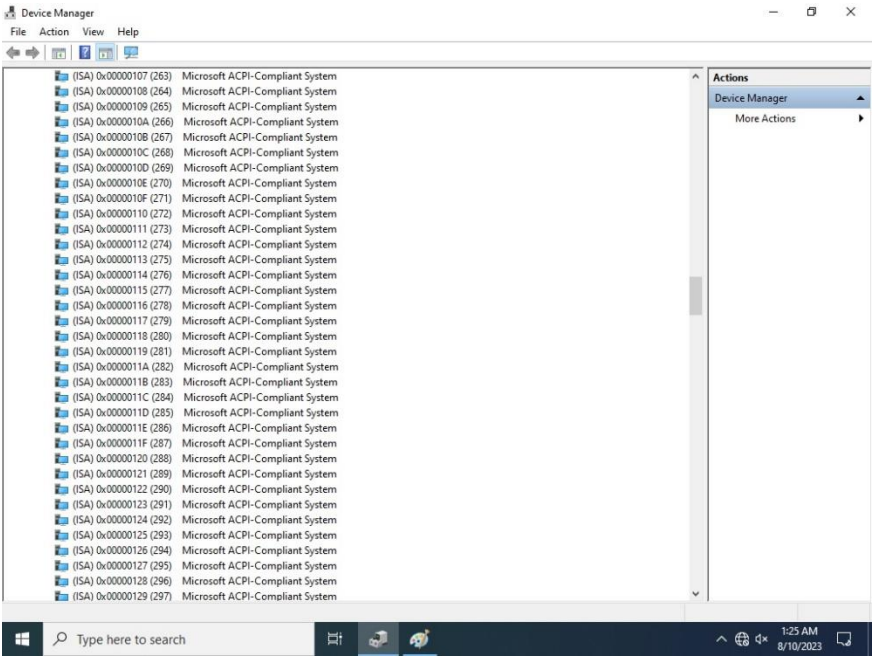
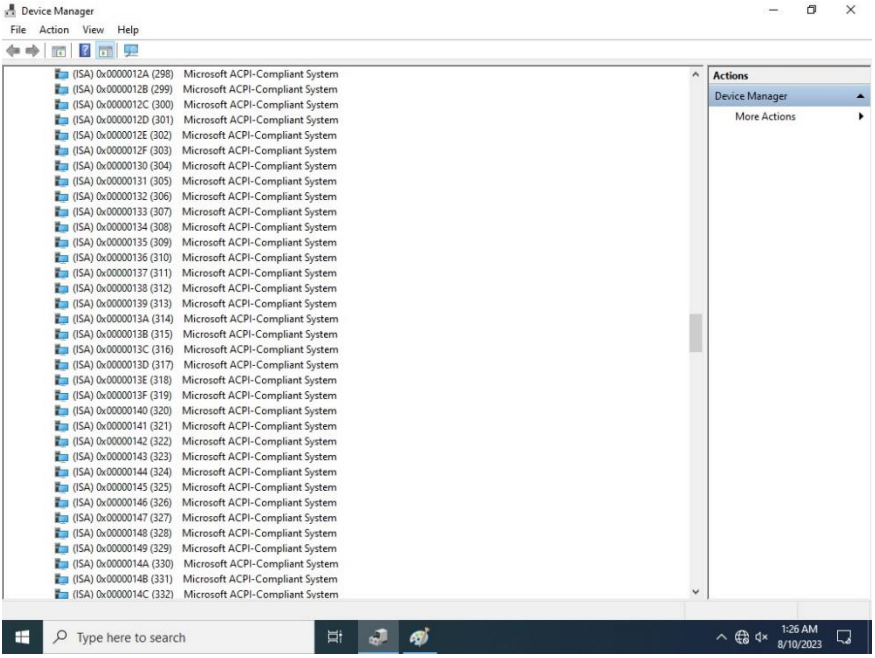
## A.3 IRQ Mapping Chart

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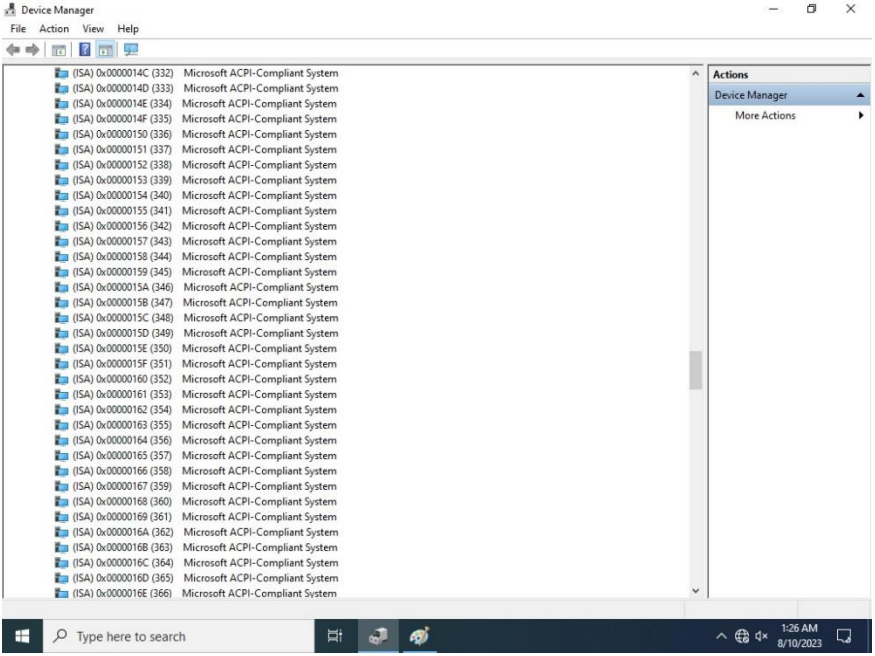
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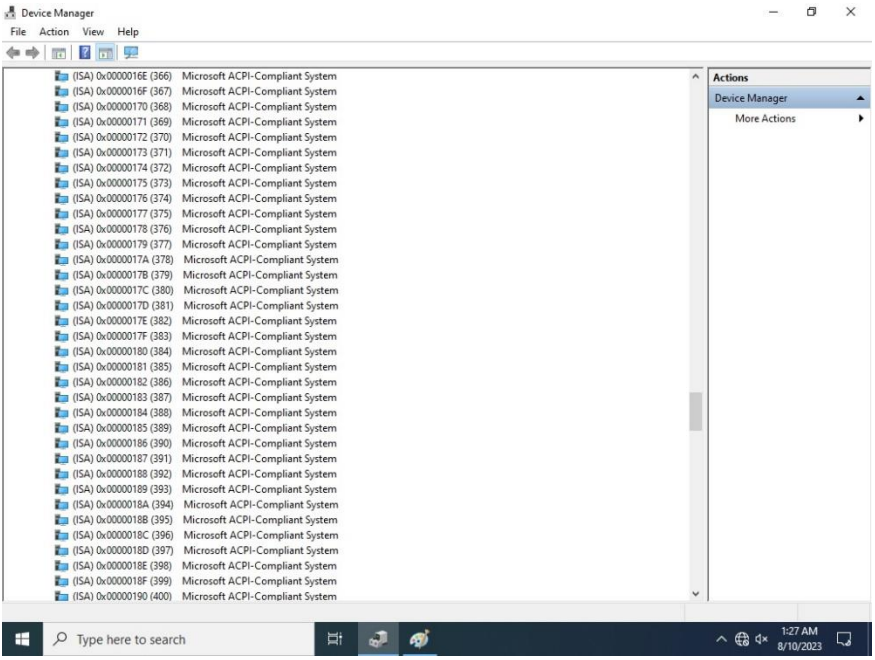
8/10/2023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Actions
Device Manager
More Actions
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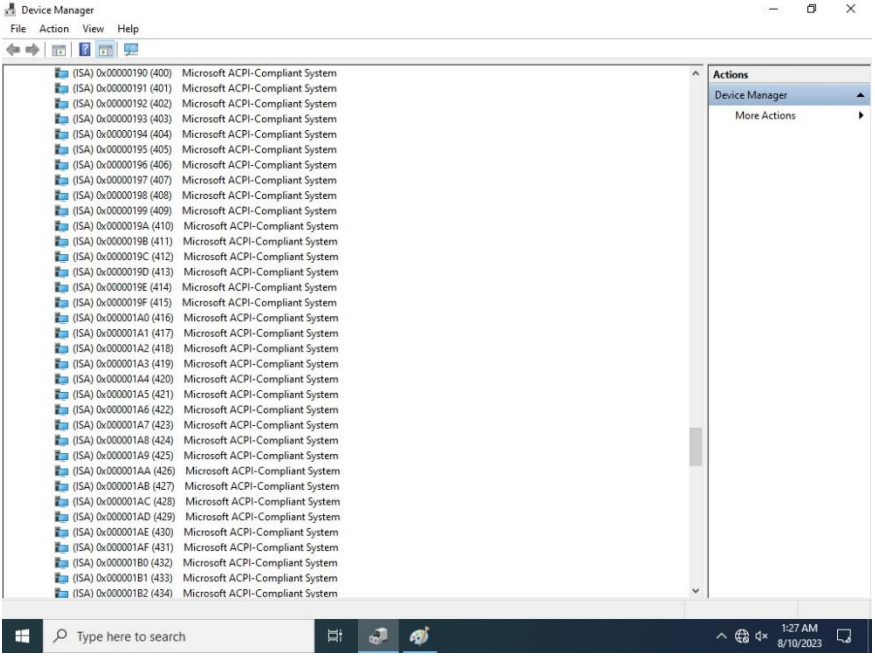
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Actions
Device Manager
More Actions
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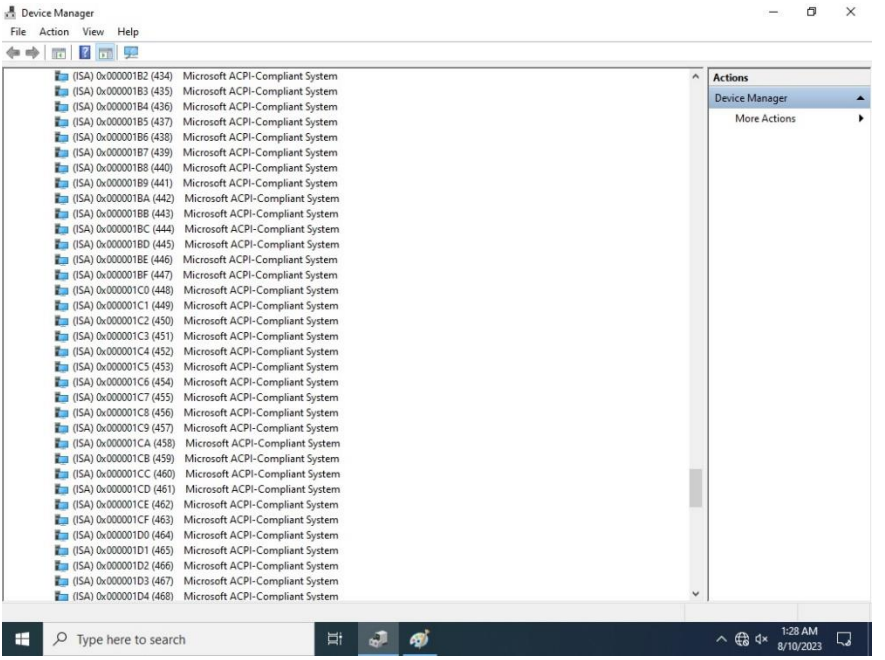
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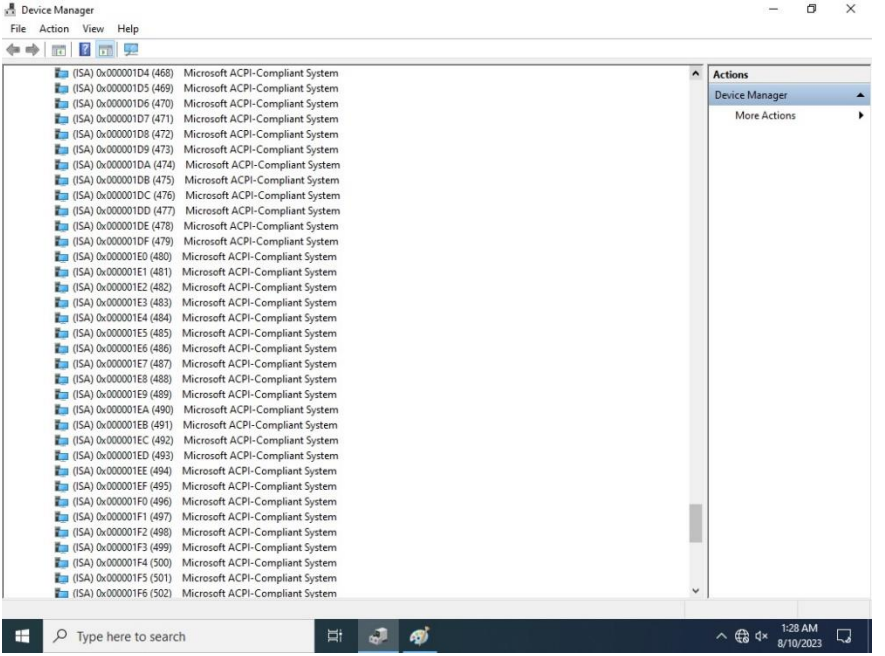
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Device Manager
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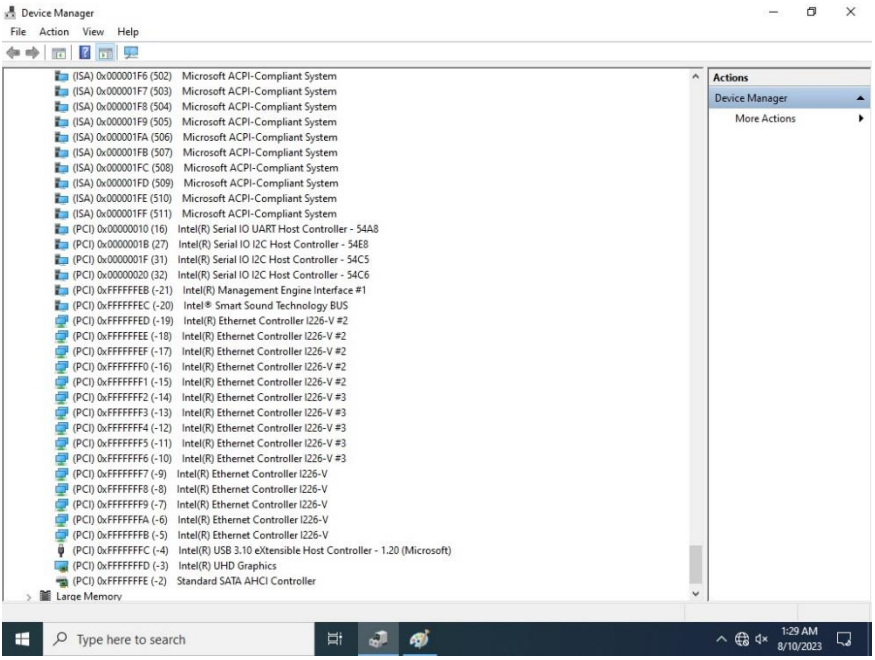
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(PCI) 0x0000001B (27) Intel(R) Serial IO I2C Host Controller - 54E8
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(PCI) 0x00000020 (32) Intel(R) Serial IO I2C Host Controller - 54C6
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(PCI) 0xFFFFFFEC (-20) Intel® Smart Sound Technology BUS
(PCI) 0xFFFFFFED (-19) Intel(R) Ethernet Controller I226-V #2
(PCI) 0xFFFFFFEE (-18) Intel(R) Ethernet Controller I226-V #2
(PCI) 0xFFFFFFEF (-17) Intel(R) Ethernet Controller I226-V #2
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(PCI) 0xFFFFFFF2 (-14) Intel(R) Ethernet Controller I226-V #3
(PCI) 0xFFFFFFF3 (-13) Intel(R) Ethernet Controller I226-V #3
(PCI) 0xFFFFFFF4 (-12) Intel(R) Ethernet Controller I226-V #3
(PCI) 0xFFFFFFF5 (-11) Intel(R) Ethernet Controller I226-V #3
(PCI) 0xFFFFFFF6 (-10) Intel(R) Ethernet Controller I226-V #3
(PCI) 0xFFFFFFF7 (-9) Intel(R) Ethernet Controller I226-V
(PCI) 0xFFFFFFF8 (-8) Intel(R) Ethernet Controller I226-V
(PCI) 0xFFFFFFF9 (-7) Intel(R) Ethernet Controller I226-V
(PCI) 0xFFFFFFFA (-6) Intel(R) Ethernet Controller I226-V
(PCI) 0xFFFFFFFB (-5) Intel(R) Ethernet Controller I226-V
(PCI) 0xFFFFFFFC (-4) Intel(R) USB 3.10 eXtensible Host Controller - 1.20 (Microsoft)
(PCI) 0xFFFFFFFD (-3) Intel(R) UHD Graphics
(PCI) 0xFFFFFFFE (-2) Standard SATA AHCI Controller
Large Memory
Actions
Device Manager
More Actions
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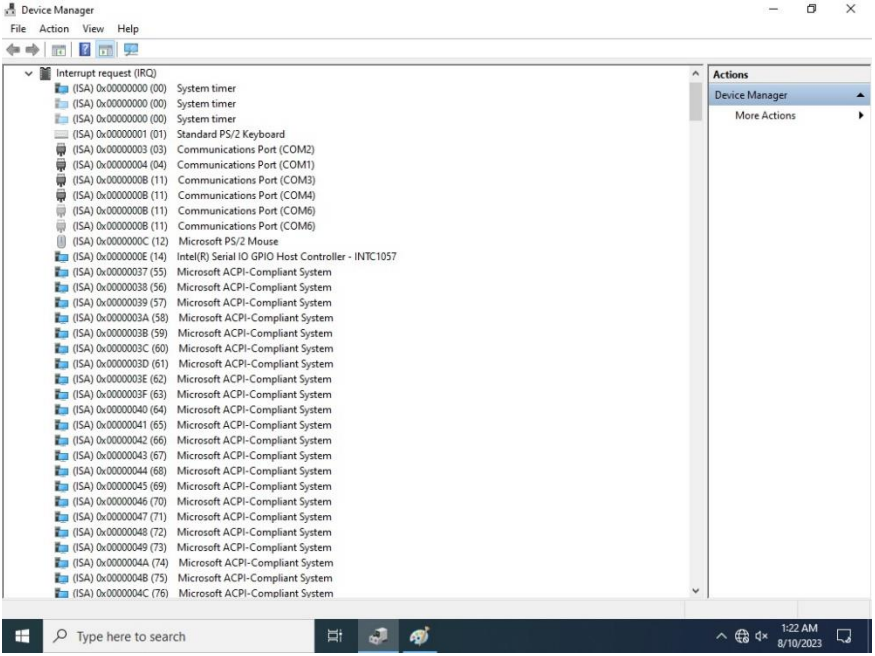
Device Manager
File Action View Help
Interrupt request (IRQ)
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(ISA) 0x00000000 (00) System timer
(ISA) 0x00000000 (00) System timer
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(ISA) 0x00000003 (03) Communications Port (COM2)
(ISA) 0x00000004 (04) Communications Port (COM1)
(ISA) 0x0000000B (11) Communications Port (COM3)
(ISA) 0x0000000B (11) Communications Port (COM4)
(ISA) 0x0000000B (11) Communications Port (COM6)
(ISA) 0x0000000B (11) Communications Port (COM6)
(ISA) 0x0000000C (12) Microsoft PS/2 Mouse
(ISA) 0x0000000E (14) Intel(R) Serial IO GPIO Host Controller - INTC1057
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(ISA) 0x0000003C (60) Microsoft ACPI-Compliant System
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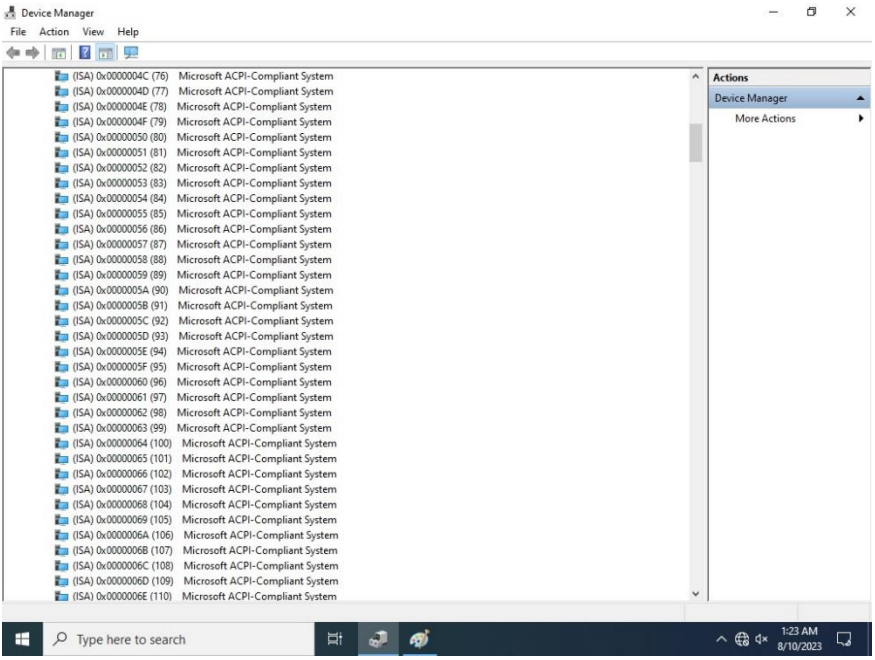
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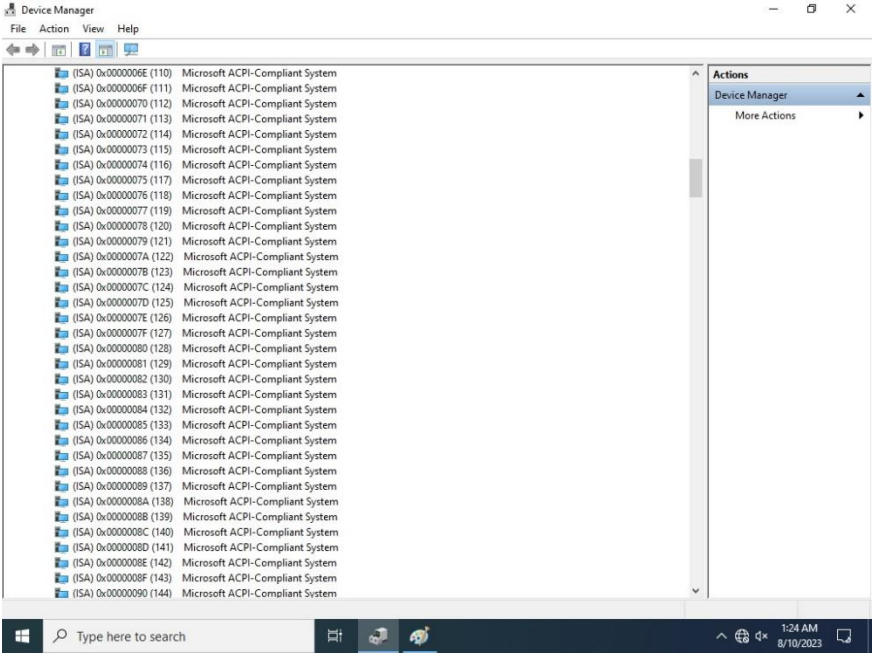
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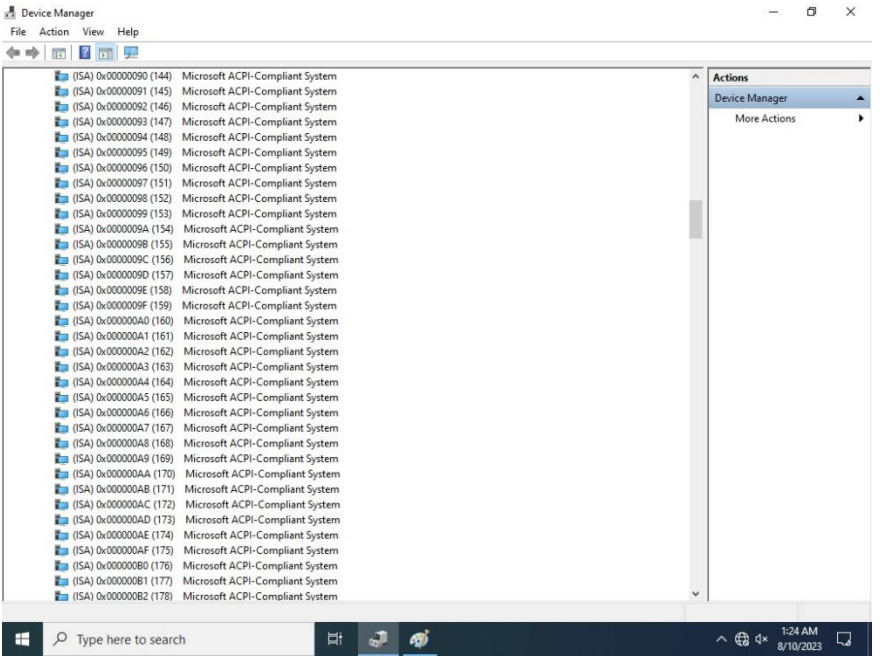
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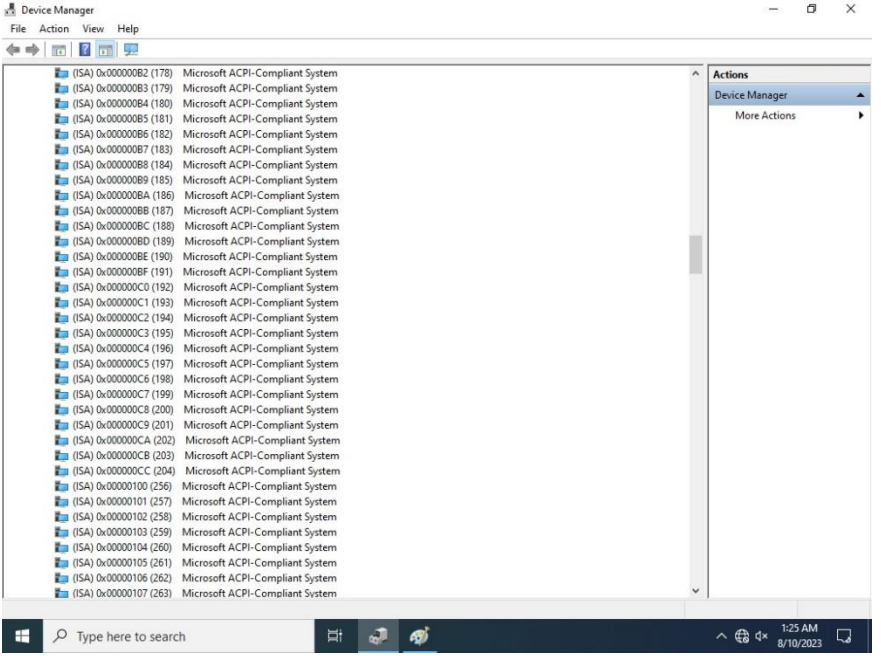
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# Appendix B

Mating Connectors and Cables

## B.1 Mating Connectors and Cables

| Label | Function | Vendor | Model No | Cable P/N |
|---|---|---|---|---|
| CN1 | SATA Power | JST | PHR-2 | 170X000085 |
| CN2 | Front Panel | JST | SHR-10V-S-B | 170X000590 |
| CN3 | Port 80 Debug Port | JST | SHR-10V-S-B | 1703100133 |
| CN5 | SATA | Molex | 887505318 | 170X000085 |
| CN9 | Power | N/A | N/A | 170204010R |
| CN11 | Audio with detect | Aces | 50247-012H0H0-001 | 170X000517 |
| CN13 | External +5VSB Power Input and PS_ON# | JST | XHP-3 | 170220020B |
| CN18 | GPIO | MOLEX | 51110-1050 | N/A |
| CN22 | LAN1 LED | JST | SHR-04V-S-B | 170X000634 |
| CN23 | LAN3 LED | JST | SHR-04V-S-B | 170X000634 |
| CN24 | LAN2 LED | JST | SHR-04V-S-B | 170X000634 |
| CN33 | CPU FAN | Molex | 51021-0400 | N/A |
| CN39 | External RTC | Molex | 51021-0200 | 175011301K |
| CN42 | I2C/SMBUS | JST | SHR-05V-S-B | 170X000743 |
| CN44 | COM Port1 | Molex | 51021-0900 | 1701090140 |
| CN45 | COM Port2 | Molex | 51021-0900 | 1701090140 |
| CN48 | USB 2.0 Port3 | Molex | 51021-0500 | 1700080104 |
| CN49 | USB 2.0 Port4 | Molex | 51021-0500 | 1700080104 |
| CN50 | USB 2.0 Port5 | Molex | 51021-0500 | 1700080171 |
| CN51 | USB 2.0 Port6 | Molex | 51021-0500 | 1700080171 |