FES-5130

Flexible Embedded System 2.5" SATA Hard Disk Drive Bay 1 Gigabit Ethernet Port/ 7 COM/ 6 USB2.0 HD Audio Codec (2CH Audio)

> FES-5130 Manual 3rd Ed. February 2012

Copyright Notice

This document is copyrighted, 2012. All rights are reserved. The original manufacturer reserves the right to make improvements to the products described in this manual at any time without notice.

No part of this manual may be reproduced, copied, translated, or transmitted in any form or by any means without the prior written permission of the original manufacturer. Information provided in this manual is intended to be accurate and reliable. However, the original manufacturer assumes no responsibility for its use, or for any infringements upon the rights of third parties that may result from its use.

The material in this document is for product information only and is subject to change without notice. While reasonable efforts have been made in the preparation of this document to assure its accuracy, AAEON assumes no liabilities resulting from errors or omissions in this document, or from the use of the information contained herein.

AAEON reserves the right to make changes in the product design without notice to its users.

Acknowledgments

All other products' name or trademarks are properties of their respective owners.

- Award is a trademark of Award Software International, Inc.
- CompactFlash[™] is a trademark of the Compact Flash Association.
- Microsoft Windows[®] is a registered trademark of Microsoft Corp.
- Intel[®], Atom[™] are trademarks of Intel Corporation.
- PC/AT, PS/2, and VGA are trademarks of International Business Machines Corporation.

All other product names or trademarks are properties of their respective owners.

Packing List

Before you begin operating your PC, please make sure that the following materials are enclosed:

- 1 FES-5130
- 1 CD-ROM for manual (in PDF format) and drivers
- 1 60W AC-DC Power Adapter
- 1 Wallmount Kit

If any of these items should be missing or damaged, please contact your distributor or sales representative immediately.

Safety & Warranty

- 1. Read these safety instructions carefully.
- 2. Keep this user's manual for later reference.
- 3. Disconnect this equipment from any AC outlet before cleaning. Do not use liquid or spray detergents for cleaning. Use a damp cloth.
- 4. For pluggable equipment, the power outlet must be installed near the equipment and must be easily accessible.
- 5. Keep this equipment away from humidity.
- 6. Put this equipment on a firm surface during installation. Dropping it or letting it fall could cause damage.
- 7. The openings on the enclosure are for air convection. Protect the equipment from overheating. DO NOT COVER THE OPENINGS.
- 8. Make sure the voltage of the power source is correct before connecting the equipment to the power outlet.
- 9. Position the power cord so that people cannot step on it. Do not place anything over the power cord.
- 10. All cautions and warnings on the equipment should be noted.
- 11. If the equipment is not used for a long time, disconnect it from the power source to avoid damage by transient over-voltage.
- 12. Never pour any liquid into an opening. This could cause fire or electrical shock.
- 13. Never open the equipment. For safety reasons, only qualified service personnel should open the equipment.
- 14. If any of the following situations arises, get the equipment checked by service personnel:
 - a. The power cord or plug is damaged.
 - b. Liquid has penetrated into the equipment.
 - c. The equipment has been exposed to moisture.

Flexible Embedded FES-5130

- d. The equipment does not work well, or you cannot get it to work according to the user's manual.
- e. The equipment has been dropped and damaged.
- f. The equipment has obvious signs of breakage.
- DO NOT LEAVE THIS EQUIPMENT IN AN ENVIRONMENT WHERE THE STORAGE TEMPERATURE IS BELOW -20°C (-4°F) OR ABOVE 65°C (149°F). IT MAY DAMAGE THE EQUIPMENT.

FCC



This device complies with Part 15 FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received including interference that may cause undesired operation.

Caution:

There is a danger of explosion if the battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions and your local government's recycling or disposal directives.

Flexible Embedded System

FES-5130

Below Table for China RoHS Requirements 产品中有毒有害物质或元素名称及含量

AAEON Boxer/ Industrial System

	有毒有害物质或元素					
部件名称	铅	汞	镉	六价铬	多溴联苯	多溴二苯醚
	(Pb)	(Hg)	(Cd)	(Cr(VI))	(PBB)	(PBDE)
印刷电路板	x	0	0		0	0
及其电子组件	~	0	0	0	0	0
外部信号	×	0	0	0	0	0
连接器及线材	~	0	0		0	0
外壳	×	0	0	0	0	0
中央处理器	×	0	0	0	0	0
与内存	~	0	0		0	0
硬盘	×	0	0	0	0	0
电源	×	0	0	0	0	0
0:表示该有毒有害	物质在	该部件周	所有均质	材料中的	含量均在	

SJ/T 11363-2006 标准规定的限量要求以下。

X:表示该有毒有害物质至少在该部件的某一均质材料中的含量超出 SJ/T 11363-2006 标准规定的限量要求。

备注:

一、此产品所标示之环保使用期限,系指在一般正常使用状况下。

二、上述部件物质中央处理器、内存、硬盘、电源为选购品。

Contents

Chapter	1	General Information
	1.1	Introduction1-2
	1.2	Features 1-3
	1.3	Specifications1-4
	1.4	General System Information1-6
Chapter	2	Hardware Installation
	2.1	Location of Jumpers and Connectors
	2.2	Mechanical Drawing2-4
	2.3	List of Jumpers2-5
	2.4	List of Connectors
	2.5	Setting Jumpers
	2.6	Clear CMOS (JP1)2-8
	2.7	LVDS Power Selection (JP3)2-8
	2.8	LVDS Inverter Voltage Selection (JP4)2-8
	2.9	CFD Voltage 3.3V/5V Selection (JP5)2-8
	2.1	0 Auto Power Selection (JP6)2-8
	2.1	1 COM1 Port Power/Ring Selection (JP10)
	2.1	2 COM2 Port Power/Ring Selection (JP11)
	2.1	3 COM2 Port Power Selection (COMV1)2-9
	2.1	4 COM1 Port Power Selection (COMV2)2-9
	2.1	5 Front Panel Connector (FP1)2-9
	2.1	6 RS-232 Serial Port Connector (COM1, COM3, COM4,
	со	M5, COM6, COM7)

2.17 RS-232/422/485 Serial Port Connector (COM2)2-10
2.18 LPT Port Connector (LPT1)2-10
2.19 LVDS Inverter (CN1)
2.20 LVDS (CN2)
2.21 Audio Pin Header (Right Channel) (CN4) 2-12
2.22 Audio Pin Header (Left Channel) (CN5)2-12
2.23 USB Connector (CN6)2-12
2.24 Digital I/O (CN7) (Address: 802h, 803h, 804h) 2-12
2.25 PS/2 Keyboard/Mouse Connector (CN8)2-13
2.26 SATA Power Connector (PWR1, 2)
2.27 Audio Connector (AUDIO1)2-13
2.28 COM Port Function On AEB-POS2-14
2.29 Installing The Hard Disk Drive
Chapter 3 Award BIOS Setup
3.1 System Test and Initialization
3.2 Award BIOS Setup 3-3
Chapter 4 Driver Installation
4.1 Installation4-3
Appendix A Programming The Watchdog Timer
A.1 ProgrammingA-2
A.2 ITE8781 Watchdog Timer Initial ProgramA-5
Appendix B I/O Information
B.1 I/O Address MapB-2
B.2 Memory Address MapB-3
B.3 IRQ Mapping ChartB-4

Flexible Embedded System	F E S - 5 1 3 0

B.4 DMA Channel AssignmentsB-4	1
--------------------------------	---

Flexible Embedded System

Chapter

General Information

Chapter 1 General Information 1-1

1.1 Introduction

FES-5130 adopts the Intel[®] Atom[™] N270 Processor, up to 1.6GHz. The chipset is equipped with Intel[®] 945GSE & ICH7M. Moreover, the system memory features 200-pin DDR2 400/533 SODIMM socket up to 2GB. It deploys one RJ-45 port that features 10/100/1000Base-TX Ethernet LAN.

This compact FES-5130 equipped with one internal 2.5" Hard Disk Drive with SATA interface. In addition, it features six RS-232/ one RS-232/422/485 COM ports and six USB2.0 ports. Furthermore, the VIA HD VT-1708B supports optional 5.1 audio channel and 2W amplifier for speaker out.

The FES-5130 supports dual-view with DVI and VGA, in addition, it can be deployed with optional wide range 9~36V DC converter. Moreover, an optional 802.11b/g Mini-PCIe module can be supported. The FES-5130 condensed appearance features desktop and wallmount form factor that fits nicely into a space-limited environment.

1.2 Features

- Intel[®] Atom[™] N270 1.6 GHz
- Intel[®] 945GSE + ICH7M Chipset
- 200-Pin Non-ECC DDR2 400/533 SODIMM x 1, Up To 2 GB
- Dual-View With DVI and VGA
- Realtek PCI-Express 10/100/1000Base-TX Ethernet x 1
- 2.5" SATA Hard Disk Drive Bay x 1
- USB 2.0 x 6, COM x 7, CompactFlash™ x 1, 8-bit Digital I/O
- Optional WiFi Module & Wide Range 9~36V DC Converter
- Fanless Solution

Flexible Embedded System

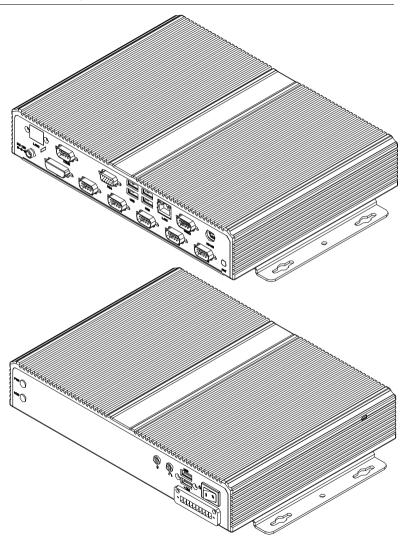
F E S - 5 1 3 0

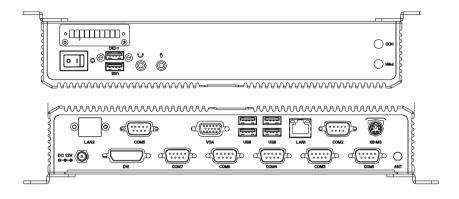
1.3 Specifications

System	
Form Factor	Compact size for Desktop/Wallmount
Main Board	IMBE-945G
Processor	Onboard Intel [®] Atom N270 1.6GHz
System Memory	200-pin DDR2 400/533 SODIMM Socket x 1, Max. 2GB
Chipset	Intel [®] 945GSE & ICH7M
Ethernet	10/100/1000Base-TX Ethernet x 1; Wireless LAN: 802.11b/g Mini-PCIe module x 1 (optional)
BIOS	Award BIOS 4Mb Flash EEPROM
Audio	VIA HD codec VT1708B 2W amplifier for speaker out
I/O Chip	ITE 8712F I/O Controller
Hard Disk Drive	2.5" Hard Disk Drive bay x 1 (SATA2 interface)
Solid Storage Disk	CompactFlash™ Type 2 socket x 1
LED	Two indicators for Power and HDD
Watchdog Timer	Reset: 1 sec.~255 min. and 1 sec. or 1 min./step
H/W Status Monitor	Monitoring system temperature, voltage, and cooling fan status
Power Supply	Single DC 12V input; Optional with wide range 9~36V DC

Flexible Embed System	Ided FES-5130
Dimension (MydyD)	Converter 10.63" x 1.97" x 7.87" (270mm x 50mm x
Weight	200mm) 9.34 lb (4.2 Kg)
<i>External I/O</i> Serial Port	RS-232 x 6 RS-232/422/485 x 1
KB & Mouse	Keyboard /Mouse x 1
Universal Serial Bus	USB2.0 x 6 (2 on the front panel and 4 at I/O)
Audio	Audio jack x 2 (Mic-in, Line-out)
Ethernet	RJ-45 x 1
Display	VGA x 1, DVI x 1
Environment	
Operating Temperature	$32^{\circ}F \sim 113^{\circ}F$ (0°C ~45°C) (Industrial grade SSD only)
Storage Temperature	4°F~140°F (-20°C ~60°C)
Operating Humidity	10~80%
Storage Humidity	10~80%, non-condensing
Vibration	SSD version comply with MIL-STD-810F 514.5C-1
Shock	40G peak acceleration (11 m sec. duration), operation

1.4 General System Information



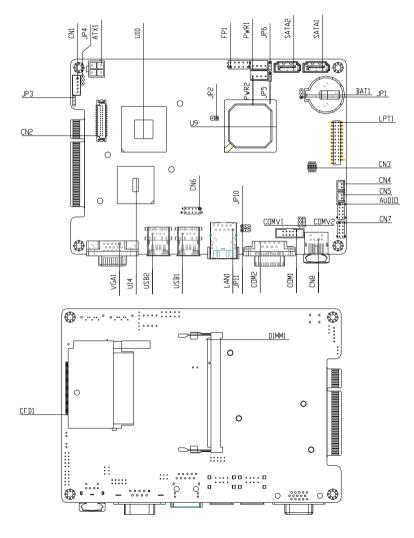




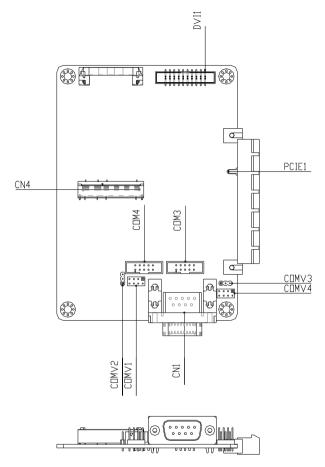
Hardware Installation

2.1 Location of Jumpers and Connectors

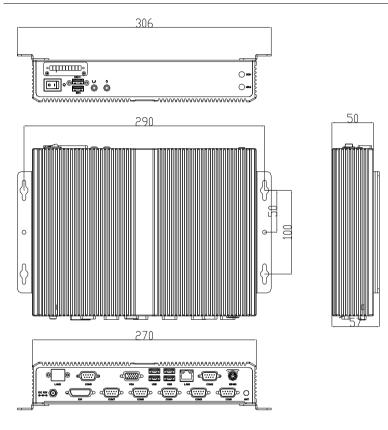
Main board



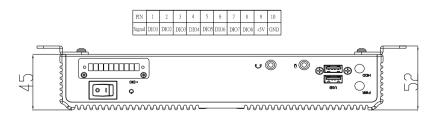
AEB-POS



2.2 Mechanical Drawing



DIO Pin Definition



Chapter 2 Hardware Installation 2-4

2.3 List of Jumpers

The board has a number of jumpers that allow you to configure your system to suit your application.

Label	Function
JP1	Clear CMOS
JP3	LVDS Power Selection
JP4	LVDS Inverter Power Selection
JP5	CFD Voltage 3.3V/5V Selection
JP6	Auto Power on Selection
JP10	COM1 Port Power/Ring Selection
JP11	COM2 Port Power/Ring Selection
COMV1~2	COM1, 2 Port Power Selection

The table below shows the function of each of the board's jumpers:

2.4 List of Connectors

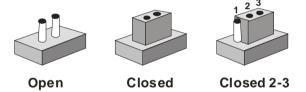
The board has a number of connectors that allow you to configure your system to suit your application. The table below shows the function of each board's connectors:

Label	Function
FP1	Front Panel Connector 1
VGA1	VGA Display Connector
COM1 \ 3 \ 4	RS-232 Serial Port Connector
COM2	RS-232/422/485 Serial Port Connector
CFD1	Compact Flash Slot (Master)
LPT1	LPT Port Connector
USB1~2	USB Connector
LAN1	10/100 or 100/1000Base-TX Ethernet Connector
DIMM1	DDRII SODIMM Slot
CN1	LVDS Inverter Power Connector
CN2	LVDS Connector
CN4	Audio Pin Header (right channel)
CN5	Audio Pin Header (left channel)
CN6	USB Pin Header
CN7	Digital I/O Connector
CN8	PS2 Keyboard/Mouse Connector
ATX1	+12V Power Connector
PWR1~2	SATA POWER Connector
SATA1~2	Serial ATA Connector
AUDIO1	Audio Connector

2.5 Setting Jumpers

You configure your card to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch. It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To "close" a jumper you connect the pins with the clip.

To "open" a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2 and 3. In this case you would connect either pins 1 and 2 or 2 and 3.



A pair of needle-nose pliers may be helpful when working with jumpers.

If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any change.

Generally, you simply need a standard cable to make most connections.

2.6 Clear CMOS (JP1)

JP1	Function
1-2	Normal (Default)
2-3	Clear CMOS

2.7 LVDS Power Selection (JP3)

JP3	Function	
1-2	+5V	
2-3	+3V (Default)	

2.8 LVDS Inverter Voltage Selection (JP4)

JP4	Function
1-2	+5V
2-3	+12V (Default)

2.9 CFD Voltage 3.3V/5V Selection (JP5)

JP5	Signal	
1-2	+3.3V	
2-3	+5V (Default)	

2.10 Auto Power Selection (JP6)

JP6	Function
Close 1-2	Enable
Close 2-3	Disable (Default)

2.11 COM1 Port Power/Ring Selection (JP10)

JP10	Function
Close 1-2	COM1 Power Selection (Default)
Close 2-3	RI1

2.12 COM2 Port Power/Ring Selection (JP11)

JP11	Function
Close 1-2	COM2 Power Selection (Default)
Close 2-3	RI1

2.13 COM2 Port Power Selection (COMV1)

COMV1	Function
Close 1-2	+12V (Default)
Close 2-3	+5V

2.14 COM1 Port Power Selection (COMV2)

COMV2	Function
Close 1-2	+12V (Default)
Close 2-3	+5V

2.15 Front Panel Connector (FP1)

Pin	Signal	Pin	Signal
1	Power On Button (-)	2	Power On Button (+)
3	IDE LED (-)	4	IDE LED (+)
5	Power LED (-)	6	Power LED (+)
7	SPK(-)	8	SPK(+)

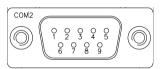
Flexible Embedded System		F E S - 5 1 3 0		
9	Reset Switch (-)	10	Reset Switch (+)	

2.16 RS-232 Serial Port Connector (COM1, COM3, COM4, COM5,

COM6, COM7)

Pin	Signal	Pin	Signal
1	DCD	2	RXD
3	TXD	4	DTR
5	GND	6	DSR
7	RTS	8	CTS
9	RI		

2.17 RS-232/422/485 Serial Port Connector (COM2)



Pin	Signal	Pin	Signal
1	DCD (422TXD-/485DATA-)	2	RXD (422RXD+)
3	TXD (422TXD+/485DATA+)	4	DTR (422RXD-)
5	GND	6	DSR
7	RTS	8	CTS
9	RI		

2.18 LPT Port Connector (LPT1)

Pin	Signal	Pin	Signal
1	#STROBE	2	#AFD
3	DATA0	4	#ERROR
5	DATA1	6	#INIT
7	DATA2	8	#SLIN

Flexible Embedded System			F E S - 5 1 3 0
9	DATA3	10	GND
11	DATA4	12	GND
13	DATA5	14	GND
15	DATA6	16	GND
17	DATA7	18	GND
19	#ACK	20	GND
21	BUSY	22	GND
23	PE	24	GND
25	SELECT	26	GND

2.19 LVDS Inverter (CN1)

Pin	Signal
1	12V / 5V
2	VCON
3	+5V
4	GND
5	BKL_EN

2.20 LVDS (CN2)

Pin	Signal	Pin	Signal
1	BKLTEN	2	BKLTCL
3	VLCD	4	GND
5	LA_CLK#	6	LA_CLK
7	VLCD	8	GND
9	LA_TX0#	10	LA_TX0
11	LA_TX1#	12	LA_TX1
13	LA_TX2#	14	LA_TX2
15	N.C.	16	N.C.

	Flexible Embedded System		F E S - 5 1 3 0
17	N.C.	18	N.C.
19	LB_TX0#	20	LB_TX0
21	LB_TX1#	22	LB_TX1
23	LB_TX2#	24	LB_TX2
25	N.C.	26	N.C.
27	VLCD	28	GND
29	LB_CLK#	30	LB_CLK

2.21 Audio Pin Header (Right Channel) (CN4)

Pin	Signal	Pin	Signal
1	SPK_RO+	2	SPK_RO-

2.22 Audio Pin Header (Left Channel) (CN5)

Pin	Signal	Pin	Signal
1	SPK_LO+	2	SPK_LO-

2.23 USB Connector (CN6)

Pin	Signal	Pin	Signal
1	+5V	2	GND
3	USBD1-	4	GND
5	USBD1+	6	USBD2+
7	GND	8	USBD2-
9	GND	10	+5V

2.24 Digital I/O (CN7) (Address: 802h, 803h, 804h)

Pin	Signal	Pin	Signal	
1	In1/Out1	2	In2/Out2	
3	In3/Out3	4	In4/Out4	

F	Flexible Embedded System		F E S - 5 1 3 0
5	In5/Out5	6	In6/Out6
7	In7/Out7	8	In8/Out8
9	+5V	10	GND

2.25 PS/2 Keyboard/Mouse Connector (CN8)

Pin	Signal
1	KB_DATA
2	MS-DATA
3	GND
4	+5V
5	KB_CLK
6	MS_CLK

2.26 SATA Power Connector (PWR1, 2)

Pin	Signal	Pin	Signal
1	+12V	2	GND
3	GND	4	+5V

2.27 Audio Connector (AUDIO1)

Pin	Signal	Pin	Signal
1	MIC1-CL	2	GND
3	MIC1-CR	4	NC
5	Front-R	6	GND
7	NC	8	NC
9	Front-L	10	GND

2.28 COM Port Function On AEB-POS

COM 3,4 Port Function Selection (COMV1)

For AEB-POS, the function of COMV1 will be as below:

COMV1	Function
Close 1-2	RI3 (Default)
Close 3-4	+5V/+12V
Close 5-6	RI4 (Default)
Close 7-8	+5V/+12V

Note: COM6, 7 will be shown COM3, 4 on AEB-POS.

COM 3,4 Port Function Selection (COMV2)

For AEB-POS, the function of COMV2 will be as below:

COMV2	Function
Close 1-2	+12V (Default)
Close 2-3	+5V

Note: COM6, 7 will be shown COM3, 4 on AEB-POS.

COM2 Port Function Selection (COMV3)

For AEB-POS, the function of COMV3 will be as below:

COMV3	Function	
Close 1-2	+12V (Default)	
Close 2-3	+5V	

Note: COM5 will be shown CN1 on AEB-POS.

UCOM2 Port Function Selection (COMV4)

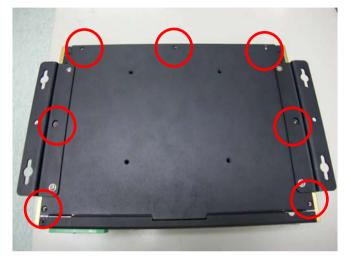
For AEB-POS, the function of COMV4 will be as below:

COMV4	Function	
Close 5-6	RI2 (Default)	
Close 7-8	+5V/+12V	

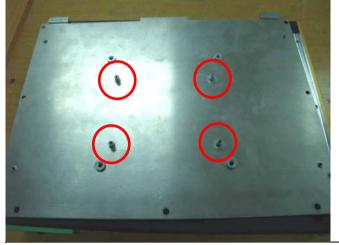
Note: COM5 will be shown CN1 on AEB-POS.

2.29 Installing the Hard Disk Drive

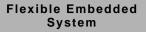
Step1 : Unfasten the seven screws on the bottom case of FES-5130.



Step2 : Move bottom case from FES-5130, the four screws at bottom case will be fastened by HDD tray.



Chapter 2 Hardware Installation 2-16

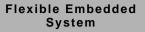


Step 3 : Fasten four screws on HDD Tray.

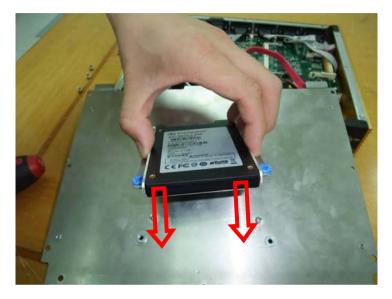


Step 4 : Put plastic rubber damper of anti-vibration on HDD tray.





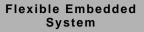
Step 5 : Put HDD with Tray on bottom case.



Step 6 : Fasten 4 screws on bottom case.



Chapter 2 Hardware Installation 2-18

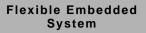


Step 7 : Connect 2 SATA cables from HDD to PCBA.

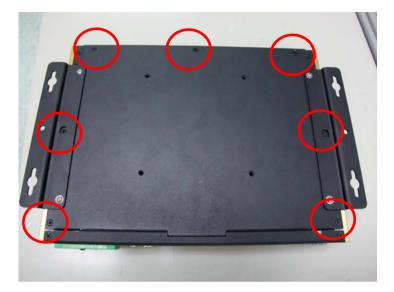


Step 8 : HDD installation finished.





Step 9 : Fasten the seven screws on the bottom case of FES-5130.



F E S - 5 1 3 0

Chapter 3

Award BIOS Setup

Chapter 3 Award BIOS Setup 3-1

3.1 System Test and Initialization

These routines test and initialize board hardware. If the routines encounter an error during the tests, you will either hear a few short beeps or see an error message on the screen. There are two kinds of errors: fatal and non-fatal. The system can usually continue the boot up sequence with non-fatal errors. Non-fatal error messages usually appear on the screen along with the following instructions:

Press <F1> to RESUME

Write down the message and press the F1 key to continue the boot up sequence.

System configuration verification

These routines check the current system configuration against the values stored in the CMOS memory. If they do not match, the program outputs an error message. You will then need to run the BIOS setup program to set the configuration information in memory.

There are three situations in which you will need to change the CMOS settings:

- 1. You are starting your system for the first time
- 2. You have changed the hardware attached to your system
- 3. The CMOS memory has lost power and the configuration information has been erased.

The FES-5130 CMOS memory has an integral lithium battery backup for data retention. However, you will need to replace the complete unit when it finally runs down.

3.2 Award BIOS Setup

Awards BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed CMOS RAM so that it retains the Setup information when the power is turned off.

Entering Setup

Power on the computer and press immediately. This will allow you to enter Setup.

Standard CMOS Features

Use this menu for basic system configuration. (Date, time, IDE, etc.)

Advanced BIOS Features

Use this menu to set the advanced features available on your system.

Advanced Chipset Features

Use this menu to change the values in the chipset registers and optimize your system performance.

Integrated Peripherals

Use this menu to specify your settings for integrated peripherals. (Primary slave, secondary slave, keyboard, mouse etc.)

Power Management Setup

Use this menu to specify your settings for power management. (HDD power down, power on by ring, KB wake up, etc.)

PnP/PCI Configurations

This entry appears if your system supports PnP/PCI.

PC Health Status

This menu allows you to set the shutdown temperature for your

system.

Frequency/Voltage Control

Use this menu to specify your settings for auto detect DIMM/PCI clock and spread spectrum.

Load Fail-Safe Defaults

Use this menu to load the BIOS default values for the minimal/stable performance for your system to operate.

Load Optimized Defaults

Use this menu to load the BIOS default values that are factory settings for optimal performance system operations. While AWARD has designated the custom BIOS to maximize performance, the factory has the right to change these defaults to meet their needs.

Set Supervisor/User Password

Use this menu to set Supervisor/User Passwords.

Save and Exit Setup

Save CMOS value changes to CMOS and exit setup.

Exit Without Saving

Abandon all CMOS value changes and exit setup.

You can refer to the "AAEON BIOS Item Description.pdf" file in the CD for the meaning of each setting in this chapter.

Chapter

Driver Installation

Chapter 4 Driver Installation 4-1

The FES-5130 comes with an AutoRun CD-ROM that contains all drivers and utilities that can help you to install the driver automatically.

Insert the driver CD, the driver CD-title will auto start and show the installation guide. If not, please follow the sequence below to install the drivers.

Follow the sequence below to install the drivers:

Step 1 – Install INF Driver Step 2 – Install VGA Driver Step 3 – Install Audio Driver Step 4 – Install LAN Driver

Please read instructions below for further detailed installations.

4.1 Installation:

Insert the FES-5130 CD-ROM into the CD-ROM drive and install the drivers from Step 1 to Step 4 in order.

Step 1 – Install INF Driver

- 1. Click on the *Step 1-INF* folder and select the OS your system is
- 2. Double click on the .exe file located in each OS folder
- 3. Follow the instructions that the window shows
- 4. The system will help you install the driver automatically

Step 2 – Install VGA Driver

- 1. Click on the **Step 2 –VGA** folder and select the OS your system is
- 2. Double click on .exe file located in each OS folder
- 3. Follow the instructions that the window shows
- 4. The system will help you install the driver automatically
- Step 3 Install Audio Driver
 - 1. Click on the *Step 3-Audio* folder and double click on the *SETUP.exe*
 - 2. Follow the instructions that the window shows
 - 3. The system will help you install the driver automatically

Step 4 – Install LAN Driver

- 1. Click on the **Step 4 –LAN** folder and select the OS your system is
- 2. Double click on setup.exe located in each OS folder
- 3. Follow the instructions that the window shows
- 4. The system will help you install the driver automatically

Appendix A

Programming the Watchdog Timer

Appendix A Programming the Watchdog Timer A-1

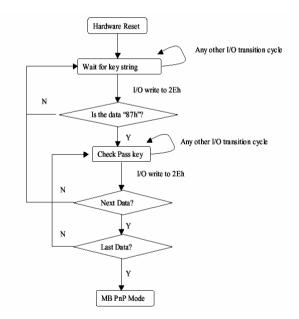
A.1 Programming

FES-5130 utilizes ITE 8781 chipset as its watchdog timer controller. (K version)

Below are the procedures to complete its configuration and the AAEON initial watchdog timer program is also attached based on which you can develop customized program to fit your application.

Configuring Sequence Description

After the hardware reset or power-on reset, the ITE 8781 enters the normal mode with all logical devices disabled except KBC.



There are three steps to complete the configuration setup: (1) Enter the MB PnP Mode; (2) Modify the data of configuration registers; (3) Exit the MB PnP Mode. Undesired result may occur if the MB PnP Mode is not exited normally.

Appendix A Programming the Watchdog Timer A-2

(1) Enter the MB PnP Mode

To enter the MB PnP Mode, four special I/O write operations are to be performed during Wait for Key state. To ensure the initial state of the key-check logic, it is necessary to perform four write opera-tions to the Special Address port (2EH). Two different enter keys are provided to select configuration ports (2Eh/2Fh) of the next step.

	Address Port	Data Port
87h, 01h, 55h, 55h:	2Eh	2Fh

(2) Modify the Data of the Registers

All configuration registers can be accessed after entering the MB PnP Mode. Before accessing a selected register, the content of Index 07h must be changed to the LDN to which the register belongs, except some Global registers.

(3) Exit the MB PnP Mode

Set bit 1 of the configure control register (Index=02h) to 1 to exit the MB PnP Mode.

LDN	Index	R/W	Reset	Configuration Register or Action
All	02H	W	N/A	Configuration Control
07H	71H	R/W	00H	WatchDog Timer Control Register
07H	72H	R/W	00H	WatchDog Timer Configuration Register
07H	73H	R/W	00H	WatchDog Timer Time-out Value (LSB)
				Register
07H	74H	R/W	00H	WatchDog Timer Time-out Value (MSB) Register

WatchDog Timer Configuration Registers

Configure Control (Index=02h)

This register is write only. Its values are not sticky; that is to say, a hardware reset will automatically clear the bits, and does not require the software to clear them.

Bit Description 7-2 Reserved 1 Returns to the Wait for Key state. This bit is used when the configuration sequence is completed. 0 Resets all logical devices and restores configuration registers to their power-on states.

WatchDog Timer Control Register (Index=71h, Default=00h)

Bit	Description
7	WDT is reset upon a CIR interrupt
6	WDT is reset upon a KBC (Mouse) interrupt
5	WDT is reset upon a KBC (Keyboard) interrupt
4	Reserved
3-2	Reserved
1	Force Time-out. This bit is self-clearing
0	WDT status
	1: WDT value reaches 0
	0: WDT value is not 0

WatchDog Timer Configuration Register (Index=72h, Default=00h)

Bit	Description
7	WDT Time-out value select
	1: Second
	0: Minute
6	WDT output through KRST (pulse) enable
5	WDT Time-out value Extra select
	1: 4s.
	0: Determine by WDT Time-out value select (bit7 of this
	register)
4	WDT output through PWROK1/PWROK2 (pulse) enable
3	Select the interrupt level ^{note} for WDT

WatchDog Timer Time-out Value (LSB) Register (Index=73h, Default=00h)

Bit	Description
7-0	WDT Time-out value 7-0

WatchDog Timer Time-out Value (MSB) Register (Index=74h, Default=00h)

Bit Description

7-0 WDT Time-out value 15-8

A.2 ITE8781 Watchdog Timer Initial Program

.MODEL SMALL .CODE

Main:

CALL Enter_Configuration_mode CALL Check_Chip mov cl, 7 call Set_Logic_Device ;time setting mov cl, 10 ; 10 Sec dec al Watch_Dog_Setting: ;Timer setting mov al, cl mov cl, 73h call Superio_Set_Reg ;Clear by keyboard or mouse interrupt mov al, 0f0h mov cl, 71h call Superio_Set_Reg ;unit is second. mov al, 0C0H mov cl, 72h call Superio_Set_Reg ; game port enable mov cl, 9 call Set_Logic_Device Initial_OK: CALL Exit_Configuration_mode MOV AH,4Ch INT 21h

Enter_Configuration_Mode PROC NEAR MOV SI,WORD PTR CS:[Offset Cfg_Port]

MOV DX,02Eh MOV CX,04h Init_1: MOV AL,BYTE PTR CS:[SI] OUT DX,AL INC SI LOOP Init_1 RET Enter_Configuration_Mode ENDP

Exit_Configuration_Mode PROC NEAR MOV AX,0202h CALL Write_Configuration_Data RET Exit_Configuration_Mode ENDP

Check_Chip PROC NEAR

MOV AL,20h CALL Read_Configuration_Data CMP AL,87h JNE Not_Initial

MOV AL,21h CALL Read_Configuration_Data CMP AL,12h JNE Not_Initial

Need_Initial:

STC

RET

Not_Initial:

CLC RET Check_Chip ENDP Read_Configuration_Data PROC NEAR MOV DX,WORD PTR CS:[Cfg_Port+04h] OUT DX,AL MOV DX,WORD PTR CS:[Cfg_Port+06h] IN AL,DX RET Read_Configuration_Data ENDP Write_Configuration_Data PROC NEAR

MOV DX,WORD PTR CS:[Cfg_Port+04h] OUT DX,AL XCHG AL,AH MOV DX,WORD PTR CS:[Cfg_Port+06h] OUT DX,AL RET Write_Configuration_Data ENDP Superio_Set_Reg proc near push ax

MOV DX,WORD PTR CS:[Cfg_Port+04h]

Flexible	Embedded
Sy	stem

mov al,cl out dx,al pop ax inc dx out dx,al ret Superio_Set_Reg endp.Set_Logic_Device proc near

Set_Logic_Device proc near push ax push cx xchg al,cl mov cl,07h call Superio_Set_Reg pop cx pop ax ret Set_Logic_Device endp

;Select 02Eh->Index Port, 02Fh->Data Port Cfg_Port DB 087h,001h,055h,055h

DW 02Eh,02Fh

END Main

•

Note: Interrupt level mapping 0Fh-Dh: not valid 0Ch: IRQ12

03h: IRQ3 02h: not valid 01h: IRQ1 00h: no interrupt selected

Appendix B

I/O Information

Appendix B I/O Information B-1

FES-5130

B.1 I/O Address Map

Bit i/O / taal ooo iiit	Υ P
🗄 🛄 Input/output (IO)	
	Direct memory access controller
[00000000 - 00000CF7]	
[00000010 - 0000001F]	
	Programmable interrupt controller
[00000022 - 0000003F]	
[00000040 - 00000043]	
[00000044 - 0000005F]	
	Standard 101/102-Key or Microsoft Natural PS/2 Keyboard
[00000062 - 00000063]	
	Standard 101/102-Key or Microsoft Natural PS/2 Keyboard
[00000065 - 0000006F]	
	System CMOS/real time clock
[00000074 - 0000007F]	
	Direct memory access controller
	Direct memory access controller
	Programmable interrupt controller
	Direct memory access controller
[000001F0 - 000001F7]	이 이 것 같은 것
	Communications Port (COM4)
	Communications Port (COM2)
	Mobile Intel(R) 945 Express Chipset Family
	Mobile Intel(R) 945 Express Chipset Family
	Communications Port (COM3)
	Primary IDE Channel
(000003F8 - 000003FF]	Communications Port (COM1)
	Motherboard resources
	Motherboard resources
	Intel(R) 82801G (ICH7 Family) SMBus Controller - 27DA
	Motherboard resources
	ISAPNP Read Data Port
	PCI bus
] Intel(R) 82801G (ICH7 Family) PCI Express Root Port - 27D0
	Realtek PCIe GBE Family Controller
	Intel(R) 82801GBM/GHM (ICH7-M Family) Serial ATA Storage Controller - 27C4
	Intel(R) 82801GBM/GHM (ICH7-M Family) Serial ATA Storage Controller - 27C4
	Intel(R) 82801GBM/GHM (ICH7-M Family) Serial ATA Storage Controller - 27C4
	Intel(R) 82801GBM/GHM (ICH7-M Family) Serial ATA Storage Controller - 27C4
	Intel(R) 82801GBM/GHM (ICH7-M Family) Serial ATA Storage Controller - 27C4
] Intel(R) 82801G (ICH7 Family) Ultra ATA Storage Controllers - 27DF
	Intel(R) 82801G (ICH7 Family) USB Universal Host Controller - 27CB
] Intel(R) 82801G (ICH7 Family) USB Universal Host Controller - 27CA
] Intel(R) 82801G (ICH7 Family) USB Universal Host Controller - 27C9
	Intel(R) 82801G (ICH7 Family) USB Universal Host Controller - 27C8
[0000FF00 - 0000FF07]	Mobile Intel(R) 945 Express Chipset Family

Appendix B I/O Information B-2

FES-5130

B.2 Memory Address Map

Mei	00000000 - 0009FFFF] System board
	000A0000 - 000BFFFF] Mobile Intel(R) 945 Express Chipset Family
	000A0000 - 000BFFFF1 PCI bus
	000C0000 - 000DFFFF] PCI bus
	000E0000 - 000EFFFF] System board
	000F0000 - 000FFFFF] System board
	00100000 - 3F6DFFFF] System board
	[3F6E0000 - 3F6FFFFF] System board
	[3F700000 - FEBFFFFF] PCI bus
	[D0000000 - DFFFFFFF] Mobile Intel(R) 945 Express Chipset Family
	[E0000000 - EFFFFFF] Motherboard resources
	[FDB00000 - FDBFFFFF] Intel(R) 82801G (ICH7 Family) PCI Express Root Port - 27D0
	[FDBF0000 - FDBFFFFF] Realtek PCIe GBE Family Controller
	[FDE00000 - FDEFFFFF] Intel(R) 82801G (ICH7 Family) PCI Express Root Port - 27D0
	[FDEFF000 - FDEFFFFF] Realtek PCIe GBE Family Controller
	[FDF00000 - FDF7FFFF] Mobile Intel(R) 945 Express Chipset Family
	[FDF80000 - FDFBFFFF] Mobile Intel(R) 945 Express Chipset Family
	[FDFF8000 - FDFFBFFF] Microsoft UAA Bus Driver for High Definition Audio
6	[FDFFE000 - FDFFE3FF] Intel(R) 82801GBM/GHM (ICH7-M Family) Serial ATA Storage Controller - 27C
-	[FDFFF000 - FDFFF3FF] Intel(R) 82801G (ICH7 Family) USB2 Enhanced Host Controller - 27CC
	[FEC00000 - FEC00FFF] System board
	[FED13000 - FED1DFFF] System board
	[FED20000 - FED8FFFF] System board
	[FEE00000 - FEE00FFF] System board
	[FFB00000 - FFB7FFFF] System board
	[FFB80000 - FFBFFFFF] Intel(R) 82802 Firmware Hub Device
	[FFF00000 - FFFFFFFF] System board

B.3 IRQ Mapping Chart

🖻 🧰 Inte	errupt req	uest (IRQ)
	(ISA) 0	System timer
- 100	(ISA) 1	Standard 101/102-Key or Microsoft Natural PS/2 Keyboard
	(ISA) 3	Communications Port (COM2)
9	(ISA) 4	Communications Port (COM1)
	(ISA) 8	System CMOS/real time clock
	(ISA) 9	Microsoft ACPI-Compliant System
	(ISA) 10	Communications Port (COM4)
	(ISA) 11	Communications Port (COM3)
	(ISA) 13	Numeric data processor
6	(ISA) 14	Primary IDE Channel
	(PCI) 15	Intel(R) 82801G (ICH7 Family) SMBus Controller - 27DA
	(PCI) 16	Intel(R) 82801G (ICH7 Family) PCI Express Root Port - 27D0
4	(PCI) 16	Intel(R) 82801G (ICH7 Family) USB Universal Host Controller - 27CB
	(PCI) 16	Microsoft UAA Bus Driver for High Definition Audio
8	(PCI) 16	Mobile Intel(R) 945 Express Chipset Family
- H	(PCI) 16	Realtek PCIe GBE Family Controller
4	(PCI) 18	Intel(R) 82801G (ICH7 Family) USB Universal Host Controller - 27CA
	(PCI) 19	Intel(R) 82801G (ICH7 Family) USB Universal Host Controller - 27C9
6	(PCI) 19	Intel(R) 82801GBM/GHM (ICH7-M Family) Serial ATA Storage Controller - 27C4
- E	(PCI) 23	Intel(R) 82801G (ICH7 Family) USB Universal Host Controller - 27C8
÷	(PCI) 23	Intel(R) 82801G (ICH7 Family) USB2 Enhanced Host Controller - 27CC

B.4 DMA Channel Assignments

Direct memory access (DMA)