

ETX-CX700M

VIA C7 / Eden (V4) Series

Processor

ETX CPU Module

CRT/ LVDS LCD/ TTL LCD

10/100Base-TX Ethernet

HD Audio CODEC/ PCI

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Packing List

Before you begin installing your card, please make sure that the following materials have been shipped:

- 1 ETX-CX700M CPU Module
- 1 CD-ROM for manual (in PDF format) and drivers

If any of these items should be missing or damaged, please contact your distributor or sales representative immediately.

Application Notes

1. ISA IRQ resource:

If you use the ISA card, you have to disable the COM3 and COM4 in the BIOS due to the IRQ resource limitation. It will release the IRQ to the ISA card.

2. VGA through PCI graphic add-on card:

If you use a PCI graphic add-on card, you have to use the onboard VGA first. Then, adjust the PCI graphic add-on card to be the primary in the OS. After re-booting, you can use the VGA through PCI graphic add-on card normally.

For more details, please contact with AAEON Application Engineering Department for help.

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Chapter

1

**General
Information**

1.1 Introduction

AAEON announced the new **Embedded Technology eXtended (ETX)** product – **ETX-CX700M**, which is based on the VIA C7/ Eden (V4) series processors combined with VIA CX700M chipset.

ETX-CX700M provides common PC peripheral functions such as graphics, USB, serial, parallel port, keyboard/mouse, Ethernet, SATA and IDE. The baseboard designer can optimize exactly which and how these functions are implemented physically.

All of AAEON's ETX modules have a standard form factor and a standard connector layout that carry a specified set of signals. By adopting this standardization, the designers can create a single system of carrier board that can accept present and future ETX modules in terms of their needs. In another word, AAEON will have different ETX solutions so that customers can upgrade the module without having to change their carrier board.

The ETX-CX700M was designed specially to improve the quality and speed of your product development. AAEON ETX series represent features of scalability, reliability and qualified services. It provides more compact size and more flexibility for your various applications as well.

1.2 Features

- VIA C7/ Eden Processors
- VIA CX700M (Single Chip)
- Support Non-ECC DDRII 400/533 Memory
- Support CRT/ Up to 24-bit Dual-channel LVDS LCD/ TV/ DVI
- 10/100 Base-TX Ethernet
- HD Audio CODEC
- Two SATAII Connectors Support RAID 0/1
- One Video Capture Port (Optional)
- +5V Operation Voltage
- Meet ETX 3.0

1.3 Specifications

System

- CPU VIA C7./ Eden. (V4 Bus) series processors up to 2.0GHz, FSB 533MHz
- Memory 200 pin DDR II SODIMM x 1, Max. 1GB (DDR II 400/533)
- Chipset VIA CX700M
- Ethernet Realtek RTL8139DL, 10/100Base-TX
- BIOS Award
- Watchdog Timer Generates a time-out system reset
- H/W Status Monitoring Support power supply voltages, fan speed and temperature monitoring
- EEPROM Microchip 24LCS52-I/SN, save BIOS and configuration data (Optional)
- Wake on LAN Yes
- Expansion Interface 32-bit PCI x 4
8-bit/ 16-bit ISA x 1
SMBus x 1
I2C x 1
- Power Supply Voltage +5V DC
- Board Size 4.5" (L) x 3.75" (W) (114mm x 95mm)
- Gross Weight 0.66 lb (0.3Kg)
- Operating Temperature 32°F~ 140°F (0°C ~ 60°C)

Display

- Chipset VIA CX700M integrated
- Memory Shared system memory up to 128MB
- Resolution Up to 1600 x 1200 @ 32bpp for CRT
Up to 1600 x 1200 @ 24bpp for LCD
- LCD Interface Up to 24-bit dual-channel LVDS TFT
LCD

I/O

- Storage PATA x 1 (two devices), SATA II x 2 (on module)
- Parallel Port 1
- Serial Port 2 (COM1:RS-232; COM2: RS-232/422/485)
- IrDA One IrDA Tx/Rx header
- Audio Mic-in, Line-in, Line-out
- USB USB 2.0 x 4

Chapter

2

**Quick
Installation
Guide**

2.1 Safety Precautions

Warning!

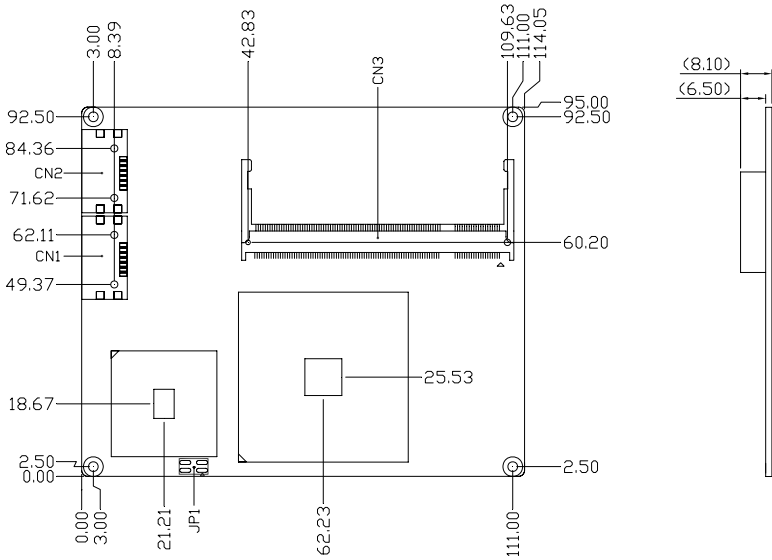
Always completely disconnect the power cord from your board whenever you are working on it. Do not make connections while the power is on, because a sudden rush of power can damage sensitive electronic components.

Caution!

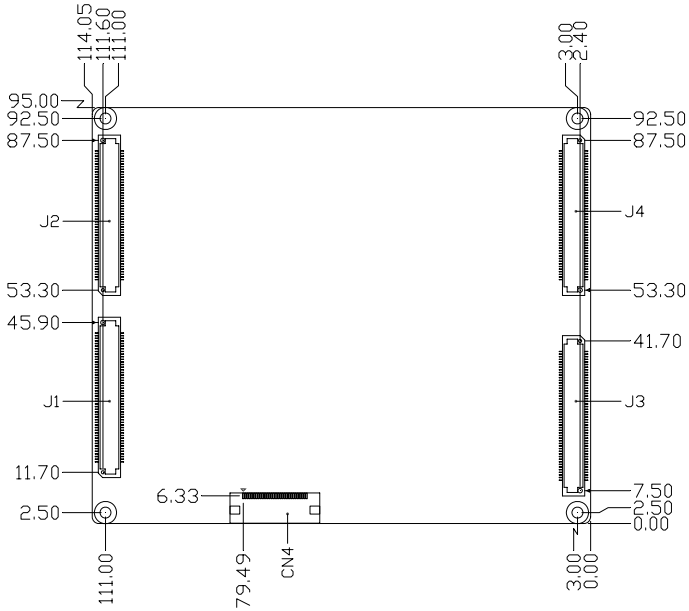
Always ground yourself to remove any static charge before touching the board. Modern electronic devices are very sensitive to static electric charges. Use a grounding wrist strap at all times. Place all electronic components on a static-dissipative surface or in a static-shielded bag when they are not in the chassis

2.2 Location of Connectors/Jumpers and Mechanical Drawings

Component Side



Solder Side



2.3 List of Jumpers

Jumpers allow users to manually customize system configurations to their suitable application needs. The following chart consist the list of each jumper function:

Label	Function
JP1	CRT/TV selection header

2.4 List of Connectors

The board has a number of connectors that allow you to configure your system to suit your application. The table below shows the function of each board's connectors:

Label	Function
CN1	SATA Connector
CN2	SATA Connector
CN3	DDRII SODIMM Slot
CN4	Video Capture Port Connector (Optional)
J1	PCI / USB / Audio
J2	ISA
J3	VGA / LCD / Video out / COMs / LPT / FDD / IrDA / Mouse / Keyboard
J4	IDE 1 / IDE 2 / Miscellaneous (ETX-CX700M only supports IDE 1 channel)

2.5 CRT/TV Selection Header (JP1)

JP1	Function
1-2	CRT(Default)
3-4	TV

2.6 Video Capture Port Connector (CN4) (Optional)

Pin	Signal	Pin	Signal
1	VCP0D0	2	VCP0D1
3	VCP0D2	4	VCP0D3
5	VCP0D4	6	VCP0D5
7	VCP0D6	8	VCP0D7
9	SPCLK	10	VCP1D0
11	VCP1D1	12	VCP1D2
13	VCP1D3	14	VCP1D4
15	VCP1D5	16	VCP1D6
17	VCP1D7	18	SPD
19	VCP0HS	20	VCP0VS
21	GND	22	VCP0CLK
23	VCP1CLK	24	GND
25	DVP0DE	26	VCP1HS
27	VCP1VS	28	TS1ERR
29	+5V	30	+3.3V

2.7 ETX Connector (J1)

Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
1	GND	2	GND	51	VCC	52	VCC
3	PCICLK3	4	PCICLK4	53	PAR	54	SERR#
5	GND	6	GND	55	PERR#3	56	NC
7	PCICLK1	8	PCICLK2	57	PCI_PME#	58	USB2N
9	REQ3#	10	GNT3#	59	PLOCK#	60	DEVSEL#
11	GNT2#	12	3V	61	TRDY#	62	USB3N
13	REQ2#	14	GNT1#	63	IRDY#	64	STOP#
15	REQ1#	16	3V	65	FRAME#	66	USB2P
17	GNT0#	18	NC	67	GND	68	GND
19	VCC	20	VCC	69	AD16	70	CBE2#
21	DREQ2/ SERIRQ	22	REQ0#	71	AD17	72	USB3P
23	AD0	24	3V	73	AD19	74	AD18
25	AD1	26	AD2	75	AD20	76	USB0N
27	AD4	28	AD3	77	AD22	78	AD21
29	AD6	30	AD5	79	AD23	80	USB1N
31	CBE0#	32	AD7	81	AD24	82	CBE3#
33	AD8	34	AD9	83	VCC	84	VCC
35	GND	36	GND	85	AD25	86	AD26
37	AD10	38	LIN_L	87	AD28	88	USB0P
39	AD11	40	MIC_IN	89	AD27	90	AD29
41	AD12	42	LIN_R	91	AD30	92	USB1P
43	AD13	44	VCCAUD	93	PCIRST#	94	AD31
45	AD14	46	LOUT_L	95	INTC#	96	INTD#
47	AD15	48	GNDAUD	97	INTA#	98	INTB#
49	CBE1#	50	LOUT_R	99	GND	100	GND

2.8 ETX Connector (J2)

Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
1	GND	2	GND	51	VCC	52	VCC
3	SD14	4	SD15	53	SA6	54	IRQ5
5	SD13	6	MASTER#	55	SA7	56	IRQ6
7	SD12	8	DREQ7	57	SA8	58	IRQ7
9	SD11	10	DACK7#	59	SA9	60	SYSCLK
11	SD10	12	DREQ6	61	SA10	62	REFSH#
13	SD9	14	DACK6#	63	SA11	64	DREQ1
15	SD8	16	DREQ5	65	SA12	66	DACK1#
17	MEMW#	18	DACK#5	67	GND	68	GND
19	MEMR#	20	DREQ0	69	SA13	70	DREQ3
21	LA17	22	DACK0#	71	SA14	72	DACK3#
23	LA18	24	IRQ14	73	SA15	74	IOR#
25	LA19	26	IRQ15	75	SA16	76	LOW#
27	LA20	28	IRQ12	77	SA18	78	SA17
29	LA21	30	IRQ11	79	SA19	80	SMEMR#
31	LA22	32	IRQ10	81	IOCHRDY	82	AEN
33	LA23	34	IO16#	83	VCC	84	VCC
35	GND	36	GND	85	SD0	86	SMEMW#
37	SBHE#	38	M16#	87	SD2	88	SD1
39	SA0	40	OSC	89	SD3	90	NOWS#
41	SA1	42	BALE	91	DREQ2	92	SD4
43	SA2	44	TC	93	SD5	94	IRQ9
45	SA3	46	DACK2#	95	SD6	96	SD7
47	SA4	48	IRQ3	97	IOCHK#	98	RSTDRV
49	SA5	50	IRQ4	99	GND	100	GND

2.9 ETX Connector (J3) -- Parallel Prot Mode Pinout

Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
1	GND	2	GND	51	LPT/FLPY#	52	NC
3	R	4	B	53	VCC	54	GND
5	HSY	6	G	55	STB#	56	AFD#
7	VSY	8	CRT_DDCCLK	57	NC	58	PD7
9	NC	10	CRT_DDCDAT	59	IRRX	60	ERR#
11	TXCLK1#	12	TXOUT13#	61	IRTX	62	PD6
13	TXCLK1	14	TXOUT13	63	RXD2	64	INIT#
15	GND	16	GND	65	GND	66	GND
17	TXOUT11	18	TXOUT12	67	RTS2#	68	PD5
19	TXOUT11#	20	TXOUT12#	69	DTR2#	70	SLIN#
21	GND	22	GND	71	DCD2#	72	PD4
23	TXOUT03#	24	TXOUT10	73	DSR2#	74	PD3
25	TXOUT03	26	TXOUT10#	75	CTS2#	76	PD2
27	GND	28	GND	77	TXD2#	78	PD1
29	TXOUT02#	30	TXCLK0	79	RI2#	80	PD0
31	TXOUT02	32	TXCLK0#	81	VCC	82	VCC
33	GND	34	GND	83	RXD1	84	ACK#
35	TXOUT00	36	TXOUT01	85	RTS1#	86	BUSY#
37	TXOUT00#	38	TXOUT01#	87	DTR1#	88	PE
39	VCC	40	VCC	89	DCD1#	90	SLCT#
41	LVDS_DDCPDATA	42	DVOCVSYNC	91	DSR1#	92	MSCLK
43	LVDS_DDCPCLK	44	LVDS_BKLEN	93	CCTS1#	94	MSDAT
45	LVDS_BKLCTL	46	LVDS_DIGON	95	TXD1#	96	KBCLK
47	CVBS	48	Y	97	RI1#	98	KBDAT
49	CSYNC	50	C	99	GND	100	GND

ETX Connector (J3) -- Floppy Support Mode Pinout

Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
1	GND	2	GND	51	LPT/FLPY#	52	NC
3	R	4	B	53	VCC	54	GND
5	HSY	6	G	55	NC	56	DENSEL
7	VSY	8	CRT_DDCCLK	57	NC	58	NC
9	NC	10	CRT_DDCDAT	59	IRRX	60	HDSEL#
11	TXCLK1#	12	TXOUT13#	61	IRTX	62	NC
13	TXCLK1	14	TXOUT13	63	RXD2	64	DIR#
15	GND	16	GND	65	GND	66	GND
17	TXOUT11	18	TXOUT12	67	RTS2#	68	NC
19	TXOUT11#	20	TXOUT12#	69	DTR2#	70	STEP#
21	GND	22	GND	71	DCD2#	72	DSKCHG#
23	TXOUT03#	24	TXOUT10	73	DSR2#	74	RDATA#
25	TXOUT03	26	TXOUT10#	75	CTS2#	76	WP#
27	GND	28	GND	77	TXD2#	78	TRKO#
29	TXOUT02#	30	TXCLK0	79	RI2#	80	INDEX#
31	TXOUT02	32	TXCLK0#	81	VCC	82	VCC
33	GND	34	GND	83	RXD1	84	DRV#
35	TXOUT00	36	TXOUT01	85	RTS1#	86	MOT#
37	TXOUT00#	38	TXOUT01#	87	DTR1#	88	WDATA#
39	VCC	40	VCC	89	DCD1#	90	WGATE#
41	LVDS_DDCPDATA	42	DVOCVSYNC	91	DSR1#	92	MSCLK
43	LVDS_DDCPCLK	44	LVDS_BKLEN	93	CCTS1#	94	MSDAT
45	LVDS_BKLCTL	46	LVDS_DIGON	95	TXD1#	96	KBCLK
47	CVBS	48	Y	97	RI1#	98	KBDAT
49	CSYNC	50	C	99	GND	100	GND

2.10 ETX Connector (J4)

Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
1	GND	2	GND	51	SIDE_IOW#	52	PIDE_IOR#
3	5V_SB	4	RSTIN#	53	SIDE_DRQ	54	PIDE_IOW#
5	PS_ON#	6	SPEAKER	55	SIDE_D15	56	PIDE_DRQ
7	PWRBTN#	8	BAT	57	SIDE_D0	58	PIDE_D15
9	FAN_TAC	10	LILED	59	SIDE_D14	60	PIDE_D0
11	NC	12	ACTLED	61	SIDE_D1	62	PIDE_D14
13	NC	14	SPEEDLED	63	SIDE_D13	64	PIDE_D1
15	NC	16	I2CLK	65	GND	66	GND
17	VCC	18	VCC	67	SIDE_D2	68	PIDE_D13
19	DACK2/ OVCR#	20	GPIO0	69	SIDE_D12	70	PIDE_D2
21	EXTSMI#	22	I2DAT	71	SIDE_D3	72	PIDE_D12
23	SMBCLK	24	SMBDATA	73	SIDE_D11	74	PIDE_D3
25	SIDE_CS3#	26	CPUFAN/NC	75	SIDE_D4	76	PIDE_D11
27	SIDE_CS1#	28	VCC	77	SIDE_D10	78	PIDE_D4
29	SIDE_A2	30	PIDE_CS3#	79	SIDE_D5	80	PIDE_D10
31	SIDE_A0	32	PIDE_CS1#	81	VCC	82	VCC
33	GND	34	GND	83	SIDE_D9	84	PIDE_D5
35	P66DET/ S66DET	36	PIDE_A2	85	SIDE_D6	86	PIDE_D9
37	SIDE_A1	38	PIDE_A0	87	SIDE_D8	88	PIDE_D6
39	SIDE_INTRQ	40	PIDE_A1	89	RING#	90	P66DET
41	S66DET/ NC	42	NC	91	RXD#	92	PIDE_D8
43	SIDE_ACK#	44	PIDE_INTRQ	93	RXD	94	SIDE_D7
45	SIDE_RDY	46	PIDE_ACK#	95	TXD#	96	PIDE_D7
47	SIDE_IOR#	48	PIDE_RDY	97	TXD	98	HDRST#
49	VCC	50	VCC	99	GND	100	GND

Below Table for China RoHS Requirements

产品中有毒有害物质或元素名称及含量

AAEON Main Board/ Daughter Board/ Backplane

部件名称	有毒有害物质或元素					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (Cr(VI))	多溴联苯 (PBB)	多溴二苯醚 (PBDE)
印刷电路板 及其电子组件	×	○	○	○	○	○
外部信号 连接器及线材	×	○	○	○	○	○
<p>O: 表示该有毒有害物质在该部件所有均质材料中的含量均在 SJ/T 11363-2006 标准规定的限量要求以下。</p> <p>X: 表示该有毒有害物质至少在该部件的某一均质材料中的含量超出 SJ/T 11363-2006 标准规定的限量要求。</p> <p>备注: 此产品所标示之环保使用期限, 系指在一般正常使用状况下。</p>						

Chapter

3

**Award
BIOS Setup**

3.1 System Test and Initialization

These routines test and initialize board hardware. If the routines encounter an error during the tests, you will either hear a few short beeps or see an error message on the screen. There are two kinds of errors: fatal and non-fatal. The system can usually continue the boot up sequence with non-fatal errors. Non-fatal error messages usually appear on the screen along with the following instructions:

Press <F1> to RESUME

Write down the message and press the F1 key to continue the boot up sequence.

System configuration verification

These routines check the current system configuration against the values stored in the CMOS memory. If they do not match, the program outputs an error message. You will then need to run the BIOS setup program to set the configuration information in memory.

There are three situations in which you will need to change the CMOS settings:

1. You are starting your system for the first time
2. You have changed the hardware attached to your system
3. The CMOS memory has lost power and the configuration information has been erased.

The ETX-CX700M memory has an integral lithium battery backup for data retention. However, you will need to replace the complete unit when it finally runs down.

3.2 Award BIOS Setup

Awards BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed CMOS RAM so that it retains the Setup information when the power is turned off.

Entering setup

Power on the computer and press immediately. This will allow you to enter Setup.

Standard CMOS Features

Use this menu for basic system configuration. (Date, time, IDE, etc.)

Advanced BIOS Features

Use this menu to set the advanced features available on your system.

Advanced Chipset Features

Use this menu to change the values in the chipset registers and optimize your system performance.

Integrated Peripherals

Use this menu to specify your settings for integrated peripherals. (Primary slave, secondary slave, keyboard, mouse etc.)

Power Management Setup

Use this menu to specify your settings for power management. (HDD power down, power on by ring etc.)

PnP/PCI Configurations

This entry appears if your system supports PnP/PCI.

PC Health Status

This menu shows you the status of PC.

Frequency/Voltage Control

This menu shows you the display of frequency/Voltage Control.

Load Fail-Safe Defaults

Use this menu to load the BIOS default values for the minimal/stable performance for your system to operate.

Load Optimized Defaults

Use this menu to load the BIOS default values that are factory settings for optimal performance system operations. While AWARD has designated the custom BIOS to maximize performance, the factory has the right to change these defaults to meet their needs.

Set Supervisor/User Password

Use this menu to set Supervisor/User Passwords.

Save and Exit Setup

Save CMOS value changes to CMOS and exit setup.

Exit Without Saving

Abandon all CMOS value changes and exit setup.

For more detailed information, you can refer to the "AAEON BIOS Item Description.pdf" file in the CD for the meaning of each setting in this chapter.

Chapter

4

**Driver
Installation**

The ETX-CX700M comes with an AutoRun CD-ROM that contains all drivers and utilities that can help you to install the driver automatically.

Insert the driver CD, the driver CD-title will auto start and show the installation guide. If not, please follow the sequence below to install the drivers.

Follow the sequence below to install the drivers:

- Step 1 – Install Chipset Driver
- Step 2 – Install VGA Driver
- Step 3 – Install LAN Driver
- Step 4 – Install Audio Driver
- Step 5 – Install PCI to ISA Bridge Driver
- Step 6 – Install RAID Driver

USB 2.0 Drivers are available for download using Windows® Update for both Windows® XP and Windows® 2000. For additional information regarding USB 2.0 support in Windows® XP and Windows® 2000, please visit www.microsoft.com/hwdev/usb/.

Please read instructions below for further detailed installations.

4.1 Installation:

Insert the ETX-CX700M CD-ROM into the CD-ROM drive. And install the drivers from Step 1 to Step 6 in order.

Step 1 – Install Chipset Driver

1. Click on the **Step 1 – Chipset** folder and double click on the **Setup.exe**
2. Follow the instructions that the window shows
3. The system will help you install the driver automatically

Step 2 – Install VGA Driver

1. Click on the **Step 2 – VGA** folder and double click on the **setup.exe** file
2. Follow the instructions that the window shows
3. The system will help you install the driver automatically

Step 3 –Install LAN Driver

1. Click on the **Step 3 – LAN** folder and double click on the **Setup.exe** file
2. Follow the instructions that the window shows
3. The system will help you install the driver automatically

Step 4 –Install Audio Driver

1. Click on the **Step 4 –Audio** folder and double click on

Setup.exe file

2. Follow the instructions that the window shows
3. The system will help you install the driver automatically

Step 5 –Install PCI to ISA Bridge Driver

Place the Driver CD-ROM into your CD-ROM drive and follow the following steps to install.

1. Click on **Start** button
2. Click on **Settings** button
3. Click on **Control Panel** button
4. Click on **System** button
5. Select **Hardware** and click on **Device Manager...**
6. Double click on **Other PCI Bridge Device**
7. Click on **Update Driver...**
8. Click on **Next**
9. Select **Search for a suitable driver...**, then click on **Next**
10. Select **Specify a location**, then click on **Next**
11. Click on **Browse**
12. Select "**Ite**" file from CD-ROM (**Driver/Step 5- PCI to ISA Bridge**) then click on **Open**
13. Click on **OK**
14. Click on **Next**
15. Click on **Finish**

Step 6 –Install RAID Driver

1. Click on the **Step 6 –RAID** folder and double click on the **SETUP.exe** file
2. Follow the instructions that the window shows
3. The system will help you install the driver automatically

Appendix

A

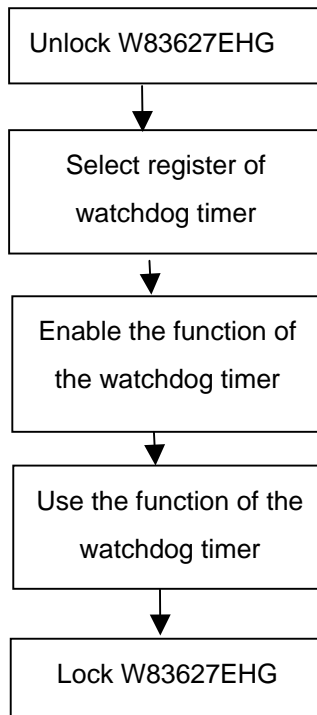
Programming the Watchdog Timer

A.1 Programming

ETX-CX700M utilizes W83627EHG chipset as its watchdog timer controller.

Below are the procedures to complete its configuration and the AAEMON initial watchdog timer program is also attached based on which you can develop customized program to fit your application.

Configuring Sequence Description



There are three steps to complete the configuration setup:

- (1) Enter the W83627EHG config Mode
- (2) Modify the data of configuration registers

- (3) Exit the W83627EHG config Mode. Undesired result may occur if the config Mode is not exited normally.

(1) Enter the W83627EHG config Mode

To enter the W83627EHG config Mode, two special I/O write operations are to be performed during Wait for Key state. To ensure the initial state of the key-check logic, it is necessary to perform two write operations to the Special Address port (2EH). The different enter keys are provided to select configuration ports (2Eh/2Fh) of the next step.

	Address Port	Data Port
87h,87h:	2Eh	2Fh

(2) Modify the Data of the Registers

All configuration registers can be accessed after entering the config Mode. Before accessing a selected register, the content of Index 07h must be changed to the LDN to which the register belongs, except some Global registers.

(3) Exit the W83627EHG config Mode

The exit key is provided to select configuration ports (2Eh/2Fh) of the next step.

	Address Port	Data Port
0aah:	2Eh	2Fh

WatchDog Timer Register I (Index=F5h, Default=00h)

CRF5 (PLED mode register. Default 0 x 00)

Bit 7-6 : select PLED mode

= 00 Power LED pin is tri-stated.

= 01 Power LED pin is driven low.

= 10 Power LED pin is a 1Hz toggle pulse with 50 duty cycle.

= 11 Power LED pin is a 1/4Hz toggle pulse with 50 duty cycle.

Bit 5-4 : Reserved

Bit 3 : select WDTO count mode.

= 0 second

= 1 minute

Bit 2 : Enable the rising edge of keyboard Reset (P20) to force Time-out event.

= 0 Disable

= 1 Enable

Bit 1-0 : Reserved

WatchDog Timer Register II (Index=F6h, Default=00h)

Bit 7-0 = 0 x 00 Time-out Disable

= 0 x 01 Time-out occurs after 1
second/minute

= 0 x 02 Time-out occurs after 2
second/minutes

= 0 x 03 Time-out occurs after 3
second/minutes

.....

= 0 x FF Time-out occurs after 255
second/minutes

WatchDog Timer Register III (Index=F7h, Default=00h)

- Bit 7** : Mouse interrupt reset Enable or Disable
- = 1 Watchdog Timer is reset upon a Mouse interrupt
 - = 0 Watchdog Timer is not affected by Mouse interrupt
- Bit 6** : Keyboard interrupt reset Enable or Disable
- = 1 Watchdog Timer is reset upon a Keyboard interrupt
 - = 0 Watchdog Timer is not affected by Keyboard interrupt
- Bit 5** : Force Watchdog Timer Time-out. Write Only
- = 1 Force Watchdog Timer time-out event: this bit is self-clearing
- Bit 4** : Watchdog Timer Status. R/W
- = 1 Watchdog Timer time-out occurred
 - = 0 Watchdog Timer counting
- Bit 3-0** : These bits select IRQ resource for Watchdog. Setting of 2 selects SMI.

A.2 W83627EHG Watchdog Timer Initial Program

Example: Setting 10 sec. as Watchdog timeout interval

;;

Mov dx,2eh ;Enter W83627EHG config mode

Mov al,87h (out 87h to 2eh twice)

Out dx,al

Out dx,al

;;

Mov al,07h

Out dx,al

Inc dx

Mov al,08h ;Select Logical Device 8 (GPIO Port
2)

Out dx,al

;;

Dec dx

Mov al,30h ;CR30 (GP20~GP27)

Out dx,al

Inc dx

Mov al,01h ;Activate GPIO2

Out dx,al

```
;/;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;/
Dec dx
Mov al,0f5h           ;CRF5 (PLED mode register)
Out dx,al
Inc dx
In al,dx
And al,not 08h       ;Set second as counting unit
Out dx,al
;/;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;/
Dec dx
Mov al,0f6h           ; CRF6
Out dx,al
Inc dx
Mov al,10             ;Set timeout interval as 10 sec.
Out dx,al
;/;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;/
Dec dx                 ;Exit W83627EHG config mode
Mov al,0aah           (out 0aah to 2eh once)
Out dx,al
;/;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;/
```


Appendix

B

I/O Information

B.2 1st MB Memory Address Map

Address Range	Device
[00000000 - 0009FFFF]	System board
[000A0000 - 000BFFFF]	PCI bus
[000A0000 - 000BFFFF]	PCI bus
[000A0000 - 000BFFFF]	VIA CPU to AGP Controller
[000A0000 - 000BFFFF]	VIA/S3G UniChrome Pro II IGP
[000C0000 - 000DFFFF]	PCI bus
[000F0000 - 000FFFFF]	System board
[00100000 - 3BEDFFFF]	System board
[3BEE0000 - 3BEFFFFF]	System board
[3BF00000 - 9FEFFFFF]	PCI bus
[9FF00000 - 9FFFFFFF]	PCI bus
[9FFFC000 - 9FFFFFFF]	Microsoft UAA Bus Driver for High Definition Audio
[A0000000 - BFFFFFFF]	VIA CPU to AGP Controller
[A0000000 - BFFFFFFF]	VIA/S3G UniChrome Pro II IGP
[A0000000 - FEBFFFFF]	PCI bus
[D0000000 - D7FFFFFF]	VIA CPU to AGP Controller
[DD000000 - DDFFFFFF]	VIA/S3G UniChrome Pro II IGP
[DD000000 - DEFFFFFF]	VIA CPU to AGP Controller
[DFE00000 - DFE000FF]	Realtek RTL8139 Family PCI Fast Ethernet NIC
[DFFFF000 - DFFFF0FF]	VIA USB Enhanced Host Controller
[E0000000 - EFFFFFFF]	Motherboard resources
[F0000000 - F0000FFF]	Motherboard resources
[FEC00000 - FEC00FFF]	System board
[FEE00000 - FEE00FFF]	System board
[FFF80000 - FFFFFFFF]	System board
[FFFF0000 - FFFFFFFF]	System board

B.3 IRQ Mapping Chart

IRQ	Device
(ISA) 0	System timer
(ISA) 1	Standard 101/102-Key or Microsoft Natural PS/2 Keyboard
(ISA) 3	Communications Port (COM2)
(ISA) 4	Communications Port (COM1)
(ISA) 6	Standard floppy disk controller
(ISA) 8	System CMOS/real time clock
(ISA) 9	Microsoft ACPI-Compliant System
(ISA) 10	Communications Port (COM3)
(ISA) 11	Communications Port (COM4)
(ISA) 12	PS/2 Compatible Mouse
(ISA) 13	Numeric data processor
(ISA) 14	Primary IDE Channel
(ISA) 15	Secondary IDE Channel
(PCI) 16	Realtek RTL8139 Family PCI Fast Ethernet NIC
(PCI) 16	VIA/S3G UniChrome Pro II IGP
(PCI) 17	Microsoft UAA Bus Driver for High Definition Audio
(PCI) 20	VIA Rev 5 or later USB Universal Host Controller
(PCI) 21	VIA Rev 5 or later USB Universal Host Controller
(PCI) 22	VIA Rev 5 or later USB Universal Host Controller
(PCI) 23	VIA USB Enhanced Host Controller

Memory

B.4 DMA Channel Assignments

DMA Channel	Device
2	Standard floppy disk controller
4	Direct memory access controller

Input/output (IO)

Interrupt request (IRQ)

Memory