

# EPIC-QM57

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EPIC Board

User's Manual 6<sup>th</sup> Ed

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## Packing List

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Before setting up your product, please make sure the following items have been shipped:

Item	Quantity
● EPIC-BDU7	1
● Jumper cap (9657666600)	1
● Product DVD with User's Manual (in pdf) and drivers	1

If any of these items are missing or damaged, please contact your distributor or sales representative immediately.

## About this Document

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This User's Manual contains all the essential information, such as detailed descriptions and explanations on the product's hardware and software features (if any), its specifications, dimensions, jumper/connector settings/definitions, and driver installation instructions (if any), to facilitate users in setting up their product.

Users may refer to the [AAEON.com](http://AAEON.com) for the latest version of this document.

## Safety Precautions

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Please read the following safety instructions carefully. It is advised that you keep this manual for future references

1. All cautions and warnings on the device should be noted.
2. Make sure the power source matches the power rating of the device.
3. Position the power cord so that people cannot step on it. Do not place anything over the power cord.
4. Always completely disconnect the power before working on the system's hardware.
5. No connections should be made when the system is powered as a sudden rush of power may damage sensitive electronic components.
6. If the device is not to be used for a long time, disconnect it from the power supply to avoid damage by transient over-voltage.
7. Always disconnect this device from any AC supply before cleaning.
8. While cleaning, use a damp cloth instead of liquid or spray detergents.
9. Make sure the device is installed near a power outlet and is easily accessible.
10. Keep this device away from humidity.
11. Place the device on a solid surface during installation to prevent falls
12. Do not cover the openings on the device to ensure optimal heat dissipation.
13. Watch out for high temperatures when the system is running.
14. Do not touch the heat sink or heat spreader when the system is running
15. Never pour any liquid into the openings. This could cause fire or electric shock.
16. As most electronic components are sensitive to static electrical charge, be sure to ground yourself to prevent static charge when installing the internal components. Use a grounding wrist strap and contain all electronic components in any static-shielded containers.

17. If any of the following situations arises, please the contact our service personnel:
  - i. Damaged power cord or plug
  - ii. Liquid intrusion to the device
  - iii. Exposure to moisture
  - iv. Device is not working as expected or in a manner as described in this manual
  - v. The device is dropped or damaged
  - vi. Any obvious signs of damage displayed on the device
18. **DO NOT LEAVE THIS DEVICE IN AN UNCONTROLLED ENVIRONMENT WITH TEMPERATURES BEYOND THE DEVICE'S PERMITTED STORAGE TEMPERATURES (SEE CHAPTER 1) TO PREVENT DAMAGE.**

### **Warning!**



This device complies with Part 15 FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received including interference that may cause undesired operation.

### **Caution:**

*There is a danger of explosion if the battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions and your local government's recycling or disposal directives.*

### **Attention:**

*Il y a un risque d'explosion si la batterie est remplacée de façon incorrecte. Ne la remplacer qu'avec le même modèle ou équivalent recommandé par le constructeur. Recycler les batteries usées en accord avec les instructions du fabricant et les directives gouvernementales de recyclage.*



## China RoHS Requirements (CN)

产品中有毒有害物质或元素名称及含量

AAEON Main Board/ Daughter Board/ Backplane

部件名称	有毒有害物质或元素					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (Cr(VI))	多溴联苯 (PBB)	多溴二苯醚 (PBDE)
印刷电路板 及其电子组件	○	○	○	○	○	○
外部信号 连接器及线材	○	○	○	○	○	○
<p>○: 表示该有毒有害物质在该部件所有均质材料中的含量均在 SJ/T 11363-2006 标准规定的限量要求以下。</p> <p>X: 表示该有毒有害物质至少在该部件的某一均质材料中的含量超出 SJ/T 11363-2006 标准规定的限量要求。</p> <p>备注: 此产品所标示之环保使用期限, 系指在一般正常使用状况下。</p>						

## China RoHS Requirement (EN)

Poisonous or Hazardous Substances or Elements in Products

AAEON Main Board/ Daughter Board/ Backplane

Component	Poisonous or Hazardous Substances or Elements					
	Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (Cr(VI))	Polybrominated Biphenyls (PBB)	Polybrominated Diphenyl Ethers (PBDE)
PCB & Other Components	○	○	○	○	○	○
Wires & Connectors for External Connections	○	○	○	○	○	○
<p>O: The quantity of poisonous or hazardous substances or elements found in each of the component's parts is below the SJ/T 11363-2006-stipulated requirement.</p> <p>X: The quantity of poisonous or hazardous substances or elements found in at least one of the component's parts is beyond the SJ/T 11363-2006-stipulated requirement.</p> <p><b>Note:</b> The Environment Friendly Use Period as labeled on this product is applicable under normal usage only</p>						

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# Chapter 1

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Product Specifications

## 1.1 Specifications

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### System

● Form Factor	EPIC Board
● Processor	Intel® Core™ i5/i7 (socket-988)
● System Memory	204-pin SODIMM DDR3 800/1066, up to 4 GB (8 GB optional)
● Chipset	Intel® QM57
● I/O Chipset	ITE8781
● Ethernet	10/100/1000Base-TX x 2 (Intel® 82577LM and Intel® 82574L respectively), RJ-45 x 2
● BIOS	AMI Plug & Play BIOS – 8MB flash
● Wake On LAN	Yes
● Watchdog Timer	Generates a time-out system reset
● H/W Status Monitoring	Supports power supply voltages, fan speed, and temperature monitoring
● Expansion Interface	PCI/104-Express Expansion (PCI-104 + PCIe/104 (Optional)) connector
● Battery	Lithium battery
● Power Requirement	DC 12 V
● Board Size	165 x 115 mm (6.5 x 4.53")
● Gross Weight	0.5 kg (1.1 lbs)

- Operating Temperature 0 ~ 60°C (32 ~ 140°F)
- Storage Temperature -40 ~ 80°C (-40 ~ 176°F)
- Operation Humidity 0 ~ 90% Relative Humidity, Non-Condensing

## Display

- Chipset Intel® CPU integrated VGA  
Integrated Graphics
- Memory Shared system memory up to 512 MB
- Resolution CRT up to 2048 x 1536  
LCD/ DVI/DisplayPort up to 1920 x 1200
- LCD Interface 24-bit dual channel LVDS
- Display Combination CRT/LCD simultaneous/ dual view display

## I/O

- Storage SATA 2 x 2 (Support RAID 0,1)  
Type2 CompactFlash™ x 1
- USB USB 2.0 x 6
- Serial Port RS-232 x 3  
RS-232/422/485 x 1
- Parallel Port SPP/EPP/ECP
- DI/O Supports 8-bit (programmable)
- Audio Line-in, Line-out, Mic-in & CD-in
- Keyboard/ Mouse PS/2 Keyboard & Mouse

# Chapter 2

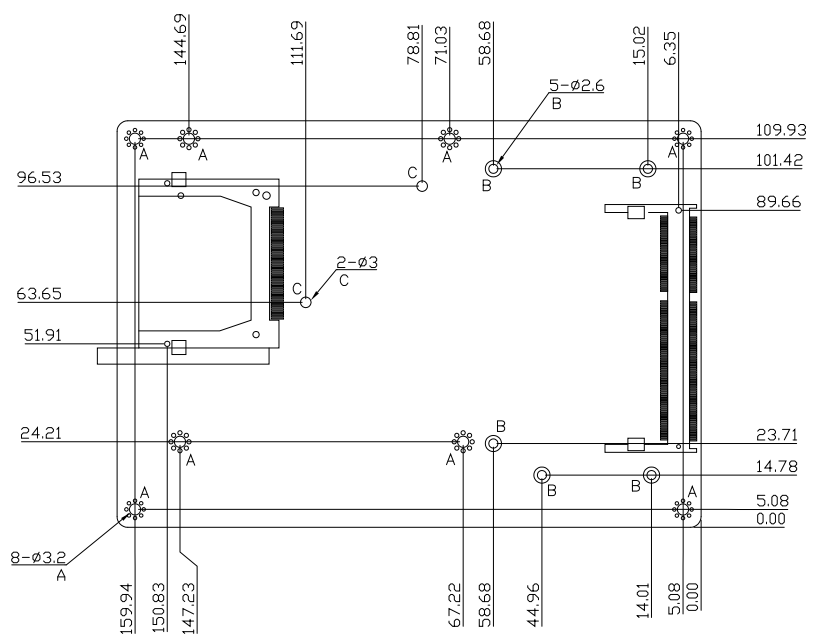
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Hardware Information



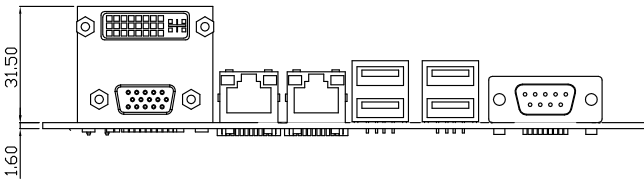
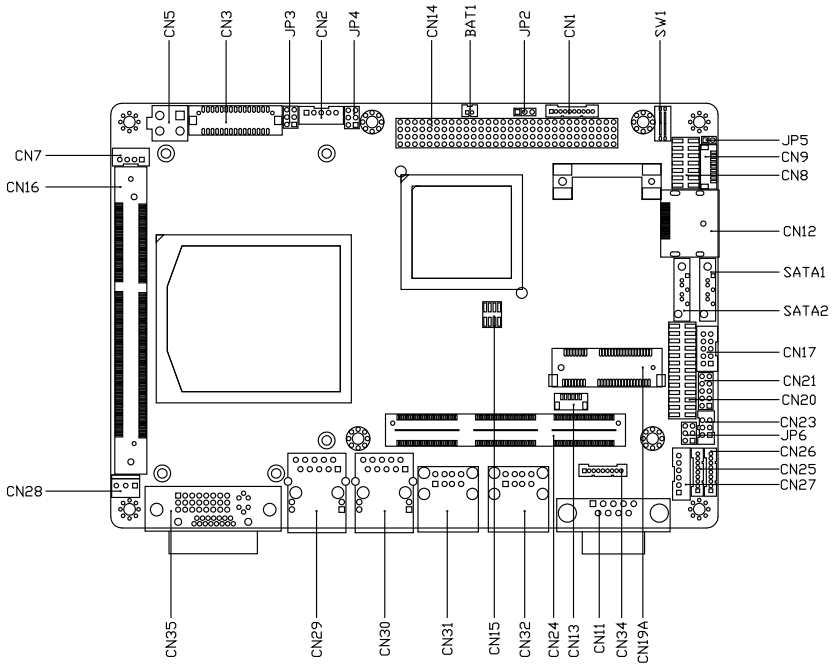


### Solder Side

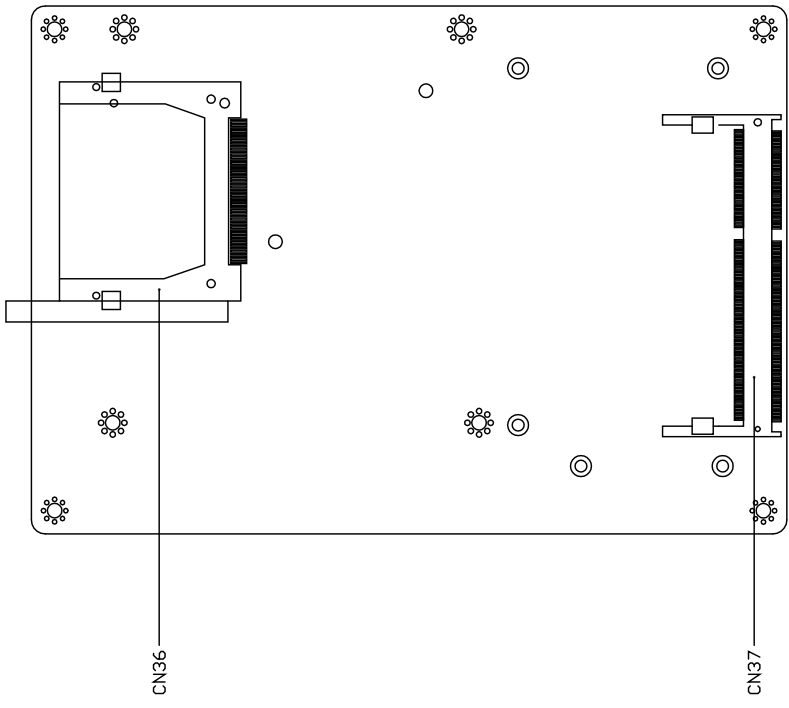


## 2.2 Jumpers and Connectors

### Component Side



### Solder Side



## 2.3 List of Jumpers

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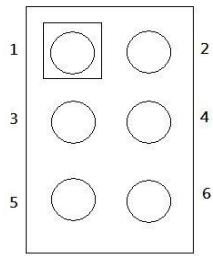
Please refer to the table below for all of the board's jumpers that you can configure for your application

Label	Function
JP2	PCI-104 I/O Voltage Selection
JP3-1	LCD Inverter/Backlight Voltage Selection
JP3-2	LVDS LCD Voltage Selection
JP4-1	Clear CMOS
JP4-2	Clear ME ROM
JP5	Touch Screen 4/5/8-wire Mode Selection
JP6	COM2 RI/+5V/+12V Selection
SW1	AT/ATX Power Mode Selection

### 2.3.1 PCI-104 I/O Voltage Selection (JP1)

Pin	Function
1-2	+5 V
2-3	+ 3.3 V (Default)

### 2.3.2 LCD Function Selection (JP3)



#### LCD Inverter/Backlight Voltage Selection (JP3-1)

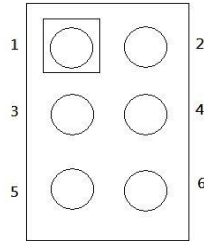
Pin	Function
1-3	+5 V (default)
3-5	+12 V

#### LCD Inverter/Backlight Voltage Selection (JP3-2)

Pin	Function
2-4	+5 V
4-6	+3.3 V (default)

### 2.3.3 Clear CMOS Function (JP4)

---



#### Clear CMOS (JP4-1)

Pin	Function
1-3	Protected (Default)
3-5	Clear

#### Clear ME ROM (JP4-2)

Pin	Function
2-4	Protected (Default)
4-6	Clear

### 2.3.4 Touch Panel 4/5/8-wire Mode Selection (JP5)

---

Pin	Function
1-2 (Open)	5 Wire
1-2 (Close)	4 & 8 Wire (Default)

### 2.3.5 COM2 RI/+5V/+12V Selection (JP6)

---

Pin	Function
1-2	+12V

---

3-4	RI
5-6	+5V (Default)

---

### 2.3.6 AT/ATX Power Mode Selection (SW1)

---

Pin	Function
1(ON), 2(OFF)	ATX Power
1(OFF), 2(ON)	AT Power (Default)

---



## 2.4 List of Connectors

Please refer to the table below for all of the board's connectors that you can configure for your application

Label	Function
CN1	Front Panel Connector
CN2	LCD Inverter/Backlight Connector
CN3	Dual Channel LVDS LCD Connector
CN5	+12V DC Power Input Connector
CN7	+5V/+12V Power Output Connector
CN8	Audio Connector
CN9	Touch Panel Connector
CN11	RS-232 Serial Port 1 Connector
CN12	Display Port Connector
CN13	UIM Connector
CN14	PCI-104 Connector
CN16,CN37	DDR3 SODIMM Slot
CN17,CN31,CN32	USB Connector
CN19	PCI Express Mini Card Connector
CN20	LPT Port Connector
CN21	Digital I/O Connector
CN23	PS2 Keyboard/Mouse Connector
CN24	PCIe/104 Connector (Optional)
CN25	RS-232 Serial Port 4 Connector
CN26	RS-232 Serial Port 3 Connector
CN27	External SMBUS and PS_ON# Connector
CN28	Fan Connector
CN29, CN30	10/100/1000Base-TX Ethernet Connector

CN34	RS-232/422/485 Serial Port 2 Connector
CN35	DVI-I Connector
CN36	CompactFlash™ Slot

### 2.4.1 Front Panel Connector (CN1)

---

Pin	Signal	Pin	Signal
1	Power On Button (+)	2	Power On Button (-)
3	External Buzzer (+)	4	External Buzzer (-)
5	IDE LED (+)	6	IDE LED (-)
7	Power LED (+)	8	Power LED (-)
9	Reset Switch (+)	10	Reset Switch (-)

### 2.4.2 LCD Inverter/Backlight Connector (CN2)

---

Pin	Signal
1	+5V/+12V
2	Brightness Control
3	GND
4	GND
5	Backlight Enable

**Note:** The max. rating of Pin 1 is 0.5A @ 12V

### 2.4.3 Dual Channel LVDS LCD Connector (CN3)

---

Pin	Signal	Pin	Signal
1	ENBKL	2	N.C
3	PPVCC	4	GND
5	LVDS1_TXCLK-	6	LVDS1_TXCLK+
7	PPVCC	8	GND
9	LVDS1_TX0-	10	LVDS1_TX0+

11	LVDS1_TX1-	12	LVDS1_TX1+
13	LVDS1_TX2-	14	LVDS1_TX2+
15	LVDS1_TX3-	16	LVDS1_TX3+
17	I2C_DATA	18	I2C_CLK
19	LVDS2_TX0-	20	LVDS2_TX0+
21	LVDS2_TX1-	22	LVDS2_TX1+
23	LVDS2_TX2-	24	LVDS2_TX2+
25	LVDS2_TX3-	26	LVDS2_TX3+
27	PPVCC	28	GND
29	LVDS2_TXCLK-	30	LVDS2_TXCLK+

**Note:** For VLCD (pin 3, 7, 27), the max. rating of each pin is 0.5A @ 5V

#### 2.4.4 +12V DC Power Input Connector (CN5)

Pin	Signal
1	GND
2	GND
3	+12V
4	+12V

**Note:** Standard type is a 2 pin power connector.

**Note:** Optional support DC 12V power connector (4 pin connector).

#### 2.4.5 +5V/+12V Power Output Connector (CN7)

Pin	Signal
1	+12V
2	GND

3	GND
---	-----

4	+5V
---	-----

**Note:** The max. rating of Pin 1 is 1A @ 12V; the max. rating of Pin 4 is 1A @ 5V

## 2.4.6 Audio Connector (CN8)

Pin	Signal	Pin	Signal
1	MIC_IN	2	MIC_+2.5V
3	LINE_IN_GND	4	CD_GND
5	LINE_IN_L	6	CD_IN_L
7	LINE_IN_R	8	CD_GND
9	LINE_IN_GND	10	CD_IN_R
11	LINE_OUT_L	12	LINE_OUT_R
13	LINE_OUT_GND	14	LINE_OUT_GND

## 2.4.7 Touch Panel Connector (CN9)

Pin	8-wire (Default)	4-wire	5-wire
1	GND	GND	GND
2	Top Excite	Top Excite	UL (Y)
3	Bottom Excite	Bottom Excite	UR (H)
4	Lef Excite	Lef Excite	LL (L)
5	Right Excite	Right Excite	LR (X)
6	Top Sense	N/A	Sense (S)
7	Bottom Sense	N/A	
8	Lef Sense	N/A	
9	Right Sense	N/A	

## 2.4.8 RS-232 Serial Port 1 Connector (CN11)

---

Pin	Signal	Pin	Signal
1	DCD1	2	RXD1
3	TXD1	4	DTR1
5	GND	6	DSR1
7	RTS1	8	CTS1
9	RI1		

## 2.4.9 Display Port Connector (CN12)

---

Pin	Signal	Pin	Signal
1	Lane0 (P)	2	GND
3	Lane0(N)	4	Lane1 (P)
5	GND	6	Lane1 (N)
7	Lane2 (P)	8	GND
9	Lane2 (N)	10	Lane3 (P)
11	GND	12	Lane3 (N)
13	Config1 (GND)	14	Config2 (GND)
15	AUX CH (P)	16	GND
17	AUX CH (N)	18	Hot Plug
19	Return PWR (GND)	20	DP_PWR

## 2.4.10 UIM Connector (CN13)

---

Pin	Signal
1	UIM_PWR
2	UIM_RST

3	UIM_CLK
4	GND
5	UIM_VPP
6	UIM_DAT

### 2.4.11 USB Connector (CN17)

Pin	Signal	Pin	Signal
1	+5V	2	GND
3	USBD1-	4	GND
5	USBD1+	6	USBD2+
7	GND	8	USBD2-
9	GND	10	+5V

### 2.4.12 PCI-Express Mini Card Connector (CN19)

Pin	Signal	Pin	Signal
51	Reserved	52	+3.3Vaux
49	Reserved	50	GND
47	Reserved	48	+1.5V
45	Reserved	46	LED_WPAN#
43	GND	44	LED_WLAN#
41	+3.3Vaux	42	LED_WWAN#
39	+3.3Vaux	40	GND
37	GND	38	USB_D+
35	GND	36	USB_D-
33	PETp0	34	GND
31	PETn0	32	SMB_DATA

29	GND	30	SMB_CLK
27	GND	28	+1.5V
25	PERp0	26	GND
23	PERn0	24	+3.3Vaux
21	GND	22	PERST#
19	Reserved (UIM_C4)	20	W_DISABLE#
17	Reserved (UIM_C8)	18	GND
<b>Mechanical Key</b>			
15	GND	16	UIM_VPP
13	REFCLK+	14	UIM_RESET
11	REFCLK-	12	UIM_CLK
9	GND	10	UIM_DATA
7	CLKREQ#	8	UIM_PWR
5	COEX2	6	1.5V
3	COEX1	4	GND
1	WAKE#	2	3.3Vaux

### 2.4.13 LPT Port Connector (CN20)

Pin	Signal	Pin	Signal
1	#STROBE	2	#AFD
3	DATA0	4	#ERROR
5	DATA1	6	#INIT
7	DATA2	8	#SLIN
9	DATA3	10	GND
11	DATA4	12	GND
13	DATA5	14	GND
15	DATA6	16	GND



17	DATA7	18	GND
19	#ACK	20	GND
21	BUSY	22	GND
23	PE	24	GND
25	SELECT	26	N.C

#### 2.4.14 Digital I/O Connector (CN21)

Pin	Signal	Pin	Signal
1	IN1	2	IN2
3	IN3	4	IN4
5	OUT1	6	OUT2
7	OUT3	8	OUT4
9	+5V	10	GND

**Note:** The max. rating of Pin 1 ~ Pin 8 is 8mA @ 5V; the max. rating of Pin 9 is 0.5A @ 5V

#### Mapping Table

BIOS Setting	Connector Definition	Address	IT8781 GPIO Setting
Port 8 @A44h	CN16 Pin 8	GPIO Set 5 / Bit 2	U50 Pin 9 (GPIO 52)
Port 7 @A44h	CN16 Pin 7	GPIO Set 5 / Bit 1	U50 Pin 10 (GPIO 51)
Port 6 @A42h	CN16 Pin 6	GPIO Set 3 / Bit 7	U50 Pin 11 (GPIO 37)
Port 5 @A42h	CN16 Pin 5	GPIO Set 3 / Bit 6	U50 Pin 12 (GPIO 36)
Port 4 @A40h	CN16 Pin 4	GPIO Set 1 / Bit 4	U50 Pin 31 (GPIO 14)
Port 3 @A40h	CN16 Pin 3	GPIO Set 1 / Bit 3	U50 Pin 32 (GPIO 13)
Port 2 @A40h	CN16 Pin 2	GPIO Set 1 / Bit 2	U50 Pin 33 (GPIO 12)
Port 1 @A40h	CN16 Pin 1	GPIO Set 1 / Bit 1	U50 Pin 34 (GPIO 11)

Digital I/O Address is A40h, A42h, A44h.

### 2.4.15 PS2 Keyboard/Mouse Connector (CN23)

---

Pin	Signal	Pin	Signal
1	KBDAT	2	KBCLK
3	GND	4	+5V
5	MSDAT	6	MSCLK

### 2.4.16 RS-232 Serial Port 4, 3 Connector (CN25, CN26)

---

Pin	Signal	Pin	Signal
1	DCDx	2	DSRx
3	RxDx	4	RTSx
5	TxDx	6	CTSx
7	DTRx	8	RIx
9	GND		

### 2.4.17 External SMBUS and PS\_ON# Connector (CN27)

---

Pin	Signal
1	SMB_DATA
2	GND
3	SMB_CLK
4	GND
5	PS_ON#
6	+5VA

### 2.4.18 FAN Connector (CN28)

---

Pin	Signal
1	GND
2	+12V(Default) or +5V
3	Speed Sense

### 2.4.19 RS-232/422/485 Serial Port 2 Connector (CN34)

---

Pin	Signal	Pin	Signal
1	DCD2(422TXD-/485DATA-)	2	DSR2
3	RXD2(422RXD+)	4	RTS2
5	TXD2(422TXD+/485DATA+)	6	CTS2
7	DTR2(422RXD-)	8	RI2 / +5V /+12V
9	GND	10	

### 2.4.20 DVI-I Connector (CN35)

---

Pin	Signal	Pin	Signal
1	DVI_TX2-	2	DVI_TX2+
3	N.C	4	N.C
5	N.C	6	SM_CLK
7	SM_DAT	8	CRT_VSYNC
9	DVI_TX1-	10	DVI_TX1+
11	GND	12	N.C
13	N.C	14	+5V
15	GND	16	HPDET

17	DVI_TX0-	18	DVI_TX0+
19	GND	20	N.C
21	N.C	22	GND
23	DVI_TXCLK+	24	DVI_TXCLK-
25	GND		
27	N.C		
C1	RED	C2	GREEN
C3	BLUE	C4	CRT_HSYNC
C5	CRT_PLUG#	C6	GND

# Chapter 3

---

BIOS Setup

## 3.1 System Test and Initialization

---

These routines test and initialize board hardware. If the routines encounter an error during the tests, you will either hear a few short beeps or see an error message on the screen. There are two kinds of errors: fatal and non-fatal. The system can usually continue the boot up sequence with non-fatal errors. Non-fatal error messages usually appear on the screen along with the following instructions:

Press <F1> to RESUME

Write down the message and press the F1 key to continue the boot up sequence.

### System configuration verification

These routines check the current system configuration against the values stored in the CMOS memory. If they do not match, the program outputs an error message. You will then need to run the BIOS setup program to set the configuration information in memory.

There are three situations in which you will need to change the CMOS settings:

1. You are starting your system for the first time
2. You have changed the hardware attached to your system
3. The CMOS memory has lost power and the configuration information has been erased.

The EPIC-QM57 CMOS memory has an integral lithium battery backup for data retention. However, you will need to replace the complete unit when it finally runs down.

## 3.2 Awards BIOS Setup

---

Awards BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed CMOS RAM so that it retains the Setup information when the power is turned off.

### Entering Setup

Power on the computer and press <Del> immediately. This will allow you to enter Setup.

### Standard CMOS Features

Use this menu for basic system configuration. (Date, time, IDE, etc.)

### Advanced BIOS Features

Use this menu to set the advanced features available on your system.

### Advanced Chipset Features

Use this menu to change the values in the chipset registers and optimize your system performance.

### Integrated Peripherals

Use this menu to specify your settings for integrated peripherals. (keyboard, mouse etc.)

### Power Management Setup

Use this menu to specify your settings for power management. (HDD power down, power on by ring, KB wake up, etc.)

### PnP/PCI Configurations

This entry appears if your system supports PnP/PCI.

**PC Health Status**

Use this menu to set PC Health Status.

**Frequency/Voltage Control**

Use this menu to specify your settings for auto detect DIMM/PCI clock and spread spectrum.

**Load Optimized Defaults**

Use this menu to load the BIOS default values that are factory settings for optimal performance system operations. While AWARD has designated the custom BIOS to maximize performance, the factory has the right to change these defaults to meet their needs.

**Set Password**

Use this menu to set Supervisor Password.

**Save and Exit Setup**

Save CMOS value changes to CMOS and exit setup.

**Exit Without Saving**

Abandon all CMOS value changes and exit setup.



# Chapter 4

---

Drivers Installation

## 4.1 Product CD/DVD

---

The EPIC-QM57 comes with a product DVD that contains all the drivers and utilities you need to setup your product. Insert the DVD and follow the steps in the autorun program to install the drivers.

In case the program does not start, follow the sequence below to install the drivers.

### Step 1 – Install Chipset Drivers

1. Open the **Step 1 – Chipset** folder followed by **infnst\_autol (9.1.1.1020).exe**
2. Follow the instructions
3. Drivers will be installed automatically

### Step 2 – Install Graphics Driver

1. Open the **STEP2 - VGA** folder and select your OS
2. Open the **Setup.exe** file in the folder
3. Follow the instructions
4. Drivers will be installed automatically

### Step 3 – Install LAN Driver

1. Open the **STEP3 – LAN** folder followed by **autorun.exe**
2. Follow the instructions
3. Drivers will be installed automatically

### Step 4 – Install ME Driver

1. Open the **STEP4 – ME** folder followed by **Setup.exe**
2. Follow the instructions
3. Drivers will be installed automatically

### Step 5 – Install Audio Drivers

1. Open the **STEP5 - Audio** folder followed by **Setup.exe**
2. Follow the instructions
3. Drivers will be installed automatically

### Step 6 – Install Touch Drivers

1. Open the **STEP6 – Touch** folder followed by **Setup.exe**
2. Follow the instructions
3. Drivers will be installed automatically

### Step 7 – Install RAID & AHCI Driver

Please refer to **Appendix D RAID & AHCI Settings**

# Appendix A

---

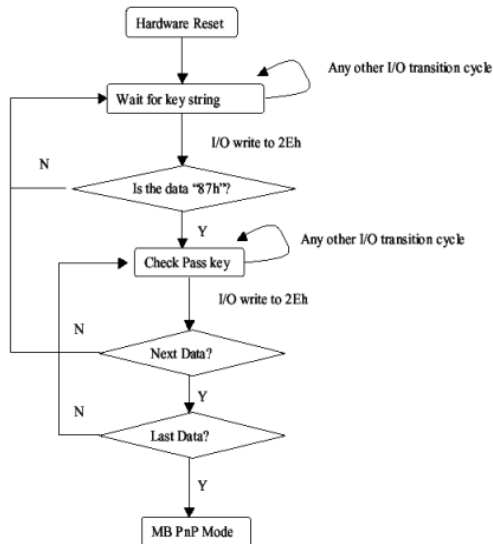
## Watchdog Timer Programming

## A.1 Watchdog Timer Programming

The EPIC-QM57 utilizes ITE 8781 chipset as its watchdog timer controller. Below are the procedures to complete its configuration and the AAEON initial watchdog timer program is also attached based on which you can develop customized program to fit your application.

### Configuring Sequence Description

After the hardware reset or power-on reset, the ITE 8781 enters the normal mode with all logical devices disabled except KBC. The initial state (enable bit) of this logical device (KBC) is determined by the state of pin 121 (DTR1#) at the falling edge of the system reset during power-on reset.



There are three steps to complete the configuration setup: (1) Enter the MB PnP Mode; (2) Modify the data of configuration registers; (3) Exit the MB PnP Mode. Undesired result may occur if the MB PnPMode is not exited normally.

### (1) Enter the MB PnP Mode

To enter the MB PnP Mode, four special I/O write operations are to be performed during Wait for Key state. To ensure the initial state of the key-check logic, it is necessary to perform four write operations to the Special Address port (2EH). Two different enter keys are provided to select configuration ports (2Eh/2Fh) of the next step.

	<b>Address Port</b>	<b>Data Port</b>
<b>87h, 01h, 55h, 55h:</b>	<b>2Eh</b>	<b>2Fh</b>

### (2) Modify the Data of the Registers

All configuration registers can be accessed after entering the MB PnP Mode. Before accessing a selected register, the content of Index 07h must be changed to the LDN to which the register belongs, except some Global registers.

### (3) Exit the MB PnP Mode

Set bit 1 of the configure control register (Index=02h) to 1 to exit the MB PnP Mode.

#### WatchDog Timer Configuration Registers

<b>LDN</b>	<b>Index</b>	<b>R/W</b>	<b>Reset</b>	<b>Configuration Register or Action</b>
All	02h	W	NA	Configure Control

07h	71h	R/W	00h	Watch Dog Timer Control Register
07h	72h	R/W	001s0000b	Watch Dog Timer Configuration Register
07h	73h	R/W	38h	Watch Dog Timer Time-out Value (LSB) Register
07h	74h	R/W	00h	Watch Dog Timer Time-out Value (MSB) Register

#### Configure Control (Index=02h)

This register is write only. Its values are not sticky; that is to say, a hardware reset will automatically clear the bits, and does not require the software to clear them.

Bit	Description
7-2	<b>Reserved</b>
1	Returns to the "Wait for Key" state. This bit is used when the configuration sequence is completed.
0	Resets all logical devices and restores configuration registers to their power-on states.

### Watch Dog Timer 1, 2, 3 Control Register (Index=71h,81h,91h Default=00h)

Bit	Description
7	<b>WDT Timeout Enable(WTE)</b> 1: Disable. 0: Enable.
6	<b>WDT Reset upon Mouse Interrupt(WRKMI)</b> 0: Disable. 1: Enable.
5	<b>WDT Reset upon Keyboard Interrupt(WRKBI)</b> 0: Disable. 1: Enable.
4	<b>Reserved</b>
3-2	<b>Reserved</b>
1	<b>Force Time-out(FTO)</b> This bit is self-clearing.
0	<b>WDT Status(WS)</b> 1: WDT value reaches 0. 0: WDT value is not 0.

### Watch Dog Timer 1, 2, 3 Configuration Register (Index=72h, 82h, 92h Default = 001s0000b)

Bit	Description
7	<b>WDT Time-out Value Select 1 (WTVS)</b> 1: Second 0: Minute
6	<b>WDT Output through KRST (Pulse) Enable(WOKE)</b> 1: Enable 0: Disable
5	<b>WDT Time-out value Extra select(WTVES)</b> 1: 64ms x WDT Timer-out value (default = 4s) 0: Determined by WDT Time-out value select 1 (bit 7 of this register)
4	<b>WDT Output through PWROK (Pulse) Enable(WOPE)</b> 1: Enable 0: Disable During LRESET#, this bit is selected by JP7 power-on strapping option
3-0	<b>Select interrupt level <sup>Note1</sup> for WDT(SIL)</b>

### Watch Dog Timer 1,2,3 Time-Out Value (LSB) Register (Index=73h,83h,93h, Default =38h)

Bit	Description
7-0	<b>WDT Time-out Value 7-0(WTV)</b>

### Watch Dog Timer 1,2,3 Time-Out Value (MSB) Register (Index=74h,84h,94h Default =00h)

Bit	Description
7-0	WDT Time-out Value 15-8(WTV)

## A.2 ITE8781 Watchdog Timer Initial Program

---

```
.MODEL SMALL
```

```
.CODE
```

```
Main:
```

```
CALL Enter_Configuration_mode
```

```
CALL Check_Chip
```

```
mov cl, 7
```

```
call Set_Logic_Device
```

```
;time setting
```

```
mov cl, 10 ; 10 Sec
```

```
dec al
```

```
Watch_Dog_Setting:
```

```
;Timer setting
```

```
mov al, cl
```

```
mov cl, 73h
```

```
call Superio_Set_Reg
```

```
;Clear by keyboard or mouse interrupt
```

```
mov al, 0f0h
```

```
mov cl, 71h
```

```
call Superio_Set_Reg
```

```
;unit is second.
```

```
mov al, 0C0H
```

```
mov cl, 72h
```

```
call Superio_Set_Reg
```



```
; game port enable
```

```
mov cl, 9
```

```
call Set_Logic_Device
```

```
Initial_OK:
```

```
CALL Exit_Configuration_mode
```

```
MOV AH,4Ch
```

```
INT 21h
```

```
Enter_Configuration_Mode PROC NEAR
```

```
MOV SI,WORD PTR CS:[Offset Cfg_Port]
```

```
MOV DX,02Eh
```

```
MOV CX,04h
```

```
Init_1:
```

```
MOV AL,BYTE PTR CS:[SI]
```

```
OUT DX,AL
```

```
INC SI
```

```
LOOP Init_1
```

```
RET
```

```
Enter_Configuration_Mode ENDP
```

```
Exit_Configuration_Mode PROC NEAR
```

```
MOV AX,0202h
```

```
CALL Write_Configuration_Data
```

```
RET
```

```
Exit_Configuration_Mode ENDP
```

Check\_Chip PROC NEAR

MOV AL,20h

CALL Read\_Configuration\_Data

CMP AL,87h

JNE Not\_Initial

MOV AL,21h

CALL Read\_Configuration\_Data

CMP AL,81h

JNE Not\_Initial

Need\_Initial:

STC

RET

Not\_Initial:

CLC

RET

Check\_Chip ENDP

Read\_Configuration\_Data PROC NEAR

MOV DX,WORD PTR CS:[Cfg\_Port+04h]

OUT DX,AL

MOV DX,WORD PTR CS:[Cfg\_Port+06h]

IN AL,DX

RET

Read\_Configuration\_Data ENDP

Write\_Configuration\_Data PROC NEAR

```
MOV DX,WORD PTR CS:[Cfg_Port+04h]
OUT DX,AL
XCHG AL,AH
MOV DX,WORD PTR CS:[Cfg_Port+06h]
OUT DX,AL
RET
Write_Configuration_Data ENDP

Superio_Set_Reg proc near
push ax
MOV DX,WORD PTR CS:[Cfg_Port+04h]
mov al,cl
out dx,al
pop ax
inc dx
out dx,al
ret
Superio_Set_Reg endp.Set_Logic_Device proc near
Set_Logic_Device  proc  near
push ax
push cx
xchg al,cl
mov cl,07h
call Superio_Set_Reg
pop cx
pop ax
ret
Set_Logic_Device endp
```

;Select 02Eh->Index Port, 02Fh->Data Port

Cfg\_Port DB 087h,001h,055h,055h

DW 02Eh,02Fh

### END Main

Note: Interrupt level mapping

0Fh-Dh: not valid

0Ch: IRQ12

.

.

03h: IRQ3

02h: not valid

01h: IRQ1

00h: no interrupt selected







































# Appendix B

---

I/O Information

## B.1 I/O Address Map

Input/output (IO)	
[00000000 - 0000000F]	Direct memory access controller
[00000000 - 00000CF7]	PCI bus
[00000010 - 0000001F]	Motherboard resources
[00000020 - 00000021]	Programmable interrupt controller
[00000022 - 0000003F]	Motherboard resources
[00000040 - 00000043]	System timer
[00000044 - 0000005F]	Motherboard resources
[00000060 - 00000060]	Standard 101/102-Key or Microsoft Natural PS/2 Keyboard
[00000061 - 00000061]	System speaker
[00000062 - 00000063]	Motherboard resources
[00000064 - 00000064]	Standard 101/102-Key or Microsoft Natural PS/2 Keyboard
[00000065 - 0000006F]	Motherboard resources
[00000070 - 00000071]	System CMOS/real time clock
[00000072 - 0000007F]	Motherboard resources
[00000080 - 00000080]	Motherboard resources
[00000081 - 00000083]	Direct memory access controller
[00000084 - 00000086]	Motherboard resources
[00000087 - 00000087]	Direct memory access controller
[00000088 - 00000088]	Motherboard resources
[00000089 - 0000008B]	Direct memory access controller
[0000008C - 0000008E]	Motherboard resources
[0000008F - 0000008F]	Direct memory access controller
[00000090 - 0000009F]	Motherboard resources
[000000A0 - 000000A1]	Programmable interrupt controller
[000000A2 - 000000BF]	Motherboard resources
[000000C0 - 000000DF]	Direct memory access controller
[000000E0 - 000000EF]	Motherboard resources
[000000F0 - 000000FF]	Numeric data processor
[00000274 - 00000277]	ISAPNP Read Data Port
[00000279 - 00000279]	ISAPNP Read Data Port
[000002E8 - 000002EF]	Communications Port (COM4)
[000002F8 - 000002FF]	Communications Port (COM2)
[00000378 - 0000037F]	Printer Port (LPT1)
[000003B0 - 000003BB]	Intel(R) HD Graphics
[000003C0 - 000003DF]	Intel(R) HD Graphics
[000003E8 - 000003EF]	Communications Port (COM3)

	[000003F8 - 000003FF]	Communications Port (COM1)
	[00000400 - 0000047F]	System board
	[000004D0 - 000004D1]	Motherboard resources
	[00000500 - 0000057F]	System board
	[00000778 - 0000077F]	Motherboard resources
	[00000A00 - 00000A1F]	Motherboard resources
	[00000A79 - 00000A79]	ISAPNP Read Data Port
	[00000D00 - 0000FFFF]	PCI bus
	[00001180 - 0000119F]	System board
	[0000D000 - 0000D00F]	Standard Dual Channel PCI IDE Controller
	[0000D000 - 0000DFFF]	Intel(R) 5 Series/3400 Series Chipset Family PCI Express Root Port 6 - 3B50
	[0000D010 - 0000D013]	Standard Dual Channel PCI IDE Controller
	[0000D020 - 0000D027]	Standard Dual Channel PCI IDE Controller
	[0000D030 - 0000D033]	Standard Dual Channel PCI IDE Controller
	[0000D040 - 0000D047]	Standard Dual Channel PCI IDE Controller
	[0000E000 - 0000E01F]	Intel(R) 82574L Gigabit Network Connection
	[0000E000 - 0000EFFF]	Intel(R) 5 Series/3400 Series Chipset Family PCI Express Root Port 6 - 3B4C
	[0000F000 - 0000F01F]	Intel(R) 5 Series/3400 Series Chipset Family SMBus Controller - 3B30
	[0000F020 - 0000F03F]	Intel(R) 82577LM Gigabit Network Connection
	[0000F040 - 0000F04F]	Intel(R) 5 Series/3400 Series Chipset Family 2 port Serial ATA Storage Controller - 3B2D
	[0000F050 - 0000F05F]	Intel(R) 5 Series/3400 Series Chipset Family 2 port Serial ATA Storage Controller - 3B2D
	[0000F060 - 0000F063]	Intel(R) 5 Series/3400 Series Chipset Family 2 port Serial ATA Storage Controller - 3B2D
	[0000F070 - 0000F077]	Intel(R) 5 Series/3400 Series Chipset Family 2 port Serial ATA Storage Controller - 3B2D
	[0000F080 - 0000F083]	Intel(R) 5 Series/3400 Series Chipset Family 2 port Serial ATA Storage Controller - 3B2D
	[0000F090 - 0000F097]	Intel(R) 5 Series/3400 Series Chipset Family 2 port Serial ATA Storage Controller - 3B2D
	[0000F0A0 - 0000F0AF]	Intel(R) 5 Series/3400 Series Chipset Family 4 port Serial ATA Storage Controller - 3B2E
	[0000F0B0 - 0000F0BF]	Intel(R) 5 Series/3400 Series Chipset Family 4 port Serial ATA Storage Controller - 3B2E
	[0000F0C0 - 0000F0C3]	Intel(R) 5 Series/3400 Series Chipset Family 4 port Serial ATA Storage Controller - 3B2E
	[0000F0D0 - 0000F0D7]	Intel(R) 5 Series/3400 Series Chipset Family 4 port Serial ATA Storage Controller - 3B2E
	[0000F0E0 - 0000F0E3]	Intel(R) 5 Series/3400 Series Chipset Family 4 port Serial ATA Storage Controller - 3B2E
	[0000F0F0 - 0000F0F7]	Intel(R) 5 Series/3400 Series Chipset Family 4 port Serial ATA Storage Controller - 3B2E
	[0000F100 - 0000F107]	Intel(R) Active Management Technology - SOL (COM5)
	[0000F110 - 0000F11F]	Standard Dual Channel PCI IDE Controller
	[0000F120 - 0000F123]	Standard Dual Channel PCI IDE Controller
	[0000F130 - 0000F137]	Standard Dual Channel PCI IDE Controller
	[0000F140 - 0000F143]	Standard Dual Channel PCI IDE Controller
	[0000F150 - 0000F157]	Standard Dual Channel PCI IDE Controller
	[0000F160 - 0000F167]	Intel(R) HD Graphics

## B.2 1<sup>st</sup> MB Memory Address Map



The image shows a screenshot of the Windows System Information tool, specifically the 'Memory' section. The 'Memory' header is highlighted in blue. Below it, a list of memory addresses and their corresponding hardware components is displayed. Each entry includes a memory address range in hexadecimal, a small icon, and the device name. The list includes various Intel graphics, PCI bus, network, and system board components.

Memory Address Range	Device Name
[000A0000 - 000BFFFF]	Intel(R) HD Graphics
[000A0000 - 000BFFFF]	PCI bus
[7C000000 - FFFFFFFF]	PCI bus
[D0000000 - DFFFFFFF]	Intel(R) HD Graphics
[E0000000 - EFFFFFFF]	System board
[FE000000 - FE3FFFFF]	Intel(R) HD Graphics
[FE400000 - FE4FFFFF]	Intel(R) 5 Series/3400 Series Chipset Family PCI Express Root Port 6 - 3B50
[FE500000 - FE51FFFF]	Intel(R) 82574L Gigabit Network Connection
[FE500000 - FE5FFFFF]	Intel(R) 5 Series/3400 Series Chipset Family PCI Express Root Port 6 - 3B4C
[FE520000 - FE523FFF]	Intel(R) 82574L Gigabit Network Connection
[FE600000 - FE61FFFF]	Intel(R) 82577LM Gigabit Network Connection
[FE620000 - FE623FFF]	Microsoft UAA Bus Driver for High Definition Audio
[FE624000 - FE624FFF]	Intel(R) Turbo Boost Technology Driver
[FE625000 - FE6250FF]	Intel(R) 5 Series/3400 Series Chipset Family SMBus Controller - 3B30
[FE626000 - FE6263FF]	Intel(R) 5 Series/3400 Series Chipset Family USB Enhanced Host Controller - 3B34
[FE627000 - FE6273FF]	Intel(R) 5 Series/3400 Series Chipset Family USB Enhanced Host Controller - 3B3C
[FE628000 - FE628FFF]	Intel(R) 82577LM Gigabit Network Connection
[FE629000 - FE629FFF]	Intel(R) Active Management Technology - SOL (COM5)
[FE62A000 - FE62A0FF]	Intel(R) Management Engine Interface
[FEC00000 - FECFFFFFFF]	System board
[FED00000 - FED003FF]	High precision event timer
[FED08000 - FED08FFF]	System board
[FED14000 - FED19FFF]	System board
[FED1C000 - FED1FFFF]	System board
[FED20000 - FED3FFFF]	System board
[FED90000 - FED93FFF]	System board
[FEE00000 - FEE0FFFF]	System board
[FF000000 - FFFFFFFF]	System board



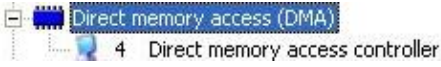
## B.3 IRQ Mapping Chart



Device	IRQ
(ISA) 0 System timer	0
(ISA) 1 Standard 101/102-Key or Microsoft Natural PS/2 Keyboard	1
(ISA) 3 Communications Port (COM2)	3
(ISA) 4 Communications Port (COM1)	4
(ISA) 8 System CMOS/real time clock	8
(ISA) 9 Microsoft ACPI-Compliant System	9
(ISA) 10 Communications Port (COM3)	10
(ISA) 11 Communications Port (COM4)	11
(ISA) 12 Microsoft PS/2 Mouse	12
(ISA) 13 Numeric data processor	13
(PCI) 10 Intel(R) 5 Series/3400 Series Chipset Family SMBus Controller - 3B30	10
(PCI) 16 Intel(R) 5 Series/3400 Series Chipset Family PCI Express Root Port 6 - 3B4C	16
(PCI) 16 Intel(R) 5 Series/3400 Series Chipset Family USB Enhanced Host Controller - 3B3C	16
(PCI) 16 Intel(R) HD Graphics	16
(PCI) 16 Intel(R) Management Engine Interface	16
(PCI) 17 Intel(R) 5 Series/3400 Series Chipset Family PCI Express Root Port 1 - 3B42	17
(PCI) 17 Intel(R) 82574L Gigabit Network Connection	17
(PCI) 17 Intel(R) Active Management Technology - SOL (COM5)	17
(PCI) 18 Intel(R) Turbo Boost Technology Driver	18
(PCI) 18 Standard Dual Channel PCI IDE Controller	18
(PCI) 19 Intel(R) 5 Series/3400 Series Chipset Family 2 port Serial ATA Storage Controller - 3B2D	19
(PCI) 19 Intel(R) 5 Series/3400 Series Chipset Family 4 port Serial ATA Storage Controller - 3B2E	19
(PCI) 19 Intel(R) 5 Series/3400 Series Chipset Family PCI Express Root Port 8 - 3B50	19
(PCI) 19 Standard Dual Channel PCI IDE Controller	19
(PCI) 20 Intel(R) 82577LM Gigabit Network Connection	20
(PCI) 22 Microsoft UAA Bus Driver for High Definition Audio	22
(PCI) 23 Intel(R) 5 Series/3400 Series Chipset Family USB Enhanced Host Controller - 3B34	23

## B.4 DMA Channel Assignments

---



# Appendix C

---

Mating Connectors

## C.1 List of Mating Connectors and Cables

Connector Label	Function	Mating Connector		Available Cable	Cable P/N
		Vendor	Model no		
CN1	Front Panel Connector	LIAN TAY	1.25mm Pitch 10 pins (LIAN TAY H752-10 or compatible)	Front Panel cable	1701010150
CN2	LCD Inverter/Backlight Connector	CATCH	2.0mm pitch 5 pin (CATCH HS-5P-2.0 or compatible)	N/A	N/A
CN3	LVDS Connector	Hirose	1.25mm Pitch 30 pins ( CATCH H716 or compatible)	N/A	N/A
CN5	Power Input Connector	N/A	N/A	Big 4P Power cable	1702002010
CN7	+5V/+12V Power Output Connector	Molex	2.0mm Pitch, 4 pins (Molex 87369-040X)	SATA Power Cable	1702150121
CN8	Audio Connector	CATCH	2.0mm pitch 14pins ( CATCH H709-2 or compatible)	Audio Cable	1709140181
CN9	Touch Panel Connector	LIAN TAY	1.0mm pitch 9pins (LIAN TAY H746-09 or compatible)	N/A	N/A
CN13	UIM Connector	LIAN TAY	1.0mm pitch, 6 pins(LIAN TAY H746-06 or	N/A	N/A

			compatible)		
CN17	USB Connector	CATCH	2.00mm Pitch 10 pins ( CATCH H754-2x5 or compatible)	USB Cable	1709100208
CN20	LPT Port Connector	CATCH	2.00mm Pitch 26 pins ( CATCH H754-2x13 or compatible)	LPT cable	1701260200
CN21	Digital I/O Connector	CATCH	2.00mm Pitch 10 pins ( CATCH H754-2x5 or compatible)	N/A	N/A
CN23	PS/2 Keyboard/Mouse Connector	CATCH	2.0mm pitch 6 pins ( CATCH MD-6PS or compatible)	Keyboard / Mouse Cable	1700060157
CN25/26/34	COM Port Connector	LIAN TAY	1.25mm pitch 9 pins (LIAN TAY H752-09 or compatible)	COM Cable	1701090150

# Appendix D

---

RAID & AHCI Settings

## D.1 Setting RAID

OS installation to setup RAID Mode

Step 1: Copy the files below from "Driver CD ->Step7- RAID & AHCI" to Disk



F6Readme  
文字文件  
3 KB



iaAHCI  
安全性目錄  
9 KB



iaAHCI  
安裝資訊  
9 KB



iaStor  
安全性目錄  
8 KB



iaStor  
安裝資訊  
8 KB



iaStor  
系統檔案  
423 KB



license  
文字文件  
5 KB

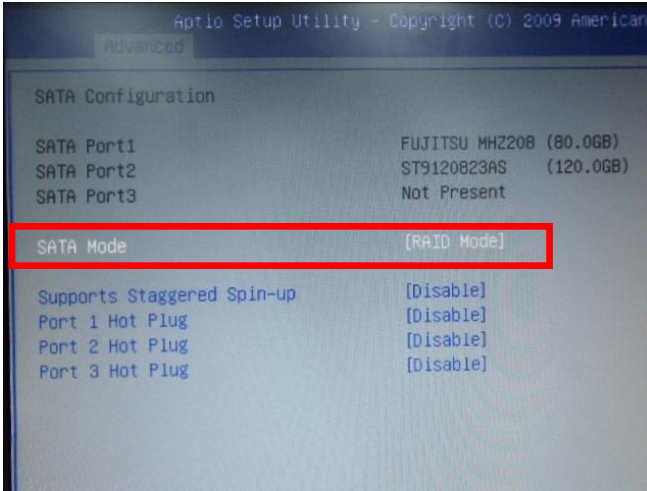


TXTSETUP.OEM  
OEM 檔案  
6 KB

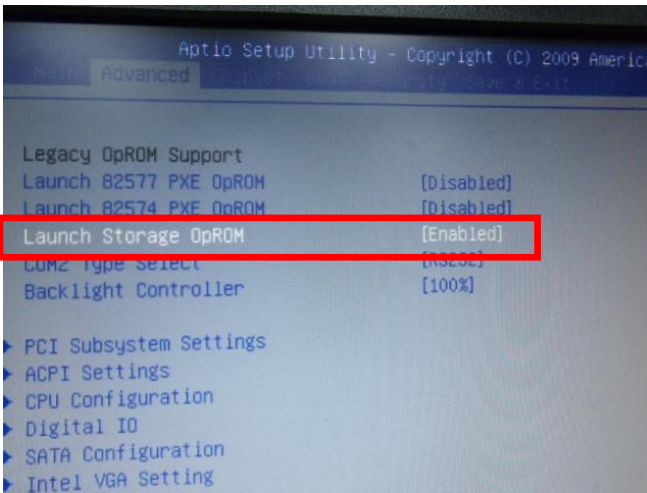
Step 2: Connect the USB Floppy (disk with RAID files) to the board



Step 3: The setting procedures “ In BIOS Setup Menu”  
 A: Advanced -> SATA Configuration -> SATA Mode -> RAID Mode



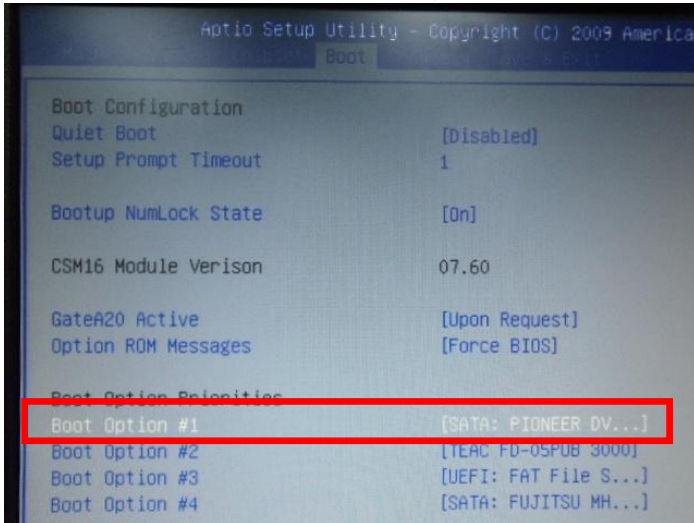
Step 4: The setting procedures “In BIOS Setup Menu”  
 B: Advanced -> Launch Storage OpROM -> Enabled





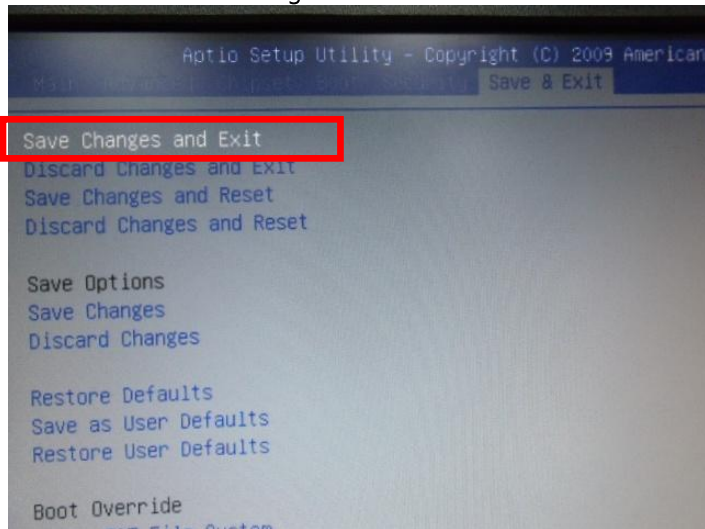
Step 5: The setting procedures "In BIOS Setup Menu"

C: Boot -> Boot Option #1 -> DVD-ROM Type

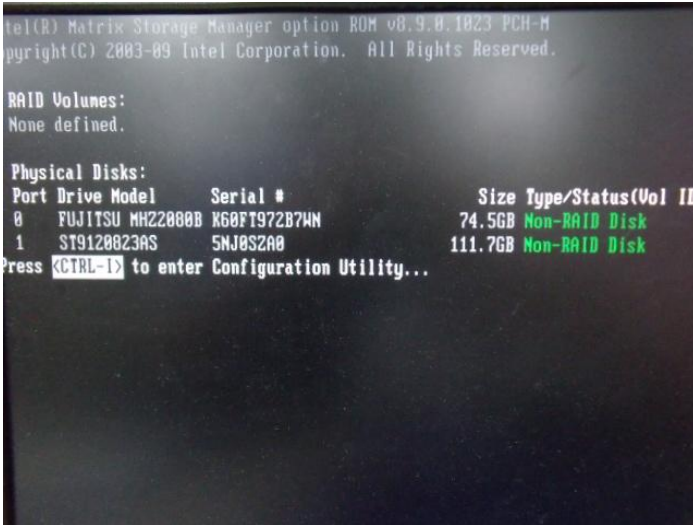


Step 6: The setting procedures "In BIOS Setup Menu"

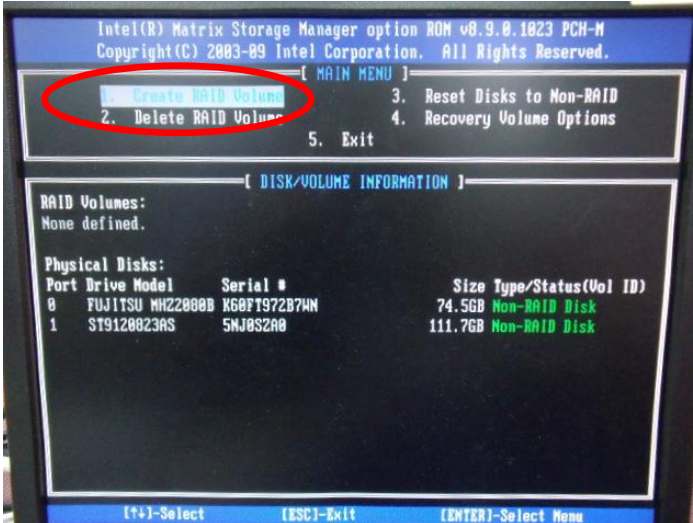
D: Save & Exit -> Save Changes and Exit



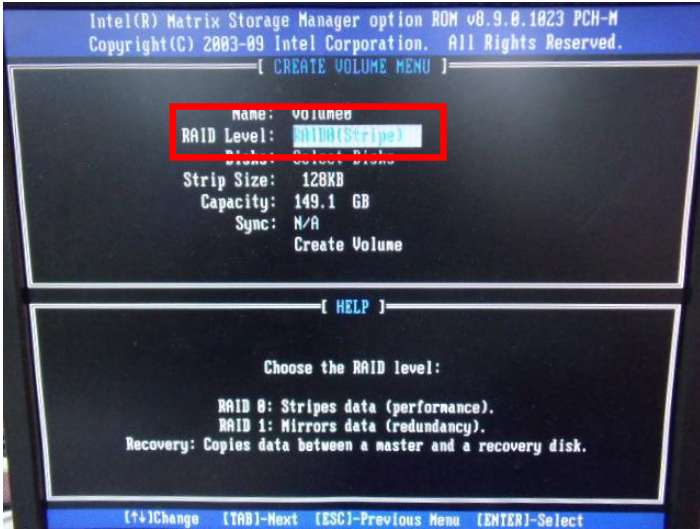
Step 7: Press **Ctrl-I** to enter **MAIN MENU**



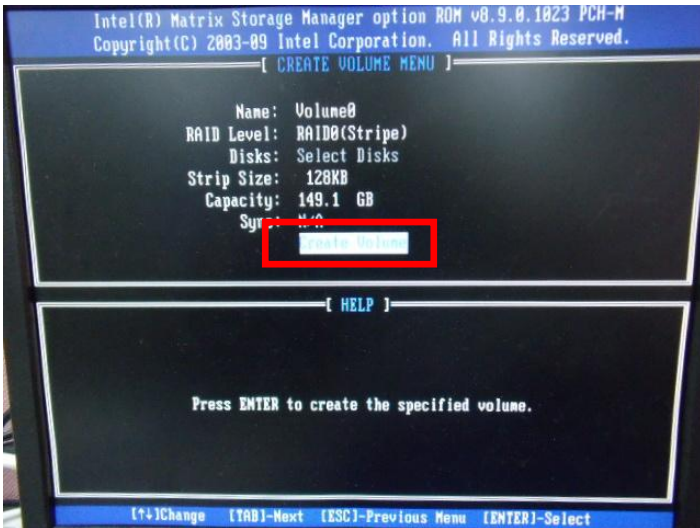
Step 8: Choose "1.Create RAID Volume"



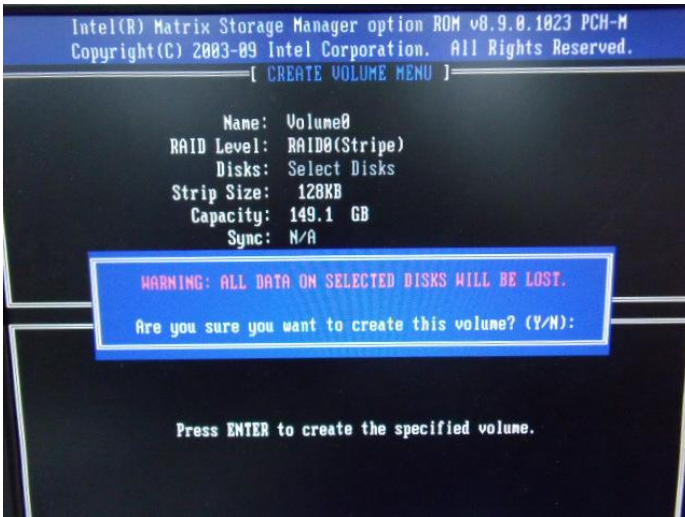
Step 9: RAID Level -> RAID0(Stripe)



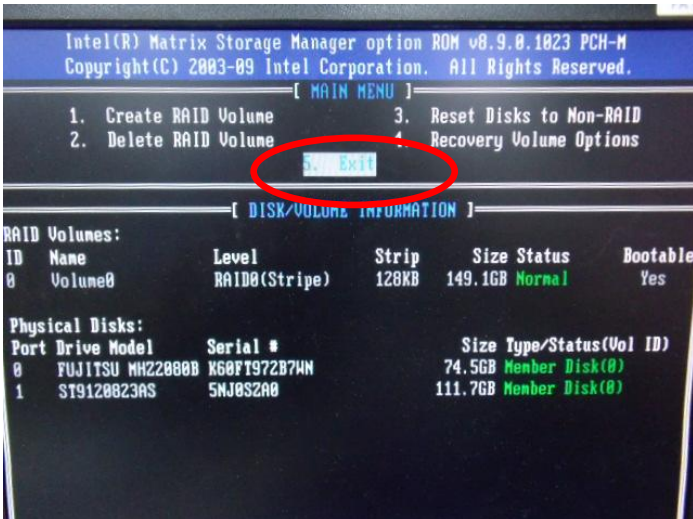
Step 10: Choose "Create Volume"



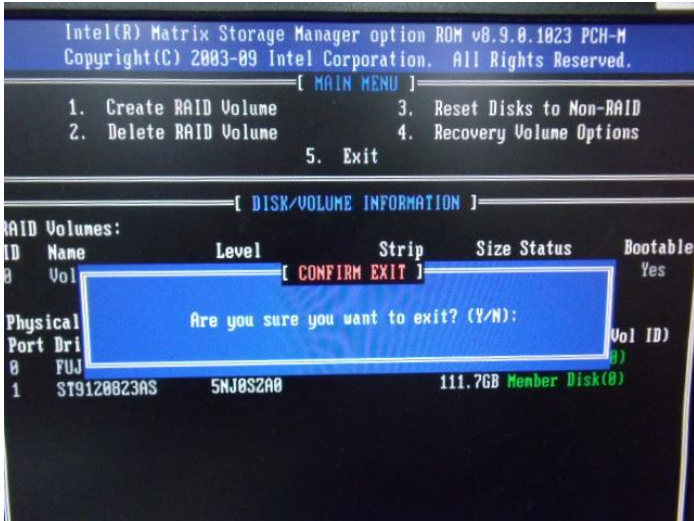
Step 11: Choose "Y"



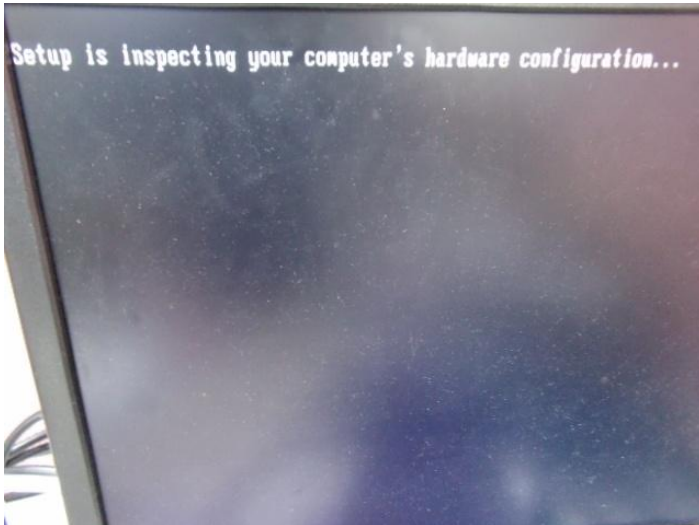
Step 12: Choose "5. Exit"



Step 13: Choose "Y"



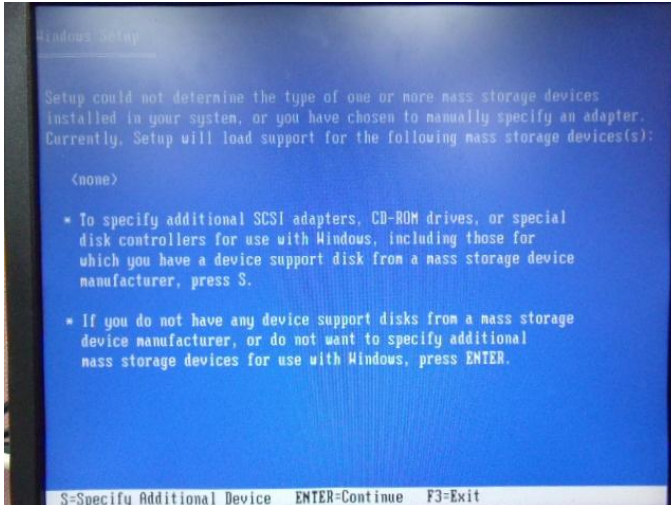
Step 14: Setup OS



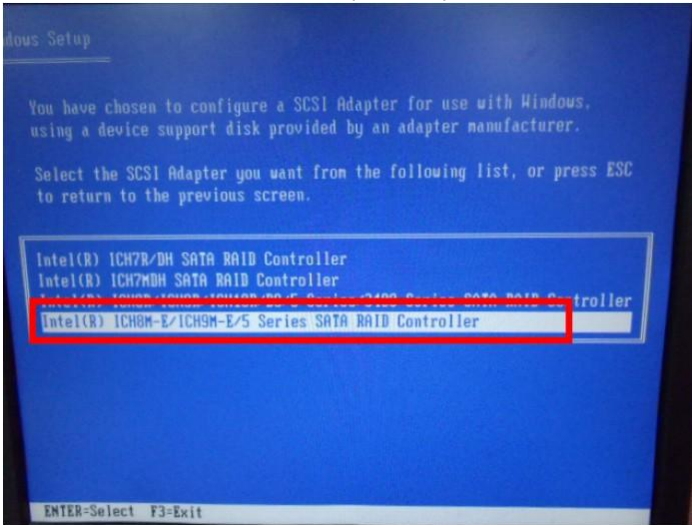
Step 15: Press "F6"



Step 16: Choose "S"



Step 17: Choose "Intel(R) Mobile Express Chipset SATA RAID Controller"



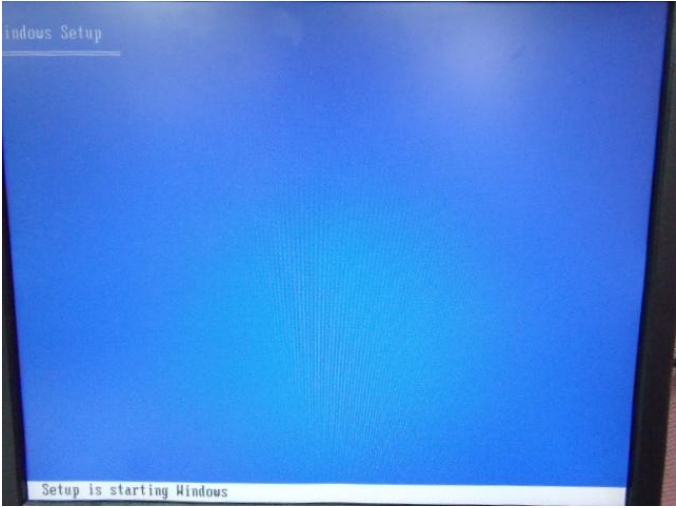
Step 18: It will show the model number you select and then press "ENTER"



Step 19: Setup is starting Windows

EPIC Board

EPIC-QM57





## D.2 Setting AHCI

OS installation to setup AHCI Mode

Step 1: Copy the files below from "Driver CD -> Step7- RAID & AHCI" to Disk

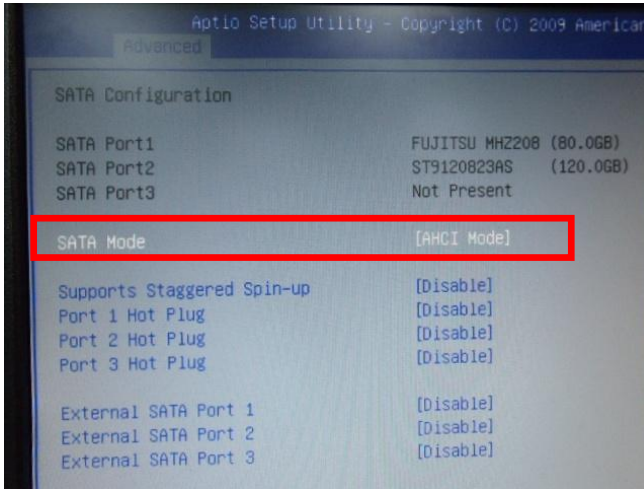


Step 2: Connect the USB Floppy (disk with AHCI files) to the board



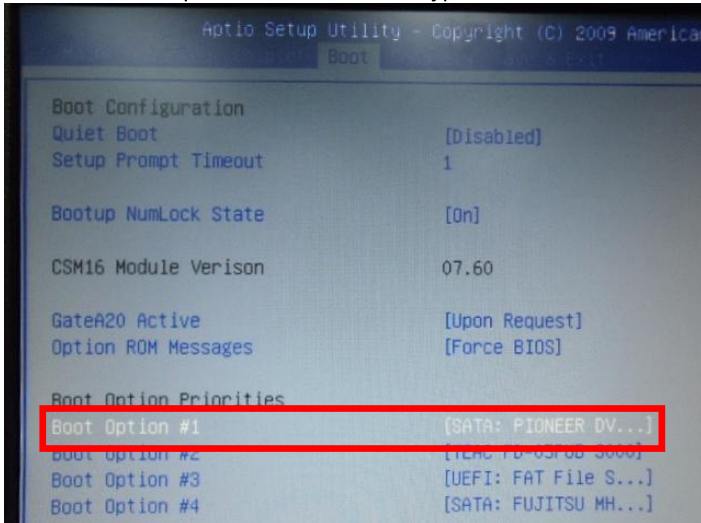
Step 3: The setting procedures "In BIOS Setup Menu"

A: Advanced -> SATA Configuration -> SATA Configuration -> SATA Mode -> AHCI Mode



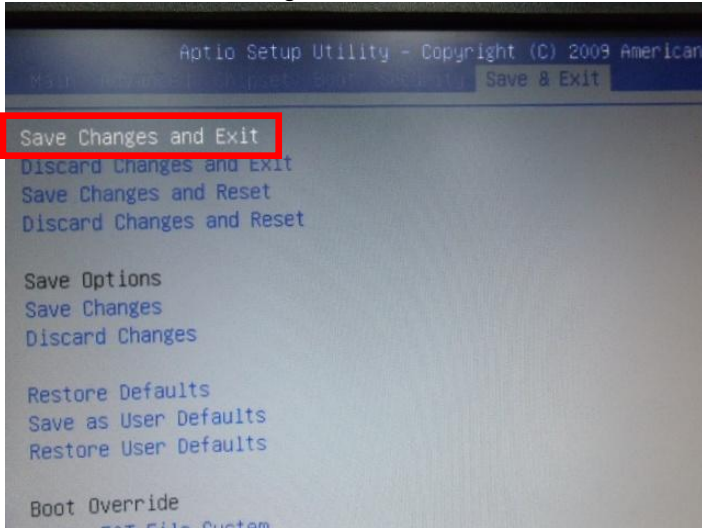
Step 4: The setting procedures "In BIOS Setup Menu"

B: Boot -> Boot Option #1 -> DVD-ROM Type

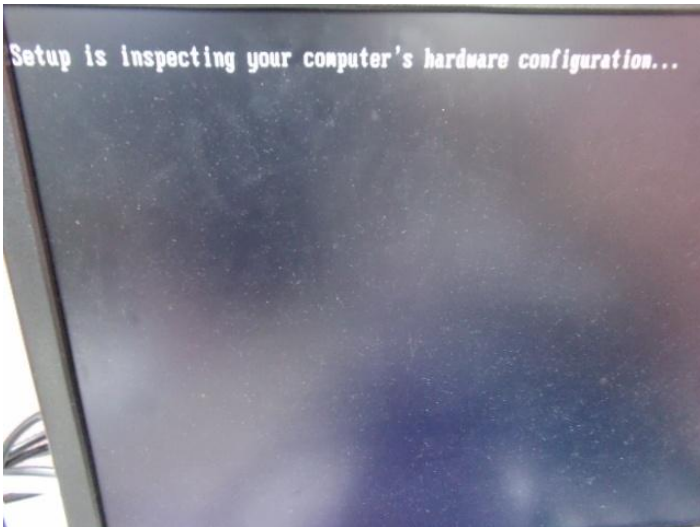


Step 5: The setting procedures "In BIOS Setup Menu"

C: Save & Exit -> Save Changes and Exit



Step 6: Setup OS



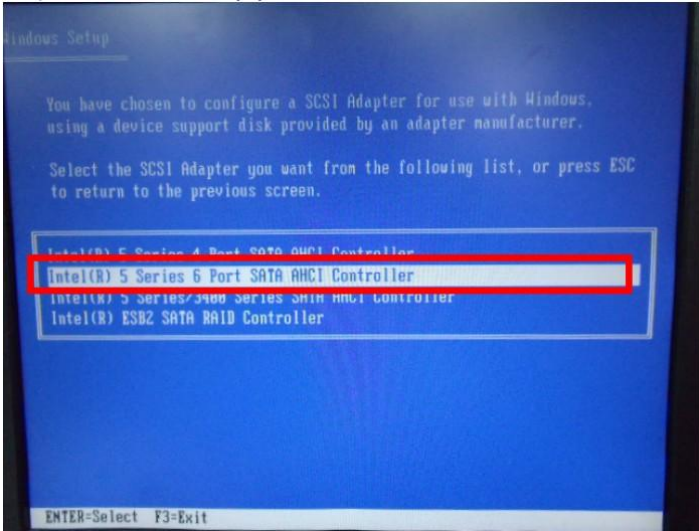
Step 7: Press "F6"



Step 8: Choose "S"



Step 9: Choose "Intel(R) 7 Series Chipset Family SATA AHCI Controller"



Step 10: It will show the model number you select and then press "ENTER"



Step 11: Setup is loading files

