

BOXER-8223AI

Compact Fanless Embedded AI@Edge Box PC
with NVIDIA[®] Jetson Nano[™]
User's Manual 1st Ed

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Packing List

Before setting up your product, please make sure the following items have been shipped:

Item	Quantity
● BOXER-8223AI	1
● Power Connector	1

If any of these items are missing or damaged, please contact your distributor or sales representative immediately.

About this Document

This User's Manual contains all the essential information, such as detailed descriptions and explanations on the product's hardware and software features (if any), its specifications, dimensions, jumper/connector settings/definitions, and driver installation instructions (if any), to facilitate users in setting up their product.

Users may refer to the product page at AAEON.com for the latest version of this document.

Safety Precautions

Please read the following safety instructions carefully. It is advised that you keep this manual for future references

1. All cautions and warnings on the device should be noted.
2. All cables and adapters supplied by AAEON are certified and in accordance with the material safety laws and regulations of the country of sale. Do not use any cables or adapters not supplied by AAEON to prevent system malfunction or fires.
3. Make sure the power source matches the power rating of the device.
4. Position the power cord so that people cannot step on it. Do not place anything over the power cord.
5. Always completely disconnect the power before working on the system's hardware.
6. No connections should be made when the system is powered as a sudden rush of power may damage sensitive electronic components.
7. If the device is not to be used for a long time, disconnect it from the power supply to avoid damage by transient over-voltage.
8. Always disconnect this device from any AC supply before cleaning.
9. While cleaning, use a damp cloth instead of liquid or spray detergents.
10. Make sure the device is installed near a power outlet and is easily accessible.
11. Keep this device away from humidity.
12. Place the device on a solid surface during installation to prevent falls
13. Do not cover the openings on the device to ensure optimal heat dissipation.
14. Watch out for high temperatures when the system is running.
15. Do not touch the heat sink or heat spreader when the system is running
16. Never pour any liquid into the openings. This could cause fire or electric shock.

17. As most electronic components are sensitive to static electrical charge, be sure to ground yourself to prevent static charge when installing the internal components. Use a grounding wrist strap and contain all electronic components in any static-shielded containers.
18. If any of the following situations arises, please the contact our service personnel:
 - i. Damaged power cord or plug
 - ii. Liquid intrusion to the device
 - iii. Exposure to moisture
 - iv. Device is not working as expected or in a manner as described in this manual
 - v. The device is dropped or damaged
 - vi. Any obvious signs of damage displayed on the device
19. **DO NOT LEAVE THIS DEVICE IN AN UNCONTROLLED ENVIRONMENT WITH TEMPERATURES BEYOND THE DEVICE'S PERMITTED STORAGE TEMPERATURES (SEE CHAPTER 1) TO PREVENT DAMAGE.**

FCC Statement

Warning!



This device complies with Part 15 FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received including interference that may cause undesired operation.

Caution:

There is a danger of explosion if the battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions and your local government's recycling or disposal directives.

Attention:

Il y a un risque d'explosion si la batterie est remplacée de façon incorrecte. Ne la remplacer qu'avec le même modèle ou équivalent recommandé par le constructeur. Recycler les batteries usées en accord avec les instructions du fabricant et les directives gouvernementales de recyclage.

产品中有毒有害物质或元素名称及含量

AAEON System

QO4-381 Rev.A0

部件名称	有毒有害物质或元素					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (Cr(VI))	多溴联苯 (PBB)	多溴二苯 醚(PBDE)
印刷电路板 及其电子组件	×	○	○	○	○	○
外部信号 连接器及线材	×	○	○	○	○	○
外壳	○	○	○	○	○	○
中央处理器 与内存	×	○	○	○	○	○
硬盘	×	○	○	○	○	○
液晶模块	×	×	○	○	○	○
光驱	×	○	○	○	○	○
触控模块	×	○	○	○	○	○
电源	×	○	○	○	○	○
电池	×	○	○	○	○	○

本表格依据 SJ/T 11364 的规定编制。

○：表示该有毒有害物质在该部件所有均质材料中的含量均在 GB/T 26572 标准规定的限量要求以下。

×：表示该有害物质的某一均质材料超出了 GB/T 26572 的限量要求，然而该部件仍符合欧盟指令 2011/65/EU 的规范。

备注：

- 一、此产品所标示之环保使用期限，系指在一般正常使用状况下。
- 二、上述部件物质中央处理器、内存、硬盘、光驱、电源为选购品。
- 三、上述部件物质液晶模块、触控模块仅一体机产品适用。

Hazardous and Toxic Materials List

AAEON System

QO4-381 Rev.A0

Component Name	Hazardous or Toxic Materials or Elements					
	Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (Cr(VI))	Polybrominated biphenyls (PBBS)	Polybrominated diphenyl ethers (PBDES)
PCB and Components	X	O	O	O	O	O
Wires & Connectors for Ext.Connections	X	O	O	O	O	O
Chassis	O	O	O	O	O	O
CPU & RAM	X	O	O	O	O	O
HDD Drive	X	O	O	O	O	O
LCD Module	X	X	O	O	O	O
Optical Drive	X	O	O	O	O	O
Touch Control Module	X	O	O	O	O	O
PSU	X	O	O	O	O	O
Battery	X	O	O	O	O	O

This form is prepared in compliance with the provisions of SJ/T 11364.
 O: The level of toxic or hazardous materials present in this component and its parts is below the limit specified by GB/T 26572.
 X: The level of toxic of hazardous materials present in the component exceed the limits specified by GB/T 26572, but is still in compliance with EU Directive 2011/65/EU (RoHS 2).

Notes:

1. The Environment Friendly Use Period indicated by labelling on this product is applicable only to use under normal conditions.
2. Individual components including the CPU, RAM/memory, HDD, optical drive, and PSU are optional.
3. LCD Module and Touch Control Module only applies to certain products which feature these components.

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Chapter 1

Product Specifications

1.1 Specifications

System

AI Accelerator	Nvidia Jetson Nano
CPU	Quad Core ARM® Cortex®-A57 MPCore Processor
System Memory	4GB LPDDR4
Storage Device	16GB eMMC + Micro SD card slot
Display Interface	Output: HDMI Type A 2.0 x 1 Input: HDMI Type A 1.4 x 1
Ethernet	POE PSE LAN x2 (802.3af)
I/O	USB Type A x 4 for USB3.2 Gen1 RJ45 for GbE LAN with LED Indicator x 1 POE PSE LAN x2 (802.3af) DB-9 x 1 for RS-232/485 DB15 x 1 Female for 13 Channel DIO O.S Flash port x 1 Recovery port x 1 Antenna opening x 2 HDMI Type A 2.0 x 1 (Output) HDMI Type A 1.4 x 1 (Input via MIPI CSI) Power button x 1
Expansion	Mini Card x 1 with SIM slot M.2 E-Key 2230 x 1 (for Wi-Fi)
Indicator	Power LED x 1
OS Support	Nvidia Jetpack 4.5 or above

Power Supply

Power Requirement DC 12V~24V 2-pin terminal block ATX mode

Mechanical

Mounting Wall mount kit (default)

Dimensions (W x D x H) 7.09" x 5.35" x 2.41" (180.0 mm x 136.0 mm x 61.1 mm)

Gross Weight 4.63 lbs. (2.1 kg)

Net Weight 2.87 lbs. (1.3 kg)

Environmental

Operating Temperature 14°F ~ 158°F (-10°C ~ 70°C), with 0.5m/s airflow

Storage Temperature -40°F ~ 176°F (-40°C ~ 80°C)

Storage Humidity 5 ~ 95% @ 40°C, non-condensing

Anti-Vibration Random, 3 Grms, 5~500Hz

Anti-Shock 50G peak acceleration (11 msec. duration, eMMC)

Certification CE/ FCC class A

1.2 Product Notice

Micro-USB: Micro-USB port is designed for flashing image only.

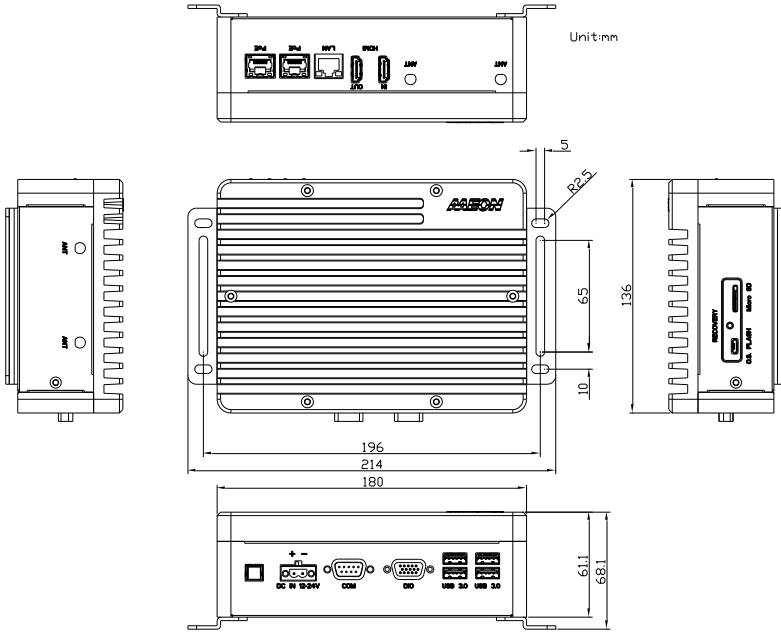
USB ports: USB ports do not support USB DVD-ROM because of file system.

USB 3.2 Gen 1: USB3.2 Gen 1 is the current name for 5Gbps specification, formerly USB 3.0.

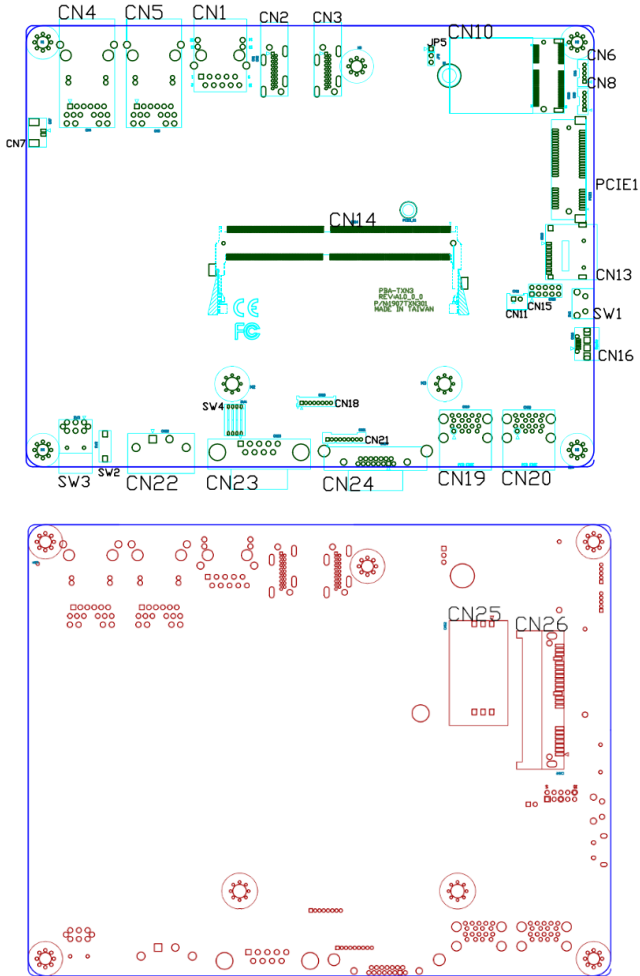
Chapter 2

Hardware Information

2.1 Dimensions



2.2 Jumpers and connectors



2.3 List of Jumpers

The board has a number of jumpers that allow you to configure your system to suit your application.

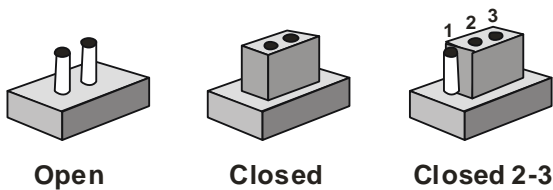
The table below shows the function of each of the board's jumpers

Label	Function
CN15 (Pins 7-8)	AT/ATX mode select
JP5	PCIe/mSATA select

2.3.1 Setting Jumpers

You can configure your system to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch. It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper you connect the pins with the clip.

To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2 and 3. In this case you would connect either pins 1 and 2 or 2 and 3.



A pair of needle-nose pliers may be helpful when working with jumpers.

If you have any questions about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes.

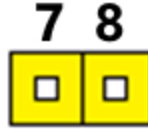
Generally, you simply need a standard cable to make most connections.

2.3.2 AT/ATX Mode Select (CN15 Pins 7-8)

The AT/ATX Mode Select functions by connecting pins 7 and 8 of CN15. To prevent damage to the system, do not connect pins 7 and 8 to any other pin.



Open – AT Mode



Closed – ATX Mode
(Default)

CN14 pins 7-8	Function
7-8 Open	AT Power Mode
7-8 Closed	ATX Power Mode (Default)

Note: Auto Power On is enabled when pins 7-8 are closed (ATX Mode) and disabled when open (AT Mode). If Auto Power is disabled, power button must be used to turn on the system.

2.3.3 PCIe/mSATA Select (JP5)

Pins	Function
1-2	mSATA (no function)
2-3	PCIe (Default)

2.4 List of Connectors

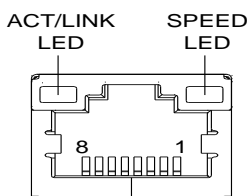
The board has a number of connectors that allow you to configure your system to suit your application.

The table below shows the function of each of the board's connectors

Label	Function
CN1	Gigabit LAN Connector
CN2	HDMI Out Connector
CN3	HDMI In Connector
CN4	POE Gigabit LAN Connector – Intel i210
CN5	POE Gigabit LAN Connector – Intel i210
CN6	Internal USB 2.0 Connector
CN7	RTC Connector
CN8	Internal USB 2.0 Connector
CN10	M.2 E key 2230
CN13	microSD Slot
CN14	Jetson Nano CPU Module Connector
CN15	Front Panel connector
CN16	Micro USB 2.0 for Flash Image
CN18	UART for Debug/I2C
CN19	USB 3.0 Connector
CN20	USB 3.0 Connector
CN21	Internal DIO Connector
CN22	DC Power In Connector
CN23	RS-232/RS-485 Connector
CN24	DIO Connector
CN25	SIM Socket
PCIE1	Mini PCIe Slot

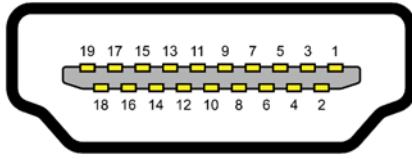
Label	Function
SW1	Recovery switch
SW2	Reset Switch
SW3	Power Switch
SW4	RS-232/RS-485 Select

2.4.1 LAN RJ45 Port (CN1)/ POE RJ45 Port – Intel i210 (CN4/CN5)



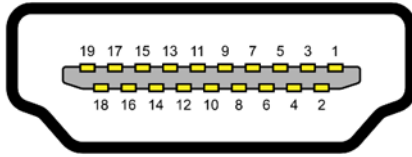
Pin	Signal	Pin	Signal
1	MDI0+	2	MDI0-
3	MDI1+	4	MDI2+
5	MDI2-	6	MDI1-
7	MDI3+	8	MDI3-

2.4.2 HDMI Out Connector (CN2)



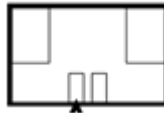
Pin	Signal	Pin	Signal
1	HDMI_DATA2_P	2	GND
3	HDMI_DATA2_N	4	HDMI_DATA1_P
5	GND	6	HDMI_DATA1_N
7	HDMI_DATA0_P	8	GND
9	HDMI_DATA0_N	10	HDMI_CLK_P
11	GND	12	HDMI_CLK_N
13	NC	14	NC
15	HDMI_SCL	16	HDMI_SDA
17	GND	18	HDMI_PWR
19	HDMI_HDP		

2.4.3 HDMI In Connector (CN3)



Pin	Signal	Pin	Signal
1	HDMI_DATA2_P	2	GND
3	HDMI_DATA2_N	4	HDMI_DATA1_P
5	GND	6	HDMI_DATA1_N
7	HDMI_DATA0_P	8	GND
9	HDMI_DATA0_N	10	HDMI_CLK_P
11	GND	12	HDMI_CLK_N
13	NC	14	NC
15	HDMI_SCL	16	HDMI_SDA
17	GND	18	HDMI_PWR
19	HDMI_HDP		

2.4.4 RTC Battery Connector (CN7)



Pin	Signal	Pin	Signal
1	+3V	2	GND

2.4.5 M.2 E-Key 2280 (CN10)

74	3.3V	GND	75
72	3.3V	RESERVED/REFCLKn1	73
70	UIM_POWER_SRC/GPIO1/PEWAKE1#	RESERVED/REFCLKp1	71
68	UIM_POWER_SNK/CLKREQ1#	GND	69
66	UIM_SWP/PERST1#	RESERVED/PETn1	67
64	RESERVED	RESERVED/PETp1	65
62	ALERT# (O)(0/3.3V)	GND	63
60	I2C_CLK (I)(0/3.3V)	RESERVED/PERn1	61
58	I2C_DATA (I/O)(0/3.3V)	RESERVED/PERp1	59
56	W_DISABLE1# (I)(0/3.3V)	GND	57
54	W_DISABLE2# (I)(0/3.3V)	PEWAKE0# (I/O)(0/3.3V)	55
52	PERST0# (I)(0/3.3V)	CLKREQ0# (I/O)(0/3.3V)	53
50	SUSCLK(32kHz) (I)(0/3.3V)	GND	51
48	COEX1 (I/O)(0/1.8V)	REFCLKn0	49
46	COEX2(I/O)(0/1.8V)	REFCLKp0	47
44	COEX3(I/O)(0/1.8V)	GND	45
42	VENDOR DEFINED	PETn0	43
40	VENDOR DEFINED	PETp0	41
38	VENDOR DEFINED	GND	39
36	UART CTS (I)(0/1.8V)	PERn0	37
34	UART RTS (O)(0/1.8V)	PERp0	35
32	UART RXD (I)(0/1.8V)	GND	33
	Module Key	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
	Module Key	Module Key	
22	UART TXD (O)(0/1.8V)	SDIO RESET# (I)(0/1.8V)	23
20	UART WAKE# (O)(0/3.3V)	SDIO WAKE# (O)(0/1.8V)	21
18	GND	SDIO DATA3(I/O)(0/1.8V)	19
16	LED2# (O)(OD)	SDIO DATA2(I/O)(0/1.8V)	17
14	PCM_IN/125 SD_IN (I)(0/1.8V)	SDIO DATA1(I/O)(0/1.8V)	15
12	PCM_OUT/125 SD_OUT (O)(0/1.8V)	SDIO DATA0(I/O)(0/1.8V)	13
10	PCM_SYNC/125 WS (I/O)(0/1.8V)	SDIO CMD(I/O)(0/1.8V)	11
8	PCM_CLK/125 SCK (I/O)(0/1.8V)	SDIO CLK(I)(0/1.8V)	9
6	LED1# (O)(OD)	GND	7
4	3.3V	USB_D-	5
2	3.3V	USB_D+	3
		GND	1

2.4.6 Jetson Nano CPU Module Connector (CN14)

Module Signal Name	Pin #	Pin #	Module Signal Name
GND	1	2	GND
CSI1_D0_N	3	4	CSI0_D0_N
CSI1_D0_P	5	6	CSI0_D0_P
GND	7	8	GND
CSI1_CLK_N	9	10	CSI0_CLK_N
CSI1_CLK_P	11	12	CSI0_CLK_P
GND	13	14	GND
CSI1_D1_N	15	16	CSI0_D1_N
CSI1_D1_P	17	18	CSI0_D1_P
GND	19	20	GND
CSI3_D0_N	21	22	CSI2_D0_N
CSI3_D0_P	23	24	CSI2_D0_P
GND	25	26	GND
CSI3_CLK_N	27	28	CSI2_CLK_N
CSI3_CLK_P	29	30	CSI2_CLK_P
GND	31	32	GND
CSI3_D1_N	33	34	CSI2_D1_N
CSI3_D1_P	35	36	CSI2_D1_P
GND	37	38	GND
DP0_TXD0_N	39	40	CSI4_D2_N
DP0_TXD0_P	41	42	CSI4_D2_P
GND	43	44	GND
DP0_TXD1_N	45	46	CSI4_D0_N
DP0_TXD1_P	47	48	CSI4_D0_P
GND	49	50	GND
DP0_TXD2_N	51	52	CSI4_CLK_N
DP0_TXD2_P	53	54	CSI4_CLK_P
GND	55	56	GND
DP0_TXD3_N	57	58	CSI4_D1_N
DP0_TXD3_P	59	60	CSI4_D1_P
GND	61	62	GND
DP1_TXD0_N	63	64	CSI4_D3_N
DP1_TXD0_P	65	66	CSI4_D3_P
GND	67	68	GND
DP1_TXD1_N	69	70	DSI_D0_N
DP1_TXD1_P	71	72	DSI_D0_P
GND	73	74	GND

Module Signal Name	Pin #	Pin #	Module Signal Name
PCIE0_RX0_P	133	134	PCIE0_TX0_N
GND	135	136	PCIE0_TX0_P
PCIE0_RX1_N	137	138	GND
PCIE0_RX1_P	139	140	PCIE0_TX1_N
GND	141	142	PCIE0_TX1_P
CAN_RX	143	144	GND
KEY	KEY	KEY	KEY
CAN_TX	145	146	GND
GND	147	148	PCIE0_TX2_N
PCIE0_RX2_N	149	150	PCIE0_TX2_P
PCIE0_RX2_P	151	152	GND
GND	153	154	PCIE0_TX3_N
PCIE0_RX3_N	155	156	PCIE0_TX3_P
PCIE0_RX3_P	157	158	GND
GND	159	160	PCIE0_CLK_N
USBSS_RX_N	161	162	PCIE0_CLK_P
USBSS_RX_P	163	164	GND
GND	165	166	USBSS_TX_N
PCIE1_RX0_N	167	168	USBSS_TX_P
PCIE1_RX0_P	169	170	GND
GND	171	172	PCIE1_TX0_N
PCIE1_CLK_N	173	174	PCIE1_TX0_P
PCIE1_CLK_P	175	176	GND
GND	177	178	MOD_SLEEP*
PCIE_WAKE*	179	180	PCIE0_CLKREQ*
PCIE0_RST*	181	182	PCIE1_CLKREQ*
PCIE1_RST*	183	184	GBE_MDIO_N
I2C0_SCL	185	186	GBE_MDIO_P
I2C0_SDA	187	188	GBE_LED_LINK
I2C1_SCL	189	190	GBE_MDIO1_N
I2C1_SDA	191	192	GBE_MDIO1_P
I2S0_DOUT	193	194	GBE_LED_ACT
I2S0_DIN	195	196	GBE_MDIO2_N
I2S0_FS	197	198	GBE_MDIO2_P
I2S0_CLK	199	200	GND
GND	201	202	GBE_MDIO3_N
UART1_TXD	203	204	GBE_MDIO3_P

Module Signal Name	Pin #	Pin #	Module Signal Name
DPI_TXD2_N	75	76	DSI_CLK_N
DPI_TXD2_P	77	78	DSI_CLK_P
GND	79	80	GND
DPI_TXD3_N	81	82	DSI_D1_N
DPI_TXD3_P	83	84	DSI_D1_P
GND	85	86	GND
GPIO00	87	88	DP0_HPD
SPI0_MOSI	89	90	DP0_AUX_N
SPI0_SCK	91	92	DP0_AUX_P
SPI0_MISO	93	94	HDMI_CEC
SPI0_CS0*	95	96	DPI_HPD
SPI0_CS1*	97	98	DPI_AUX_N
UART0_TXD	99	100	DPI_AUX_P
UART0_RXD	101	102	GND
UART0_RTS*	103	104	SPI1_MOSI
UART0_CTS*	105	106	SPI1_SCK
GND	107	108	SPI1_MISO
USB0_D_N	109	110	SPI1_CS0*
USB0_D_P	111	112	SPI1_CS1*
GND	113	114	CAM0_PWDN
USB1_D_N	115	116	CAM0_MCLK
USB1_D_P	117	118	GPIO01
GND	119	120	CAM1_PWDN
USB2_D_N	121	122	CAM1_MCLK
USB2_D_P	123	124	GPIO02
GND	125	126	GPIO03
GPIO04	127	128	GPIO05
GND	129	130	GPIO06
PCIE0_RXD_N	131	132	GND

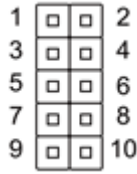
Legend

Ground

Power

Module Signal Name	Pin #	Pin #	Module Signal Name
UART1_RXD	205	206	GPIO07
UART1_RTS*	207	208	GPIO08
UART1_CTS*	209	210	CLK_32K_OUT
GPIO09	211	212	GPIO10
CAM_I2C_SCL	213	214	FORCE_RECOVERY*
CAM_I2C_SDA	215	216	GPIO11
GND	217	218	GPIO12
SDMMC_DAT0	219	220	I2S1_DOUT
SDMMC_DAT1	221	222	I2S1_DIN
SDMMC_DAT2	223	224	I2S1_FS
SDMMC_DAT3	225	226	I2S1_SCLK
SDMMC_CMD	227	228	GPIO13
SDMMC_CLK	229	230	GPIO14
GND	231	232	I2C2_SCL
SHUTDOWN_REQ*	233	234	I2C2_SDA
PMIC_BBAT	235	236	UART2_TXD
POWER_EN	237	238	UART2_RXD
SYS_RESET*	239	240	SLEEP/WAKE*
GND	241	242	GND
GND	243	244	GND
GND	245	246	GND
GND	247	248	GND
GND	249	250	GND
VDD_IN	251	252	VDD_IN
VDD_IN	253	254	VDD_IN
VDD_IN	255	256	VDD_IN
VDD_IN	257	258	VDD_IN
VDD_IN	259	260	VDD_IN

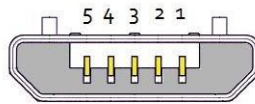
2.4.7 Front Panel Connector (CN15)



Pin	Signal	Pin	Signal
1	Power Button	2	GND
3	Recovery	4	GND
5	Reset	6	GND
7	Latch Set	8	Latch Set
9	PWR LED	10	+5V

Note: Pins 7-8 are used for setting AT/ATX Mode and Auto Power. See Ch 2.3.2 for details.

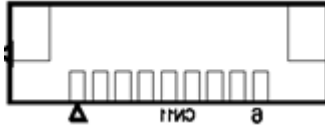
2.4.8 Micro USB 2.0 for Flash Image (CN16)



USB Micro-B

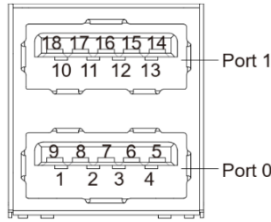
Pin	Signal	Pin	Signal
1		2	USB1-
3	USB1+	4	
5	GND		

2.4.9 UART Debug Port Connector (CN18)



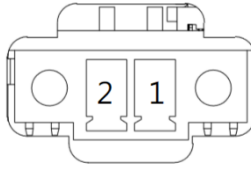
Pin	Signal	Pin	Signal
1	3.3V	2	UART0 TXD
3	UART0 RXD	4	GND
5		6	I2C SCL
7	I2C SDA	8	GND

2.4.10 USB 3.0 Connector (CN19/20)



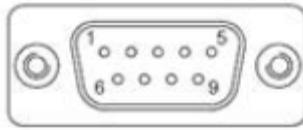
Pin	Signal	Pin	Signal
U1	VBUS_1	U10	VBUS_2
U2	(A)D-	U11	(B)D-
U3	(A)D+	U12	(B)D+
U4	GND	U13	GND
U5	(A)SSRX-	U14	(B)SSRX-
U6	(A)SSRX+	U15	(B)SSRX+
U7	GND	U16	GND
U8	(A)SSTX-	U17	(B)SSTX-
U9	(A)SSTX+	U18	(B)SSTX+

2.4.11 DC Power In Connector (CN22)



Pin	Signal	Pin	Signal
1	PWR IN	2	GND

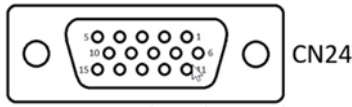
2.4.12 RS-232/RS-485 Connector (CN23)



Pin	RS-232	RS-422	RS-485
1		TX-	RA-
2	RXD	TX+	RA+
3	TXD	RX+	
4		RX-	
5	GND		
6			
7	CAN0 L	CAN0 L	CAN0 L
8	CAN0 H	CAN0 H	CAN0 H
9			

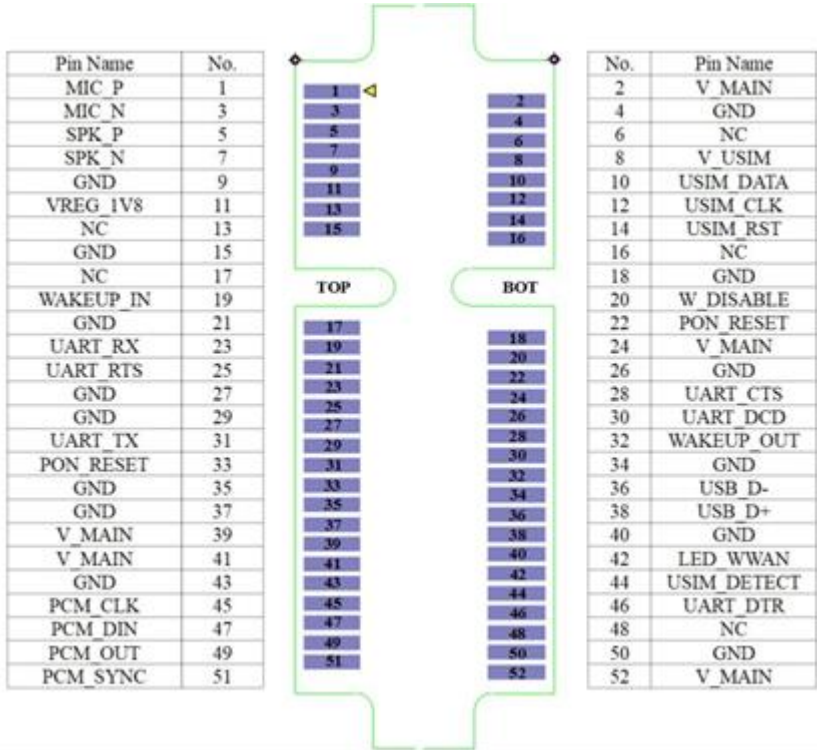
Note: RS-232/485 mode is controlled by SW4. See Ch 2.4.31 for setting details.

2.4.13 External DIO Connector (CN24)



Pin	Signal	Pin	SYSFS GPIO
1	+3.3V	1	Power
2	37P_SPI1_MOSI_LS	2	GPIO12
3	22P_SPI1_MISO_LS	3	GPIO13
4	13P_SPI1_SCK_LS	4	GPIO14
5	18P_SPI1_CS0_LS	5	GPIO15
6	19P_SPI0_MOSI_LS	6	GPIO16
7	21P_SPI0_MISO_LS	7	GPIO17
8	23P_SPI0_SCK_LS	8	GPIO18
9	24P_SPI0_CS0_LS	9	GPIO19
10	26P_SPI0_CS1_LS	10	GPIO20
11	35P_I2S0_LRCK_LS	11	GPIO76
12	38P_I2S0_SDIN_LS	12	GPIO77
13	40P_I2S0_SDOOUT_LS	13	GPIO78
14	12P_I2S0_SCLK_LS	14	GPIO79
15	GND	15	GND

2.4.14 Mini PCIe Connector (PCIE1)



2.4.15 RS-232/485 Select (SW4)



Mode	S-1	S-2	S-3	S-4
1T/1R RS-232	On	On		
Reserved – No Function	On	Off		
1T/1R RS-485	Off	On		
Low Power Shutdown	Off	Off		
Enable RS-422/RS-485 bias and termination resistors			On	
Disable RS-422/RS-485 bias and termination resistors			Off	
250kbps for RS-232 and RS-485/RS-422				On
RS-232 3Mbps RS-485/RS-422 20Mbps				Off

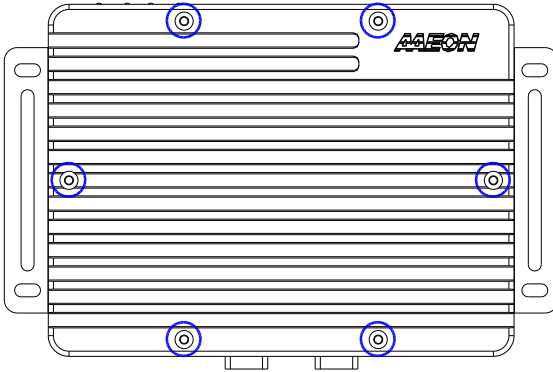
Note: SW4 controls the RS-232/485 mode for CN23. See Ch 2.4.23 for Pin Definitions

2.5 Hardware Assembly

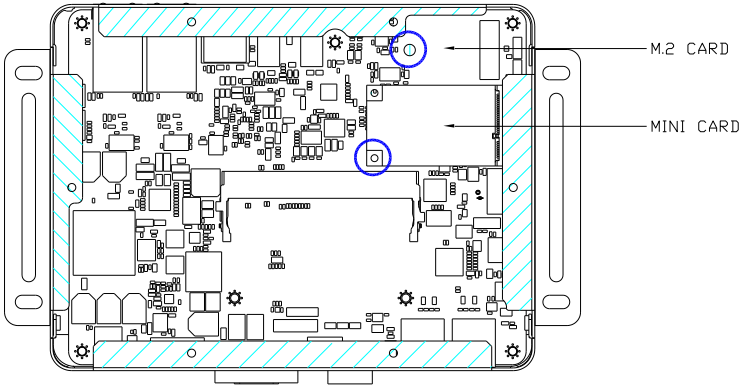
This section details the hardware assembly steps for the BOXER-8223AI. Please read this section thoroughly before beginning installation and ensure you have all necessary components ready. A Phillips head screwdriver is required.

2.5.1 Expansion Card Installation

Step 1: Remove the top chassis cover by unscrewing the six screws as shown:

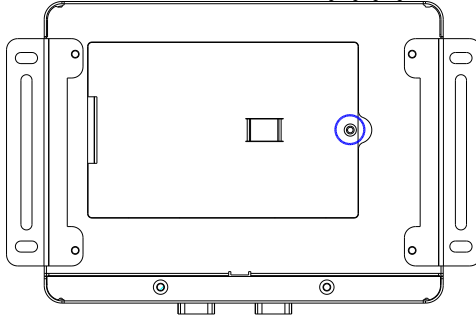


Step 2: Locate the M.2 and Mini-Card slots as shown. Follow standard procedures for installing expansion cards. Note the location of the mounting screws.

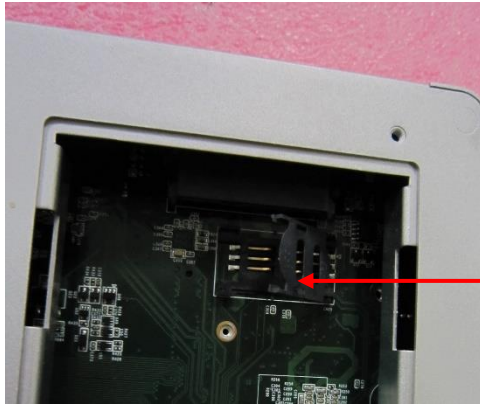


2.5.2 SIM Card Installation

Step 1: Remove the bottom panel by loosening the retaining screw as shown:

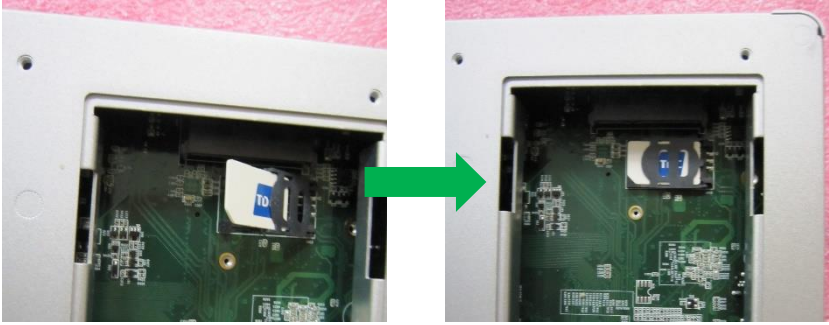


Step 2: Open the SIM card holder.



SIM Card Holder

Step 3: Slide the SIM card into the holder, then gently press down to secure in place.



Chapter 3

OS Flash Guide

3.1 Flash OS Image to System

3.1.1 Introduction

This chapter details the steps to flashing the operating system to your BOXER-8223AI NVIDIA Jetson Nano system. The operating system image can be downloaded from the product page at:

<https://www.aaeon.com/en/p/edge-ai-box-pc-nvidia-jetson-nano-boxer-8223ai>

After downloading the file, the filename will be formatted as follows:

Ubuntu_18.04_{OS_IF}._{PLF_IF}._{PJ_IF}._{BN}.tar.gz

{OS_IF} is OS Information. For example, the entry UB1804X decodes as UB for Ubuntu, 1804 for version 18.04, and X for desktop version.

{PLF_IF} is Platform Information and may be coded as NV03.

{PJ_IF} is Project Information and displays which model this image pertains to. Make sure it says BOXER-8223AI.

{BN} is Build Number, such as 0, 1, 2, 3 and so on.

For example, build number 4 will be named as:

Ubuntu_18.04_UB1804X.NV03.BOXER-8223AI.4.tar.gz

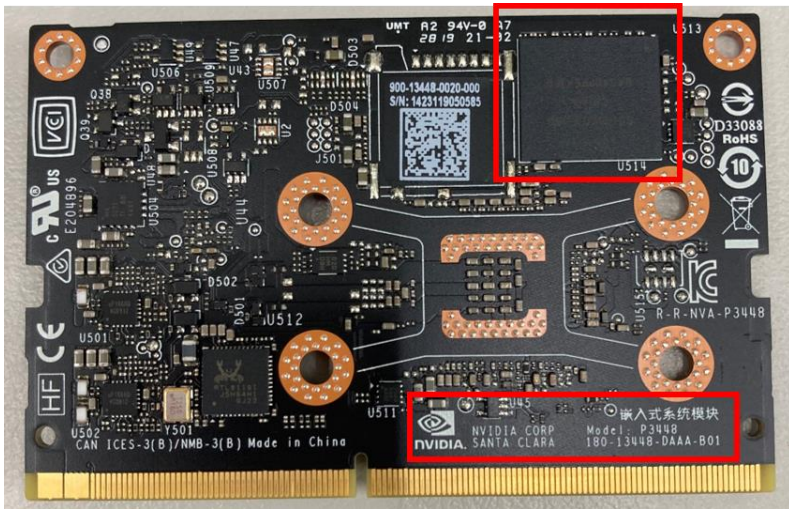
If you have any questions or need help, please contact AAEON support or your AAEON representative for assistance.

3.1.2 Before You Begin

Before beginning the process ensure you have the following:

- One host PC with operating system Ubuntu 16.04 or 18.04
- Operating System image downloaded to host computer
- USB Cable with at least one Micro USB connector
- Jetson Nano Development Kit B01 module (with onboard eMMC storage); see

image below for reference:



Finally, on the Linux host PC, extract the image file you downloaded using the following command in terminal (remember to replace {} with the actual file name):

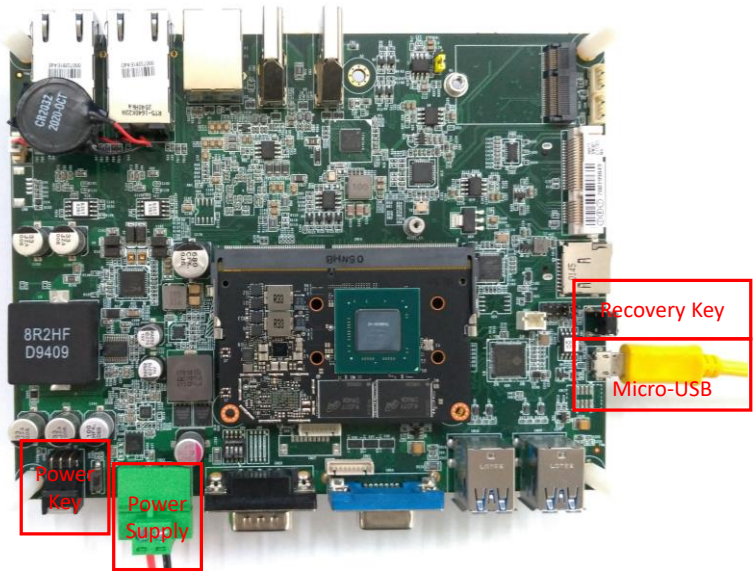
```
$ tar xzf Ubuntu_18.04_{OS_IF}._{PLF_IF}._{PJ_IF}._{BN}.tar.gz
```

3.1.3 Flash Image to Board

Step 1: Connect the host PC to the BOXER-8223AI with a Micro USB cable.

Step 2: Connect the BOXER-8223AI carrier board to a 12~24V power supply.

Step 3: Force Recovery Mode: Press and hold the recovery key, then press the power key. After 2 seconds, release the recovery key. The BOXER-8223AI should enter recovery mode.



Step 5: You can use `lsusb` command on host PC to check if the device is in recovery mode:

```
$ lsusb | grep 0955:7f21
```

You should see the following return if device is in recovery mode:

```
~/Ubuntu_18.04_UB1804X.NV03.BOXER-8223AI-B.1/bootloader $ lsusb | grep 0955:7f21
Bus 003 Device 076: ID 0955:7f21 NVidia Corp.
```

Step 6: In terminal, enter the bootloader folder that you unzipped earlier.

Step 7: Run the following command to flash the image:

```
$ sudo ./flashall.sh
```

```
~/Ubuntu_18.04_UB1804X.NV03.BOXER-8223AI-B.1/bootloader $ sudo ./flashall.sh
[sudo] password for marble:
Welcome to Tegra Flash
version 1.0.0
Type ? or help for help and q or quit to exit
Use ! to execute system commands
[ 0.0012 ] tegrasign --key None --getnode mode.txt
[ 0.0027 ] Assuming zero filled SBK key
[ 0.0029 ]
[ 0.0030 ] Generating RCM messages
[ 0.0040 ] tegrarcn --listrcm rcm_list.xml --chip 0x21 0 --download rcm nvtboot_recovery.bin 0 0
[ 0.0050 ] RCM 0 is saved as rcm_0.rcm
[ 0.0218 ] RCM 1 is saved as rcm_1.rcm
[ 0.0218 ] List of rcm files are saved in rcm_list.xml
[ 0.0218 ]
[ 0.0219 ] Signing RCM messages
[ 0.0235 ] tegrasign --key None --list rcm_list.xml --pubkeyhash pub_key.key
[ 0.0245 ] Assuming zero filled SBK key
```

Step 8: Wait for the process to complete.

```
[ 474.0879 ] [.....] 100%
[ 474.0978 ] Writing partition LNX with boot.img.encrypt
[ 474.1334 ] [.....] 100%
[ 474.1641 ] Writing partition DTB-1 with kernel_tegra210-p3448-0002-p3449-0000-b00-boxe
[ 474.2103 ] [.....] 100%
[ 474.2266 ] Writing partition TOS-1 with tos-mon-only.img.encrypt
[ 474.2265 ] [.....] 100%
[ 474.2785 ] Writing partition EKS-1 with eks.img
[ 474.3145 ] [.....] 100%
[ 474.3251 ] Writing partition LNX-1 with boot.img.encrypt
[ 474.3614 ] [.....] 100%
[ 474.3917 ] Writing partition BMP with bmp.blob
[ 474.4598 ] [.....] 100%
[ 474.4845 ] Writing partition RP4 with rp4.blob
[ 474.5232 ] [.....] 100%
[ 474.5740 ]
[ 474.5951 ] tegradevflash --write BCT P3448_A00_lpddr4_204Mhz_P987.bct
[ 474.5971 ] Cboot version 00.01.0000
[ 474.5994 ] Writing partition BCT with P3448_A00_lpddr4_204Mhz_P987.bct
[ 474.6001 ] [.....] 100%
[ 475.0060 ]
[ 475.0061 ] Flashing completed
[ 475.0062 ] Coldbooting the device
[ 475.0082 ] tegradevflash --reboot coldboot
[ 475.0099 ] Cboot version 00.01.0000
[ 475.0128 ]
~/Ubuntu_18.04_UB1804X.NV03.BOXER-8223AI-B.1/bootloader $
```

Appendix A

Glue Removal Procedure

A.1 Removing Glue from Your System

To protect components from damage and ensure proper operation out of the box, glue may have been applied to some cables or connectors to keep them in place during shipping. This glue must be removed before attempting to swap components or perform maintenance. This section details the steps needed to remove the glue.

Before performing any kind of system maintenance, ensure the system is shut down (not in sleep or hibernate mode) and the power cable has been removed. Follow steps in Chapter 2 to access the components inside.

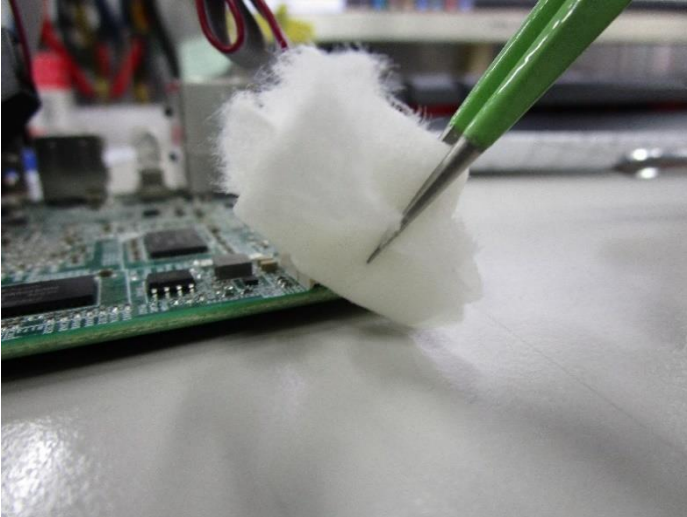
You will need the following items for this step:

- Cotton or cotton swab
- Anti-static tweezers
- An alcohol solution that is at least 99.5% alcohol (ethanol solution or denatured alcohol). AAeon recommends using an eye dropper or a bottle with a nozzle as in the picture below:

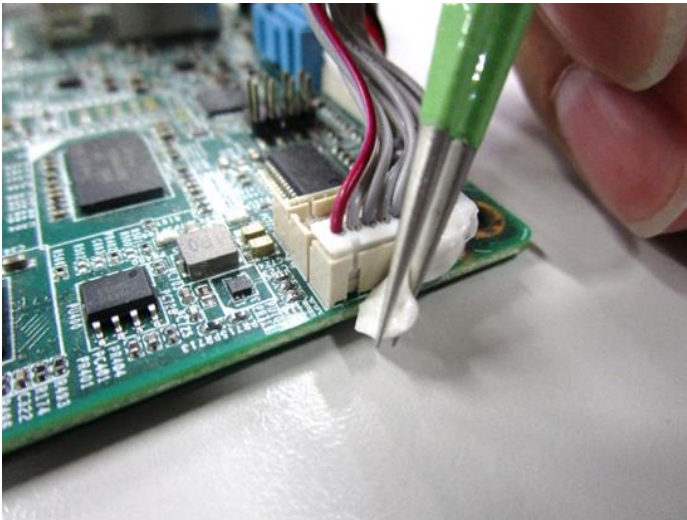


Step 1: Using an eyedropper or bottle as shown above, apply a few drops of alcohol to the glue.

Step 2: Allow the alcohol to soak for 10 seconds, then use a cotton swab or cotton with anti-static tweezers to evenly rub the alcohol over the glue.



Step 3: Let soak for 10 more seconds, then use anti-static tweezers to remove the glue.



If you encounter any issues or need support, please contact your AAEON representative or visit our [Support Page](#) at AAEON.com