ACP-1106

10.1" Infotainment Multi-Touch Panel PC

User's Manual 5th Ed

Last Updated: December 3, 2020

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Acknowledgement

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Packing List

Before setting up your product, please make sure the following items have been shipped:

Item	Quantity
● ACP-1106	1
● Panel mount kit	1
● VESA Screws M4x6mm	4

If any of these items are missing or damaged, please contact your distributor or sales representative immediately.

About this Document

This User's Manual contains all the essential information, such as detailed descriptions and explanations on the product's hardware and software features (if any), its specifications, dimensions, jumper/connector settings/definitions, and driver installation instructions (if any), to facilitate users in setting up their product.

Users may refer to the product page at AAEON.com for the latest version of this document.

Safety Precautions

Please read the following safety instructions carefully. It is advised that you keep this manual for future references

1. All cautions and warnings on the device should be noted.
2. All cables and adapters supplied by AAEON are certified and in accordance with the material safety laws and regulations of the country of sale. Do not use any cables or adapters not supplied by AAEON to prevent system malfunction or fires.
3. Make sure the power source matches the power rating of the device.
4. Position the power cord so that people cannot step on it. Do not place anything over the power cord.
5. Always completely disconnect the power before working on the system's hardware.
6. No connections should be made when the system is powered as a sudden rush of power may damage sensitive electronic components.
7. If the device is not to be used for a long time, disconnect it from the power supply to avoid damage by transient over-voltage.
8. Always disconnect this device from any AC supply before cleaning.
9. While cleaning, use a damp cloth instead of liquid or spray detergents.
10. Make sure the device is installed near a power outlet and is easily accessible.
11. Keep this device away from humidity.
12. Place the device on a solid surface during installation to prevent falls
13. Do not cover the openings on the device to ensure optimal heat dissipation.
14. Watch out for high temperatures when the system is running.
15. Do not touch the heat sink or heat spreader when the system is running
16. Never pour any liquid into the openings. This could cause fire or electric shock.

17. As most electronic components are sensitive to static electrical charge, be sure to ground yourself to prevent static charge when installing the internal components. Use a grounding wrist strap and contain all electronic components in any static-shielded containers.
18. If any of the following situations arises, please the contact our service personnel:
    i. Damaged power cord or plug
    ii. Liquid intrusion to the device
    iii. Exposure to moisture
    iv. Device is not working as expected or in a manner as described in this manual
    v. The device is dropped or damaged
    vi. Any obvious signs of damage displayed on the device
19. DO NOT LEAVE THIS DEVICE IN AN UNCONTROLLED ENVIRONMENT WITH TEMPERATURES BEYOND THE DEVICE'S PERMITTED STORAGE TEMPERATURES (SEE CHAPTER 1) TO PREVENT DAMAGE.

FCC Statement

This device complies with Part 15 FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received including interference that may cause undesired operation.

Caution:

There is a danger of explosion if the battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions and your local government's recycling or disposal directives.

Attention:

Il y a un risque d'explosion si la batterie est remplacée de façon incorrecte.
Ne la remplacer qu'avec le même modèle ou équivalent recommandé par le constructeur. Recycler les batteries usées en accord avec les instructions du fabricant et les directives gouvernementales de recyclage.

China RoHS Requirements (CN)

产品中有毒有害物质或元素名称及含量

AAEON System QO4-381 Rev.A0

部件名称	有毒有害物质或元素					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (Cr(VI))	多溴联苯 (PBB)	多溴二苯醚(PBDE)
印刷电路板及其电子组件	×	○	○	○	○	○
外部信号连接器及线材	×	○	○	○	○	○
外壳	○	○	○	○	○	○
中央处理器与内存	×	○	○	○	○	○
硬盘	×	○	○	○	○	○
液晶模块	×	×	○	○	○	○
光驱	×	○	○	○	○	○
触控模块	×	○	○	○	○	○
电源	×	○	○	○	○	○
电池	×	○	○	○	○	○

本表格依据 SJ/T 11364 的规定编制。

○：表示该有毒有害物质在该部件所有均质材料中的含量均在GB/T 26572标准规定的限量要求以下。

×：表示该有害物质的某一均质材料超出了GB/T 26572的限量要求，然而该部件仍符合欧盟指令2011/65/EU 的规范。

备注:

一、此产品所标示之环保使用期限，系指在一般正常使用状况下。

二、上述部件物质中央处理器、内存、硬盘、光驱、电源为选购品。

三、上述部件物质液晶模块、触控模块仅一体机产品适用。

China RoHS Requirement (EN)

Hazardous and Toxic Materials List

AAEON System QO4-381 Rev.A0

Component Name	Hazardous or Toxic Materials or Elements					
	Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (Cr(VI))	Polybrominated biphenyls (PBBs)	Polybrominated diphenyl ethers (PBDEs)
PCB and Components	X	O	O	O	O	O
Wires & Connectors for Ext.Connections	X	O	O	O	O	O
Chassis	O	O	O	O	O	O
CPU & RAM	X	O	O	O	O	O
HDD Drive	X	O	O	O	O	O
LCD Module	X	X	O	O	O	O
Optical Drive	X	O	O	O	O	O
Touch Control Module	X	O	O	O	O	O
PSU	X	O	O	O	O	O
Battery	X	O	O	O	O	O

This form is prepared in compliance with the provisions of SJ/T 11364.

O: The level of toxic or hazardous materials present in this component and its parts is below the limit specified by GB/T 26572.

X: The level of toxic of hazardous materials present in the component exceed the limits specified by GB/T 26572, but is still in compliance with EU Directive 2011/65/EU (RoHS 2).

Notes:

1. The Environment Friendly Use Period indicated by labelling on this product is applicable only to use under normal conditions.
2. Individual components including the CPU, RAM/memory, HDD, optical drive, and PSU are optional.
3. LCD Module and Touch Control Module only applies to certain products which feature these components.

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Chapter 1

Product Specifications

1.1 Specifications

System	
Processor	Intel® Pentium® N4200 processor, 1.1 GHz (Quad-Core), Burst frequency 2.5GHz Intel® Celeron® N3350 processor, 1.1 GHz (Dual-Core), Burst frequency 2.4GHz
System Memory	204-pin DDR3L SODIMM x 1, Build-in 4GB, Max. 8GB
LCD/CRT controller	Integrated in processor
Ethernet	10/100/1000Base-TX, RJ-45 x 2
Bottom I/O Port	DB9 Type RS-232 x 1 USB 3.0 x 2 HDMI x 1 RJ-45 10/100/1000 LAN x 2 2 Pin terminal block for power input Power button x 1
Top I/O Port	Power Button x 1 Type A USB 2.0 x 1 Expansion I/O Slot
Storage Disk Drive	Full Size mSATA x 1
Expansion Slot	Half size mini card x 1(Internal)
OS support	Windows® 10, Linux Kernal 4.4.0 or higher

Mechanical

Construction	Aluminum Front Bezel + Metal Chassis
Mounting	VESA 75, Panel Mount
Dimension	10.47" x 7.24 x 1.89" (266 x 184 x 48mm)
Carton Dimension	13.38" x 7.64 x 9.96" (340 x 194 x 253mm)
Gross Weight	6.38 lb (2.9 kg)

Environmental

Operating Temperature	23°F ~ 122°F (-5°C ~ 50°C) with 0.5 m/s airflow
Storage Temperature	-4°F ~ 158°F (-20°C ~ 70°C)
Storage Humidity	90% @40°C; non-condensing
Vibration	1 Grms/ 5~ 500Hz/ operation – with HDD
Shock	—
EMC	CE/FCC Class A

Power Supply

DC Input	DC 9~30 V

LCD

Display Type	10.1" TFT-LCD, LED
Max Resolution	1280 x 800
Max. Colors	262K colors (6 bit for R,G,B)
Luminance	400 cd/m2
Viewing Angle	170°(H), 170°(V)
Back Light	LED
Back Light MTBF (Hours)	25,000 hours

Touchscreen

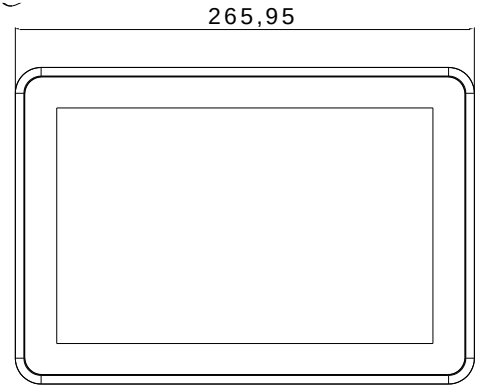
Type	P-CAP Touchscreen
Light Transmission	90%

Chapter 2

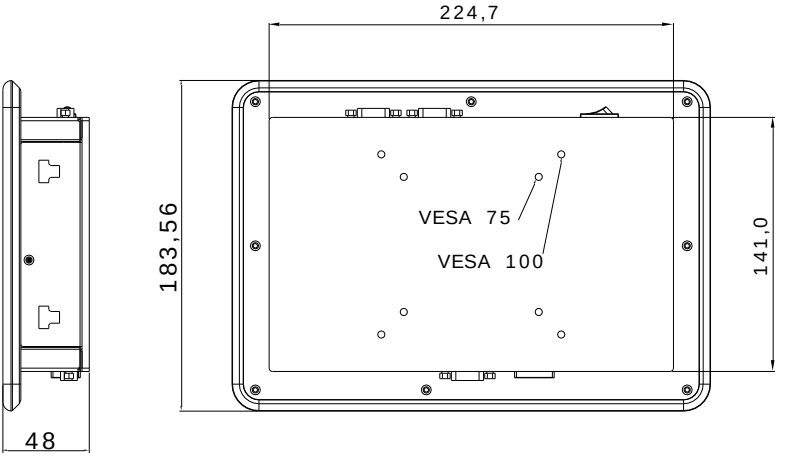
Hardware Information

2.1 Dimensions

Front (Panel) Side

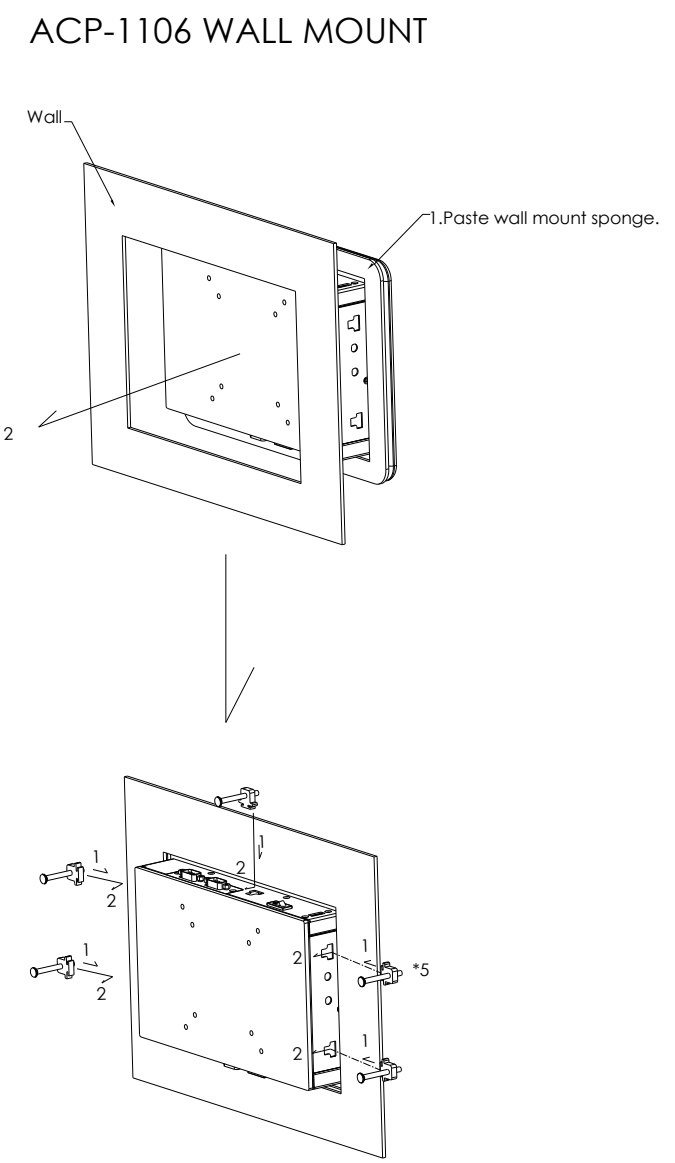


Side and Rear (System) Sides



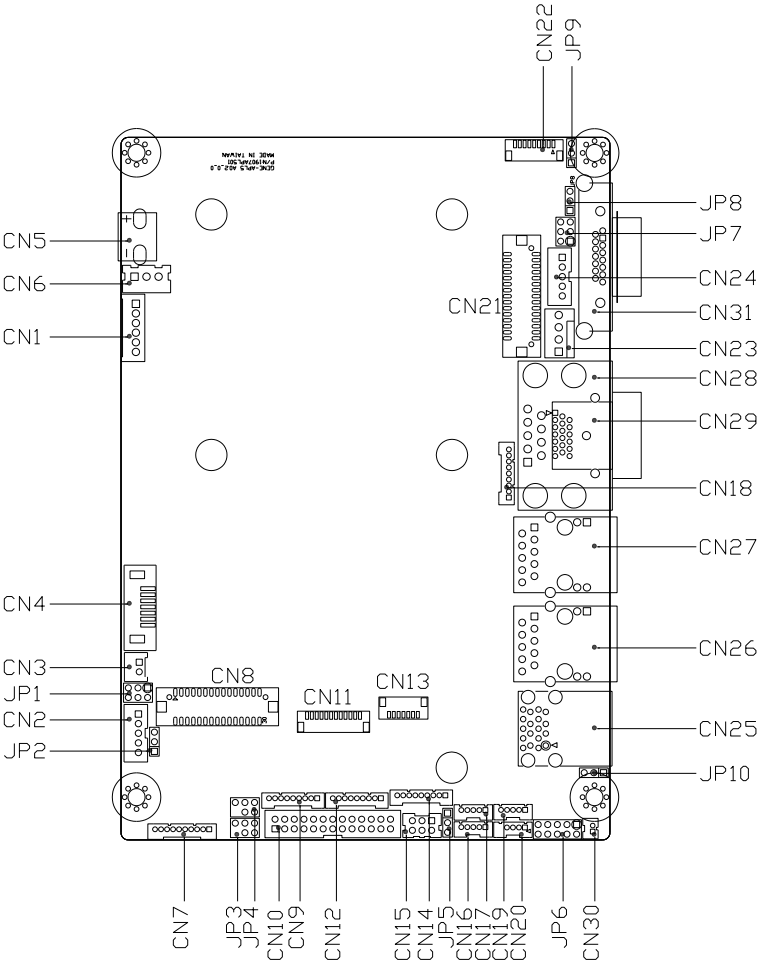
VESA SCREW:M4*6mm*4Pcs
Mounting Cutout:229*155mm

2.2 Panel Mount Guide

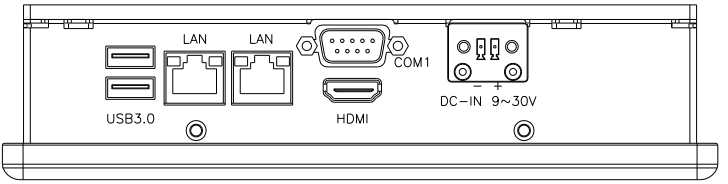


2.3 Jumpers and Connectors

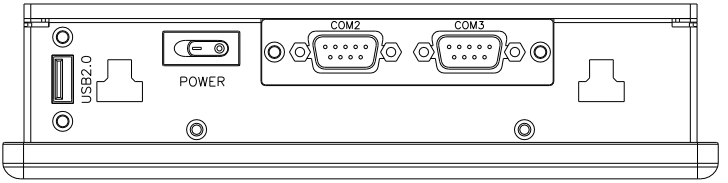
Jumpers (Board)



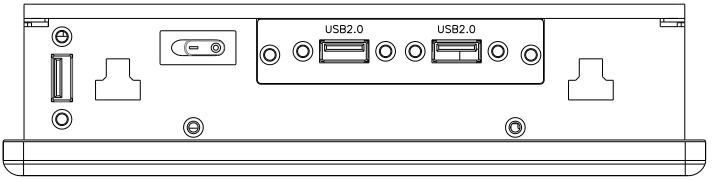
Connectors



COM Option



USB Option

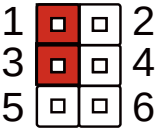


2.4 List of Jumpers

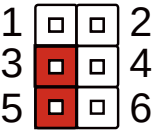
Please refer to the table below for all of the board's jumpers that you can configure for your application

Label	Function
JP1	LVDS Port1 Backlight Inverter VCC Selection and Operating VDD Selection
JP2	LVDS Port1 Backlight Lightness Control Mode Selection
JP3	COM2 Pin8 Function Selection
JP4	COM3 Pin8 Function Selection
JP5	Auto Power Button Enable/Disable Selection
JP6	Front Panel Connector
JP9	Touch Screen 4/5/8-wire Mode Selection
JP10	Clear CMOS Jumper

2.4.1 LVDS Port 1 Backlight Inverter VCC Selection (JP1)

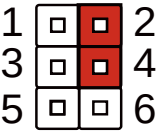


+12V

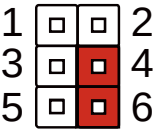


+5V (Default)

2.4.2 LVDS Port 1 Operating VDD Selection (JP1)

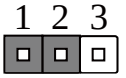


+3.3V (Default)

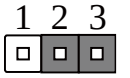


+5V

2.4.3 LVDS Port 1 Backlight Lightness Control Mode Selection (JP2)

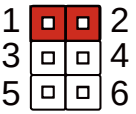


VR Mode

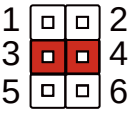


PWM Mode (Default)

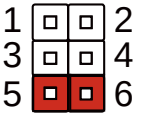
2.4.4 COM2 Pin8 Function Selection (JP3)



+12V

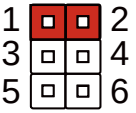


Ring (Default)

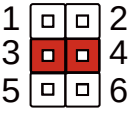


+5V

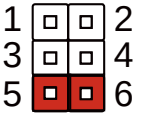
2.4.5 COM3 Pin8 Function Selection (JP4)



+12V

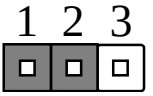


Ring (Default)

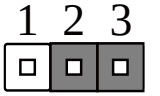


+5V

2.4.6 Auto Power Button Enable/Disable Selection (JP5)

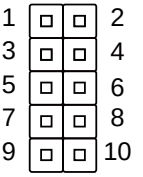


Enable/ATX (Default)



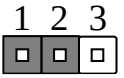
Disable/AT

2.4.7 Front Panel Connector (JP6)

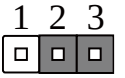


Pin	Pin Name	Pin	Pin Name
1	PWR_BTN-	2	PWR_BTN+
3	HDD_LED-	4	HDD_LED+
5	SPEAKER-	6	SPEAKER+
7	PWR_LED-	8	PWR_LED+
9	H/W RESET-	10	H/W RESET+

2.4.8 Touch Screen 4, 5, 8 Wire Selection (JP9)

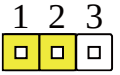


4/8 Wires Mode

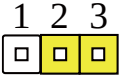


5 Wires Mode (Default)

2.4.9 Clear CMOS Jumper (JP10)



Normal (Default)



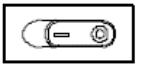
Clear CMOS

2.5 List of Connectors

Please refer to the table below for all of the board's connectors that you can configure for your application

Label	Function
Power	Power On/ Off
USB 2.0	USB 2.0 Port
COM2/ COM3	RS-232/ RS-422/ RS-485
DC-IN	DC Power Input 9 – 30V
COM1	RS-232
HDMI	HDMI Port
USB 3.0	USB 3.0 Port
LAN	10/ 100/ 1000 Ethernet Port

2.5.1 Power Button: On/Off



POWER

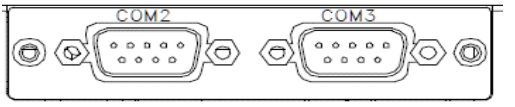
2.5.2 USB 2.0 Port

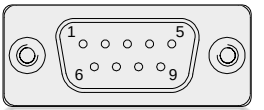
Pin	Pin Name	Signal Type	Signal Level
1	+5VSB	PWR	+5V
2	USB4_D-	DIFF	
3	USB4_D+	DIFF	
4	GND	GND	

2.5.3 USB 2.0 Port x 2 (optional)

Pin	Pin Name	Signal Type	Signal Level
1	+5VSB	PWR	+5V
2	USB4_D-	DIFF	
3	USB4_D+	DIFF	
4	GND	GND	

2.5.4 COM2/ COM3 Port (RS-232/ RS-422/ RS-485)



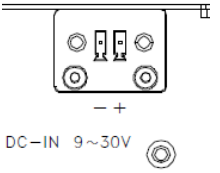


RS-232			
Pin	Pin Name	Signal Type	Signal Level
1	DCD	IN	
2	RX	IN	
3	TX	OUT	±5V
4	DTR	OUT	±5V
5	GND	GND	
6	DSR	IN	
7	RTS	OUT	±5V
8	CTS	IN	
9	RI/ +5V/ +12V	IN/ PWR	+5V/ +12V

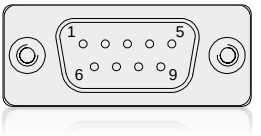
RS-422			
Pin	Pin Name	Signal Type	Signal Level
1	RS422_TX-	OUT	±5V
2	RS422_TX+	OUT	±5V
3	RS422_RX+	IN	-
4	RS422_RX-	IN	-
5	GND	GND	-
6	NC		-
7	NC		-
8	NC		-
9	NC/ +5V/ +12V	PWR	+5V/ +12V

RS-485			
Pin	Pin Name	Signal Type	Signal Level
1	RS485_D-	I/O	±5V
2	RS485_D+	I/O	±5V
3	NC		-
4	NC		-
5	GND	GND	-
6	NC		-
7	NC		-
8	NC		-
9	NC/ +5V/ +12V	PWR	+5V/ +12V

2.5.5 DC-In Connector

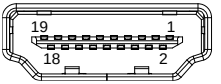


2.5.6 COM1 Port (RS-232)



Pin	Pin Name	Signal Type	Signal Level
1	DCD	IN	
2	RX	IN	
3	TX	OUT	±9V
4	DTR	OUT	±9V
5	GND	GND	
6	DSR	IN	
7	RTS	OUT	±9V
8	CTS	IN	
9	RI	IN	

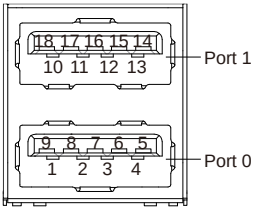
2.5.7 HDMI Port



Pin	Pin Name	Signal Type	Signal Level
1	TMDS_DAT2+	DIFF	
2	GND	GND	
3	TMDS_DAT2-	DIFF	
4	TMDS_DAT1+	DIFF	
5	GND	GND	
6	TMDS_DAT1-	DIFF	
7	TMDS_DAT0+	DIFF	

Pin	Pin Name	Signal Type	Signal Level
8	GND	GND	
9	TMDS_DAT0-	DIFF	
10	TMDS_CLK+	DIFF	
11	GND	GND	
12	TMDS_CLK-	DIFF	
13	NC		
14	NC		
15	DDC_CLK	I/O	+5V
16	DDC_DATA	I/O	+5V
17	GND	GND	
18	+5V	I/O	+5V
19	HPLG_DETECT	IN	

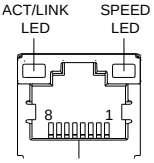
2.5.8 USB 3.0 Port x 2



Pin	Pin Name	Signal Type	Signal Level
1	+5VSB	PWR	+5V
2	USB_D-	DIFF	
3	USB_D+	DIFF	
4	GND	GND	
5	USB_SSRX–	DIFF	
6	USB_SSRX+	DIFF	
7	GND	GND	
8	USB_SSTX–	DIFF	
9	USB_SSTX+	DIFF	

Pin	Pin Name	Signal Type	Signal Level
10	+5VSB	PWR	+5V
11	USB_D-	DIFF	
12	USB_D+	DIFF	
13	GND	GND	
14	USB_SSRX–	DIFF	
15	USB_SSRX+	DIFF	
16	GND	GND	
17	USB_SSTX–	DIFF	
18	USB_SSTX+	DIFF	

2.5.9 LAN Port x 2



Pin	Pin Name	Signal Type	Signal Level
1	MDI0+	DIFF	
2	MDI0-	DIFF	
3	MDI1+	DIFF	
4	MDI2+	DIFF	
5	MDI2-	DIFF	
6	MDI1-	DIFF	
7	MDI3+	DIFF	
8	MDI3-	DIFF	

Chapter 3

AMI BIOS Setup

3.1 System Test and Initialization

The system uses certain routines to perform testing and initialization. If an error, fatal or non-fatal, is encountered, a few short beeps or an error message will be outputted. The board can usually continue the boot up sequence with non-fatal errors.

The system configuration verification routines check the current system configuration against the values stored in the CMOS memory. If they do not match, an error message will be outputted, in which case you will need to run the BIOS setup program to set the configuration information in memory.

There are three situations in which you will need to change the CMOS settings:

- You are starting your system for the first time
- You have changed your system's hardware
- The CMOS memory has lost power and the configuration information is erased

The system's CMOS memory uses a backup battery for data retention, which is to be replaced once emptied.

3.2 AMI BIOS Setup

The AMI BIOS ROM has a pre-installed Setup program that allows users to modify basic system configurations, which is stored in the battery-backed CMOS RAM and BIOS NVRAM so that the information is retained when the power is turned off.

To enter BIOS Setup, press <Del> immediately while your computer is powering up.

The function for each interface can be found below.

Main – Date and time can be set here. Press <Tab> to switch between date elements

Advanced – Enable/ Disable boot option for legacy network devices

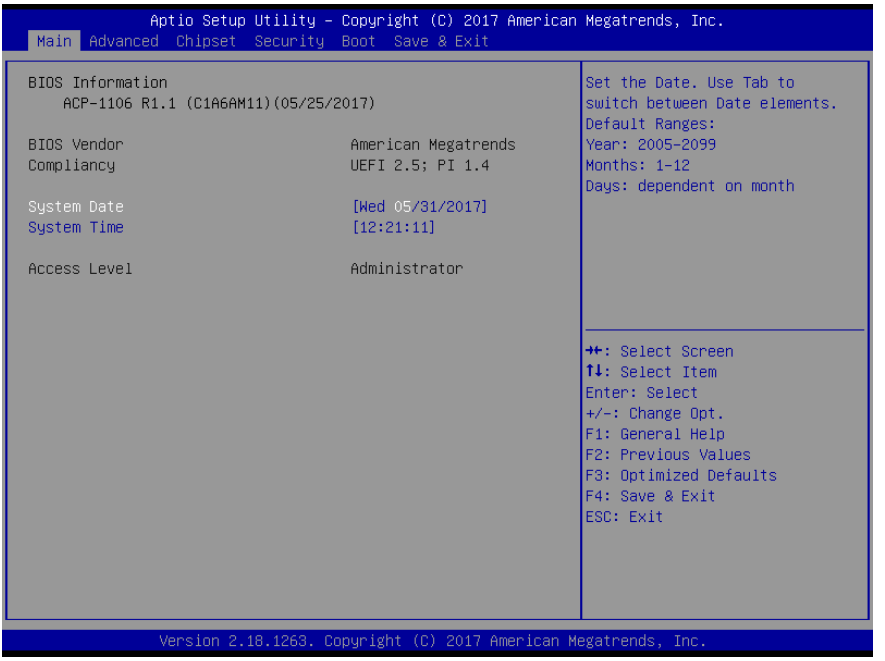
Chipset – For hosting bridge parameters

Boot – Enable/ Disable quiet Boot Option

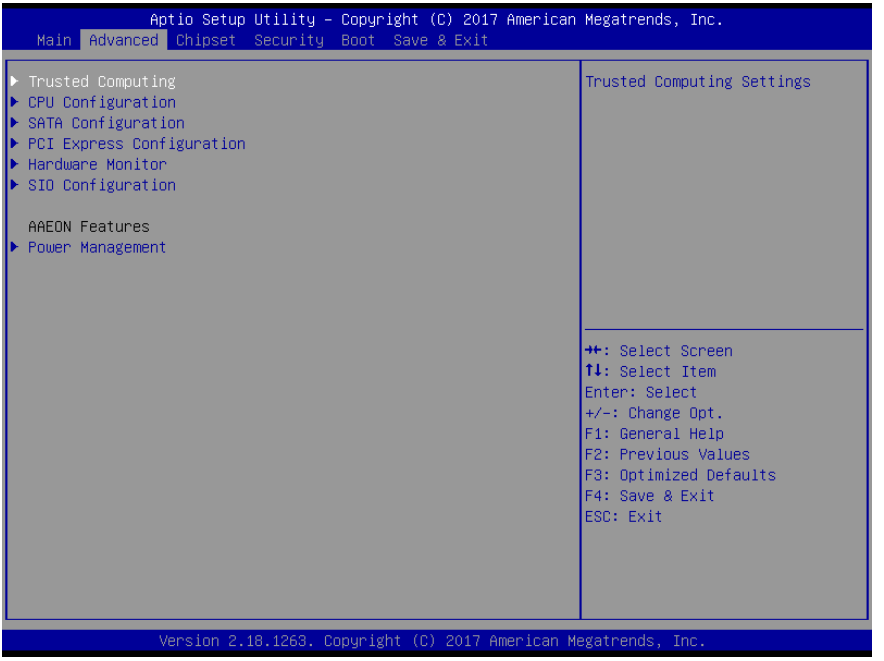
Security – The setup administrator password can be set here

Save & Exit –Save your changes and exit the program

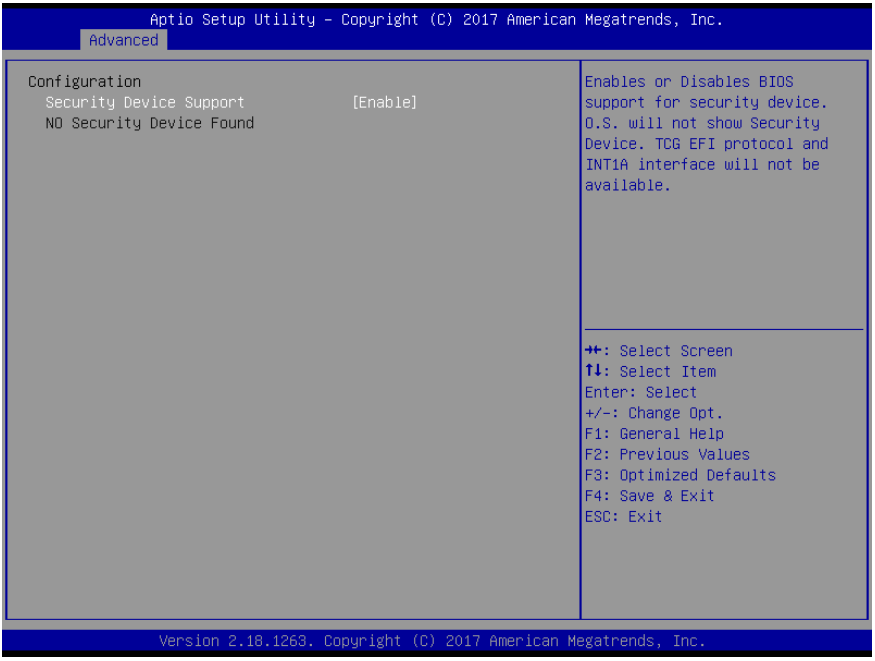
3.3 Setup Submenu: Main



3.4 Setup Submenu: Advanced



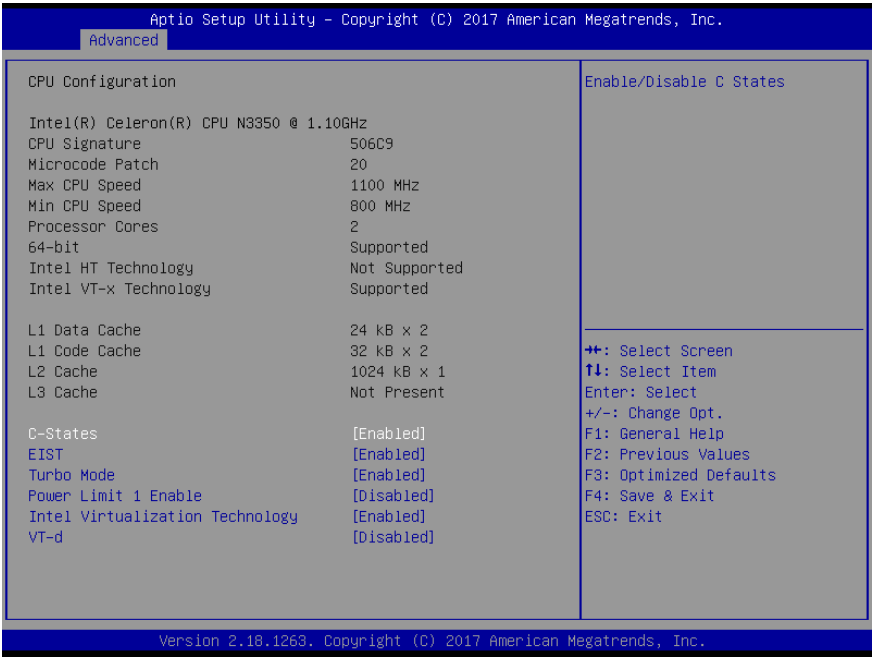
3.4.1 Advanced: Trusted Computing



Options summary:

Security Device Support	Enabled	Optimal Default, Failsafe Default
	Disabled	
Enable or Disable SATA BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.		

3.4.2 Advanced: CPU Configuration

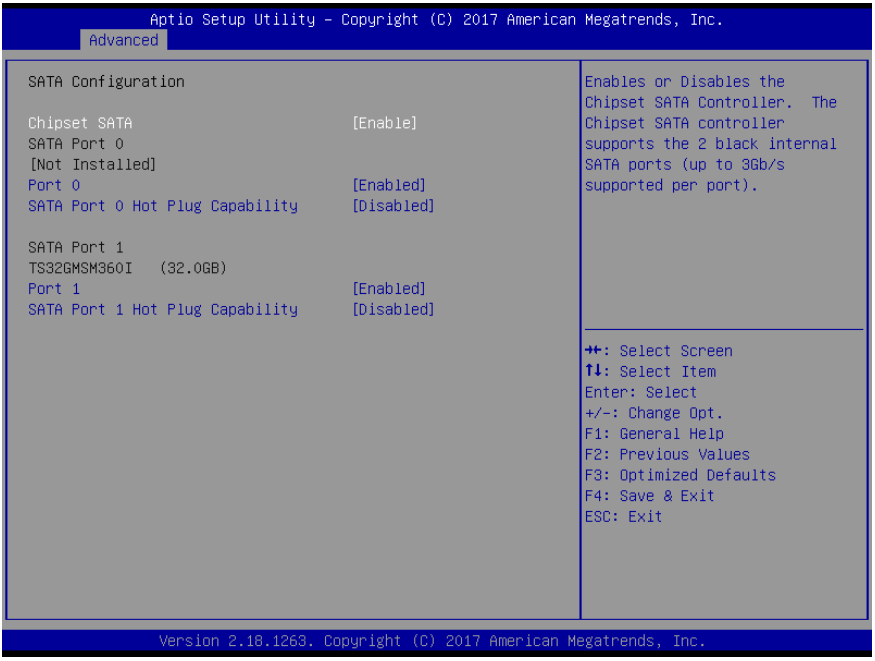


Options summary:

C-States	Disabled	Optimal Default, Failsafe Default
	Enabled	
Enabled/Disable C states		
EIST	Disabled	Optimal Default, Failsafe Default
	Enabled	
Enabled/Disable Intel SpeedStep		
Turbo Mode	Disabled	Optimal Default, Failsafe Default
	Enabled	
Turbo Mode		
Power Limit 1 Enable	Disabled	Optimal Default, Failsafe Default
	Enabled	
Enabled/Disable Power Limit 1		
Intel Virtualization Technology	Disabled	Optimal Default, Failsafe Default
	Enabled	
When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology		

VT-d	Disabled	Optimal Default, Failsafe Default
	Enabled	
Enabled/Disable CPU VT-d		

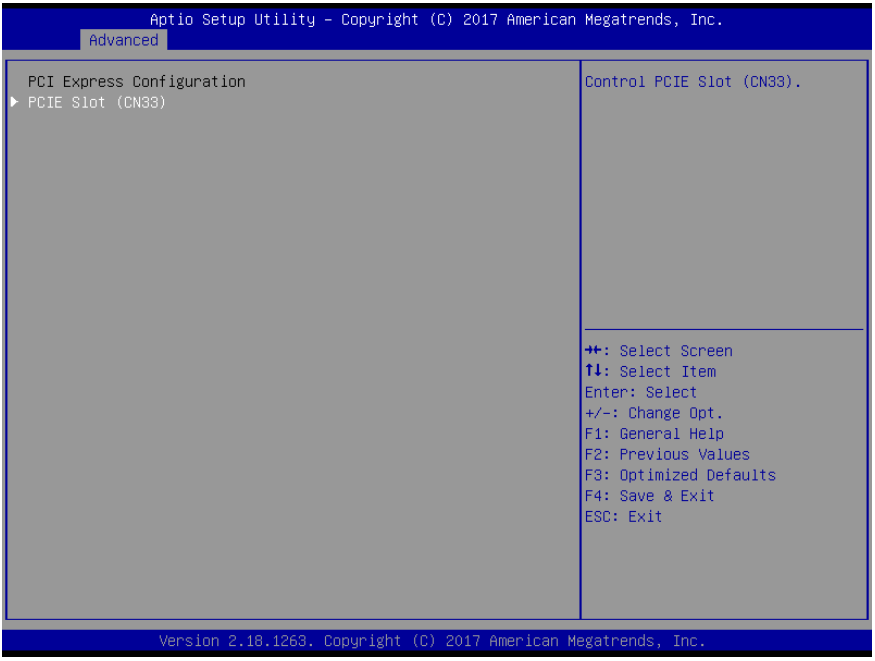
3.4.3 Advanced: SATA Configuration

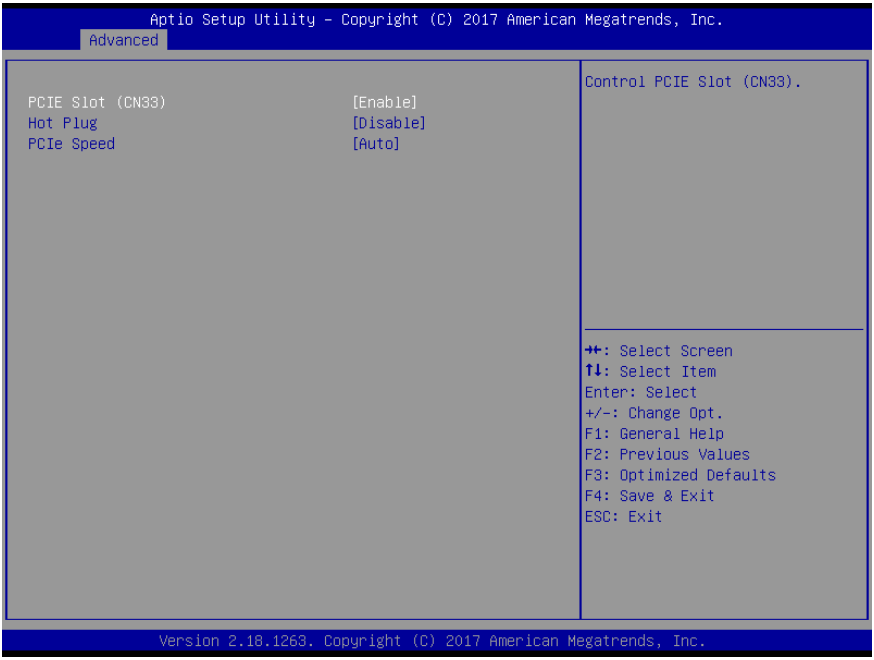


Options summary:

Chipset SATA	Enabled	Optimal Default, Failsafe Default
	Disabled	
Enables or Disables the Chipset SATA Controller. The Chipset SATA controller supports the 2 black internal SATA ports (up to 3Gb/s supported per port)		
Port 0	Enabled	Optimal Default, Failsafe Default
	Disabled	
Enable or Disable SATA Port.		
SATA Port 0 Hot PlugCapability	Enabled	
	Disabled	Optimal Default, Failsafe Default
If enabled, SATA port will be reported as Hot Plug capable		
Port 1	Enabled	Optimal Default, Failsafe Default
	Disabled	
Enable or Disable SATA Port.		
SATA Port 1 Hot PlugCapability	Enabled	
	Disabled	Optimal Default, Failsafe Default
If enabled, SATA port will be reported as Hot Plug capable		

3.4.4 Advanced: PCI Express Configuration

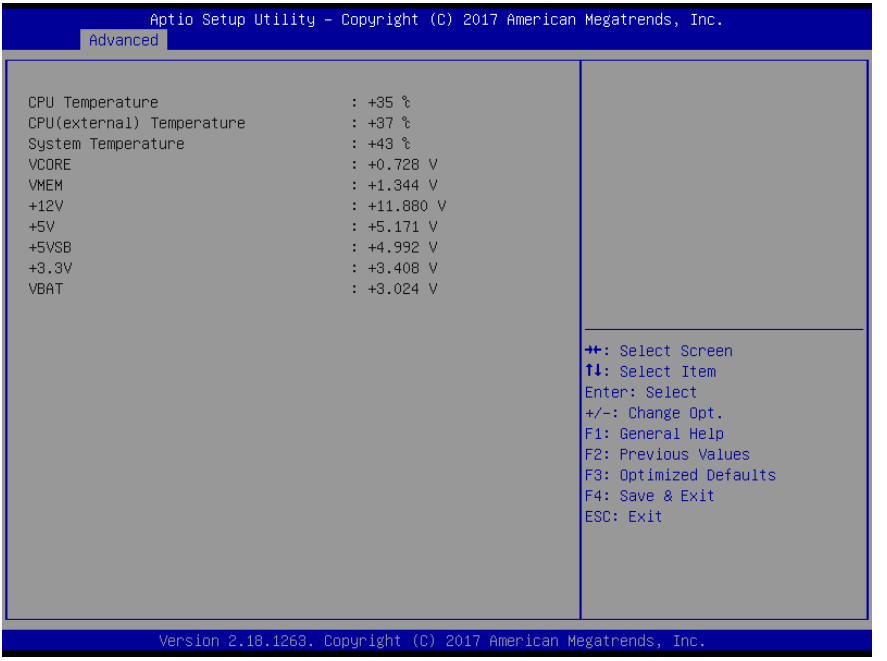




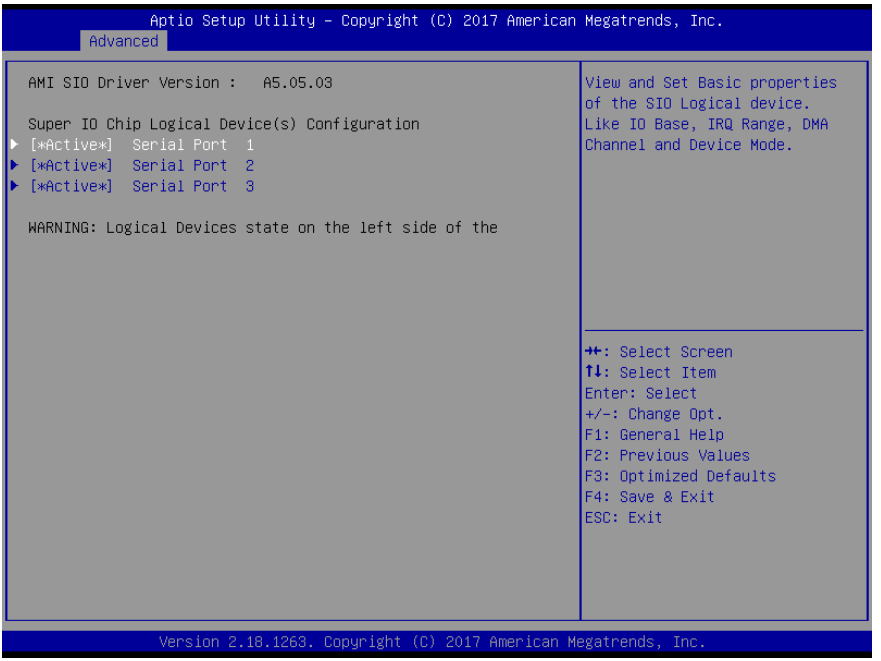
Options summary:

PCIE Slot (CN33)	Enable	Optimal Default, Failsafe Default
	Disable	
Control PCIE Slot (CN33)		
Hot Plug	Disable	Optimal Default, Failsafe Default
	Enable	
PCI Express Hot Plug Enable/Disable		
PCIe Speed	Auto	Optimal Default, Failsafe Default
	Gen1	
	Gen2	
Configure PCIe Speed		

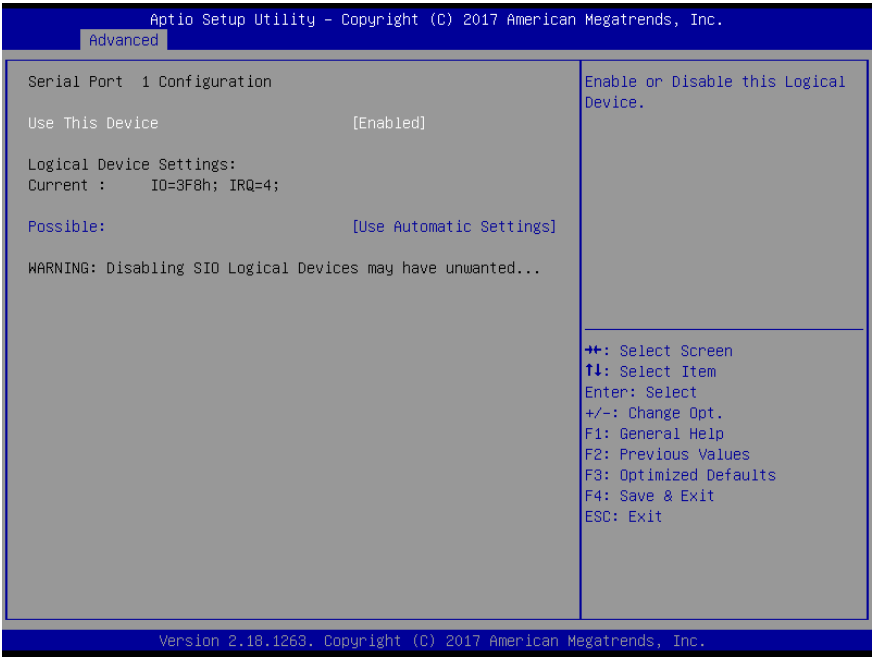
3.4.5 Advanced: Hardware Monitor



3.4.6 Advanced: SIO Configuration



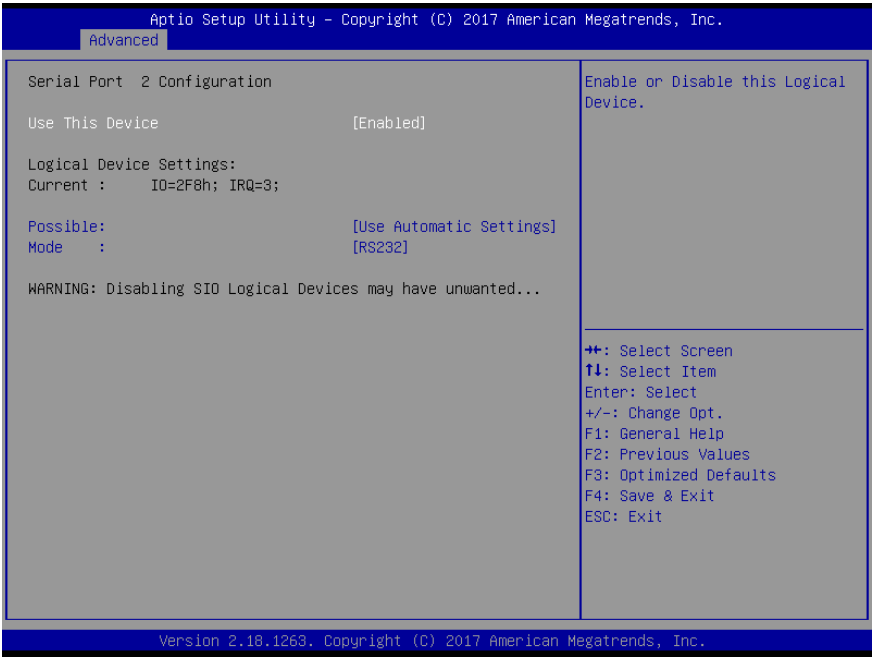
3.4.6.1 SIO Configuration: Serial Port 1 Configuration



Options summary:

Use This Device	Disabled	Optimal Default, Failsafe Default
	Enabled	
Enable or Disable this Logical Device.		
Possible:	Use Automatic Settings	Optimal Default, Failsafe Default
	IO=2F8; IRQ=3;	
	IO=3F8; IRQ=4;	
Allows user to change Device's Resource settings. New settings will be reflected on This Setup Page after System restarts.		

3.4.6.2 SIO Configuration: Serial Port 2 Configuration



Options summary:

Use This Device	Disabled	Optimal Default, Failsafe Default
	Enabled	
Enable or Disable this Logical Device.		
Possible:	Use Automatic Settings	Optimal Default, Failsafe Default
	IO=2F8; IRQ=3;	
	IO=3F8; IRQ=4;	
Allows user to change Device's Resource settings. New settings will be reflected on This Setup Page after System restarts.		
Mode:	RS232	Optimal Default, Failsafe Default
	RS422	
	RS485	
UART RS232, 422, 485 selection		

3.4.6.3 SIO Configuration: Serial Port 3 Configuration

Options summary:

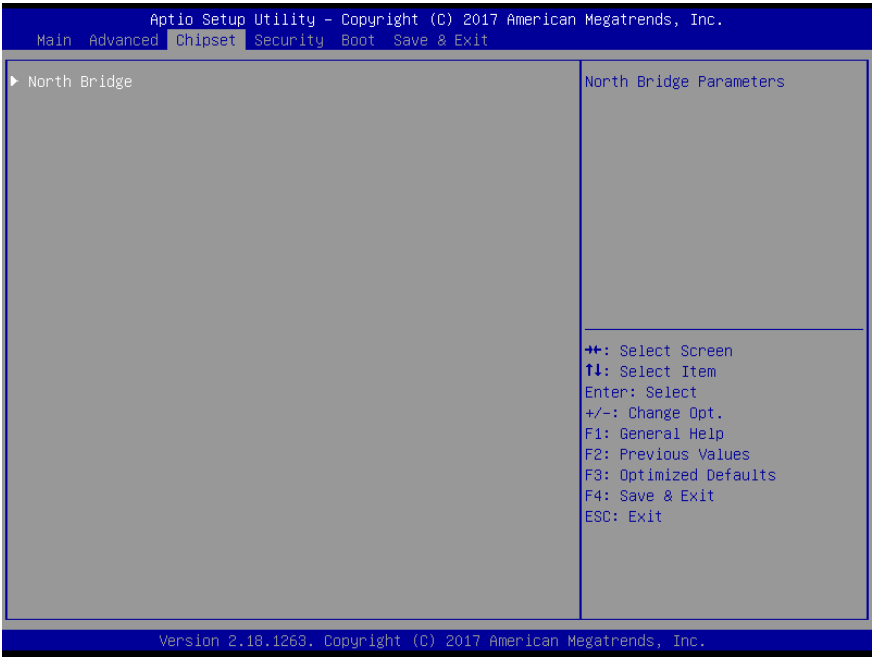
Use This Device	Disabled	Optimal Default, Failsafe Default
	Enabled	
Enable or Disable this Logical Device.		
Possible:	Use Automatic Settings	Optimal Default, Failsafe Default
	IO=3E8; IRQ=11;	
	IO=2E8; IRQ=11;	
Allows user to change Device's Resource settings. New settings will be reflected on This Setup Page after System restarts.		
Mode:	RS232	Optimal Default, Failsafe Default
	RS422	
	RS485	
UART RS232, 422, 485 selection		

3.4.7 Advanced: Power Management

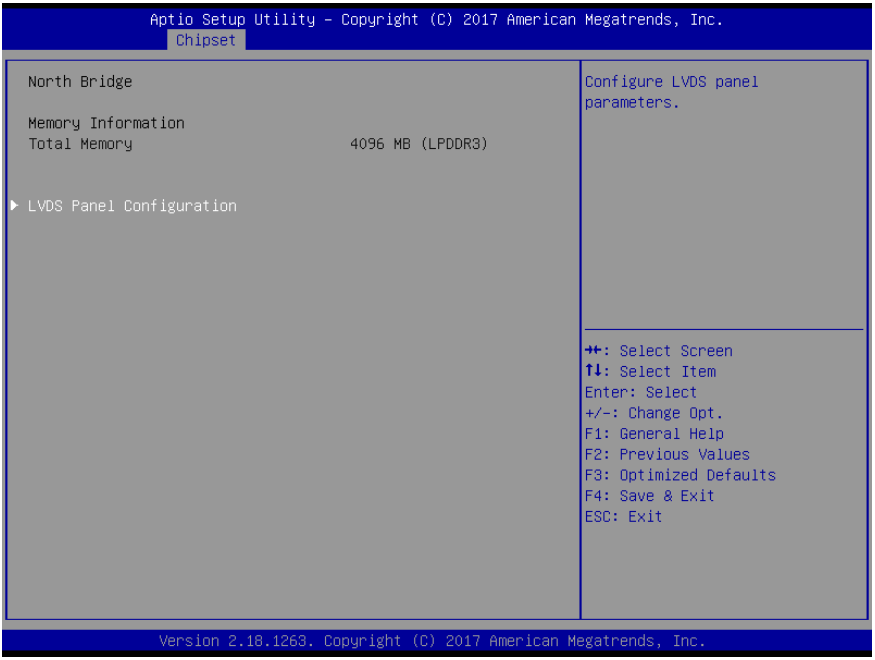
Options summary:

Power Mode	ATX Type	Optimal Default, Failsafe Default
	AT Type	
Select power supply mode.		
Restore AC Power Loss	Last State	Optimal Default, Failsafe Default
	Always On	
Select power state when power is re-applied after a power failure.		
RTC wake system from S5	Disabled	Optimal Default, Failsafe Default
	Fixed Time	
	Dynamic Time	
Fixed Time: System will wake on the hr::min::sec specified./n Dynamic Time: System will wake on the current time + Increase minute(s)		

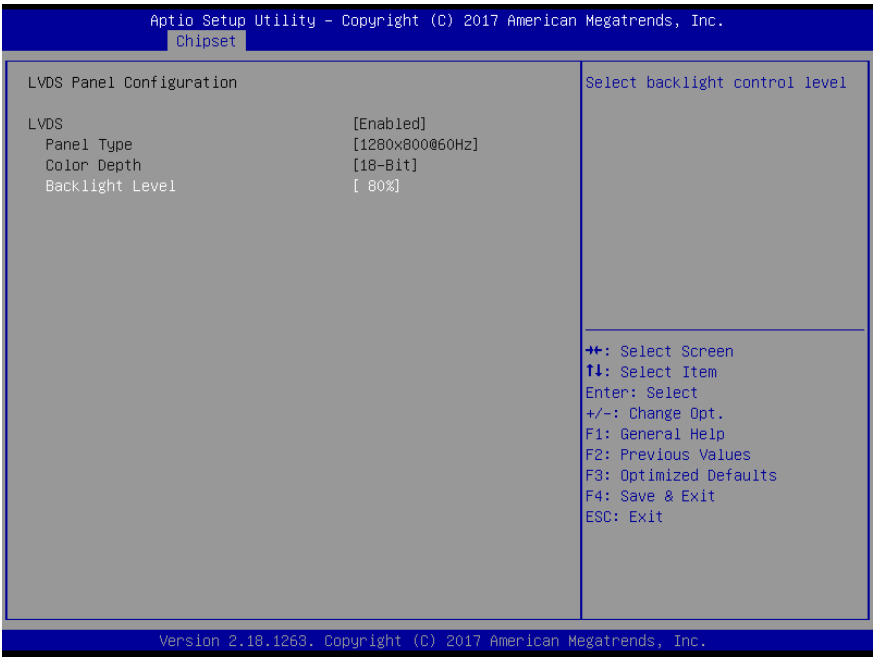
3.5 Setup submenu: Chipset



3.5.1 North Bridge



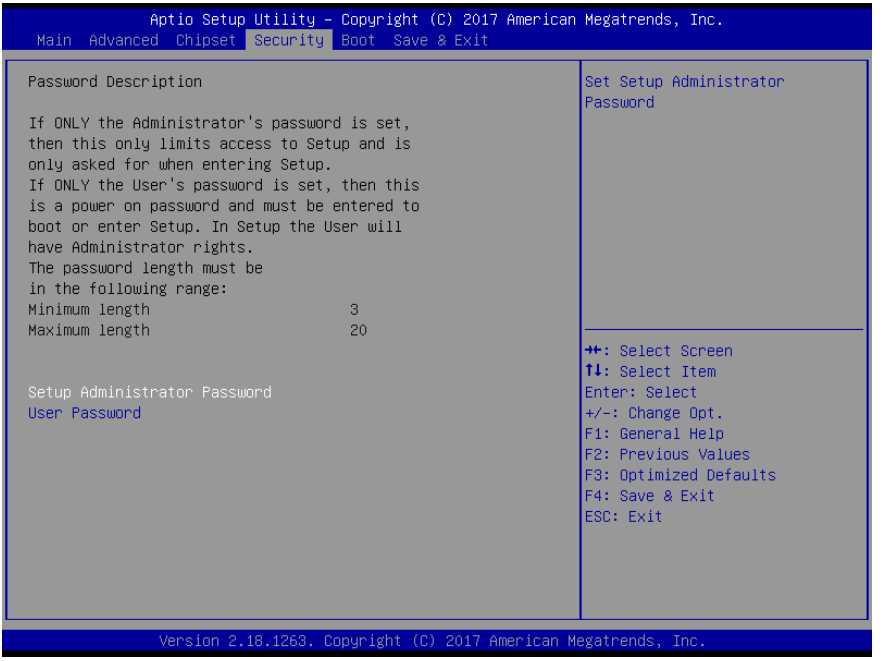
3.5.1.1 LVDS Panel Configuration



Options summary:

Backlight Level	10%	Optimal Default, Failsafe Default
	20%	
	30%	
	40%	
	50%	
	60%	
	70%	
	80%	
	90%	
	100%	
Select backlight control level		

3.6 Setup submenu: Security



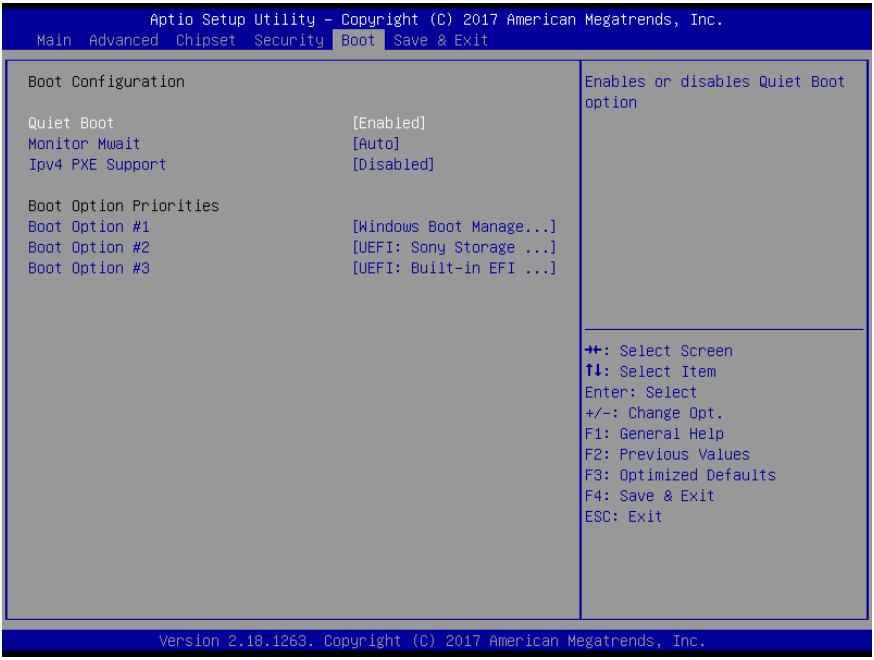
Change User/Administrator Password

You can set a User Password once an Administrator Password is set. The password will be required during boot up, or when the user enters the Setup utility. Please Note that a User Password does not provide access to many of the features in the Setup utility. Select the password you wish to set, press Enter to open a dialog box to enter your password (you can enter no more than six letters or numbers). Press Enter to confirm your entry, after which you will be prompted to retype your password for a final confirmation. Press Enter again after you have retyped it correctly.

Removing the Password

Highlight this item and type in the current password. At the next dialog box press Enter to disable password protection.

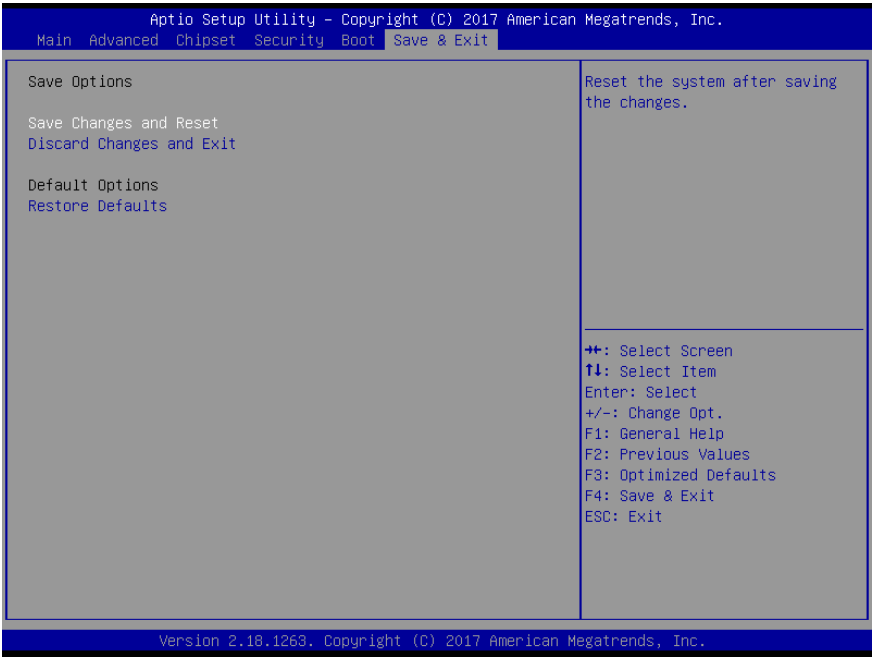
3.7 Setup submenu: Boot



Options summary:

Quiet Boot	Disabled	
	Enabled	Optimal Default, Failsafe Default
Enables or disables Quiet Boot option.		
Monitor Mwait	Disabled	
	Enabled	
	Auto	Optimal Default, Failsafe Default
Enable/Disable Monitor Mwait. To install Linux OS, please set this item to disable.		
Ipv4 PXE Support	Disabled	Optimal Default, Failsafe Default
	Enabled	
Enables Ipv4 PXE Boot Support. If disabled IPV4 PXE boot option will not be created.		

3.8 Setup submenu: Save & Exit



Chapter 4

Drivers Installation

4.1 Driver Download and Installation

Drivers for the ACP-1106 can be downloaded from the product page on the AAEON website by following this link:

https://www.aaeon.com/en/p/multi-touch-panel-pc-for-infotainment-acp-1106

Download the driver(s) you need and follow the steps below to install them.

Step 1 – Install Chipset Drivers

1. Open the **STEP 1 - CHIPSET** folder and followed by **SetupChipset.exe** file
2. Follow the instructions
3. Drivers will be installed automatically

Step 2 – Install Graphics Driver

1. Open the **STEP 2 - VGA** folder and followed by the **Setup.exe** file
2. Follow the instructions
3. Drivers will be installed automatically

Step 3 – Install Network Driver

1. Open the **STEP 3 - LAN** folder and followed by the **PROWinx64_21.1.exe** file
2. Follow the instructions
3. Drivers will be installed automatically

Step 4 – Install TXE Driver

1. Open the **STEP 4 - TXE** folder and followed by the **SetupTXE.exe** file
2. Follow the instructions
3. Drivers will be installed automatically

Step 5 – Install Touch Drivers

1. Open the **STEP 5 - TOUCH** folder and select your OS
2. Open the s**etup.exe** file in the folder
3. Follow the instructions
4. Drivers will be installed automatically

Appendix A

Watchdog Timer Programming

A.1 Watchdog Timer Initial Program

Table 1 : SuperIO relative register table		
	Default Value	Note
Index	0x2E(Note1)	SIO MB PnP Mode Index Register 0x2E or 0x4E
Data	0x2F(Note2)	SIO MB PnP Mode Data Register 0x2F or 0x4F

Table 2 : Watchdog relative register table					
	LDN	Register	BitNum	Value	Note
Timer Counter	0x07(Note3)	0xF6(Note4)		(Note24)	Time of watchdog timer (0~255) This register is byte access
Counting Unit	0x07(Note5)	0xF5(Note6)	3(Note7)	0(Note8)	Select time unit. 0: second 1: minute
Watchdog Enable	0x07(Note9)	0xF5(Note10)	5(Note11)	1(Note12)	0: Disable 1: Enable
Timeout Status	0x07(Note13)	0xF5(Note14)	6(Note15)	1	1: Clear timeout status
Output Mode	0x07(Note16)	0xF5(Note17)	4(Note18)	1(Note19)	Select WDTRST# output mode 0: level 1: pulse
WDTRST output	0x07(Note20)	0xFA(Note21)	0(Note22)	1(Note23)	Enable/Disable time out output via WDTRST# 0: Disable 1: Enable

```
*********************************************************************************
// SuperIO relative definition (Please reference to Table 1)
#define byte   SIOIndex   //This parameter is represented from Note1
#define byte   SIOData    //This parameter is represented from Note2
#define   void   IOWriteByte(byte IOPort, byte Value);
#define   byte   IOReadByte(byte IOPort);
// Watch Dog relative definition (Please reference to Table 2)
#define byte   TimerLDN   //This parameter is represented from Note3
#define byte   TimerReg   //This parameter is represented from Note4
#define byte   TimerVal   // This parameter is represented from Note24
#define byte   UnitLDN    //This parameter is represented from Note5
#define byte   UnitReg    //This parameter is represented from Note6
#define byte   UnitBit    //This parameter is represented from Note7
#define byte   UnitVal    //This parameter is represented from Note8
#define byte   EnableLDN   //This parameter is represented from Note9
#define byte   EnableReg   //This parameter is represented from Note10
#define byte   EnableBit   //This parameter is represented from Note11
#define byte   EnableVal   //This parameter is represented from Note12
#define byte   StatusLDN   // This parameter is represented from Note13
#define byte   StatusReg   // This parameter is represented from Note14
#define byte   StatusBit   // This parameter is represented from Note15
#define byte   ModeLDN   // This parameter is represented from Note16
#define byte   ModeReg   // This parameter is represented from Note17
#define byte   ModeBit   // This parameter is represented from Note18
#define byte   ModeVal   // This parameter is represented from Note19
#define byte   WDTRstLDN   // This parameter is represented from Note20
#define byte   WDTRstReg   // This parameter is represented from Note21
#define byte   WDTRstBit   // This parameter is represented from Note22
#define byte   WDTRstVal   // This parameter is represented from Note23
*********************************************************************************
```

```
*********************************************************************************
VOID   Main(){
       // Procedure : AaeonWDTConfig
       // (byte)Timer : Time of WDT timer.(0x00~0xFF)
       // (boolean)Unit : Select time unit(0: second, 1: minute).
       AaeonWDTConfig();

       // Procedure : AaeonWDTEnable
        // This procudure will enable the WDT counting.
       AaeonWDTEnable();
}
*********************************************************************************
```

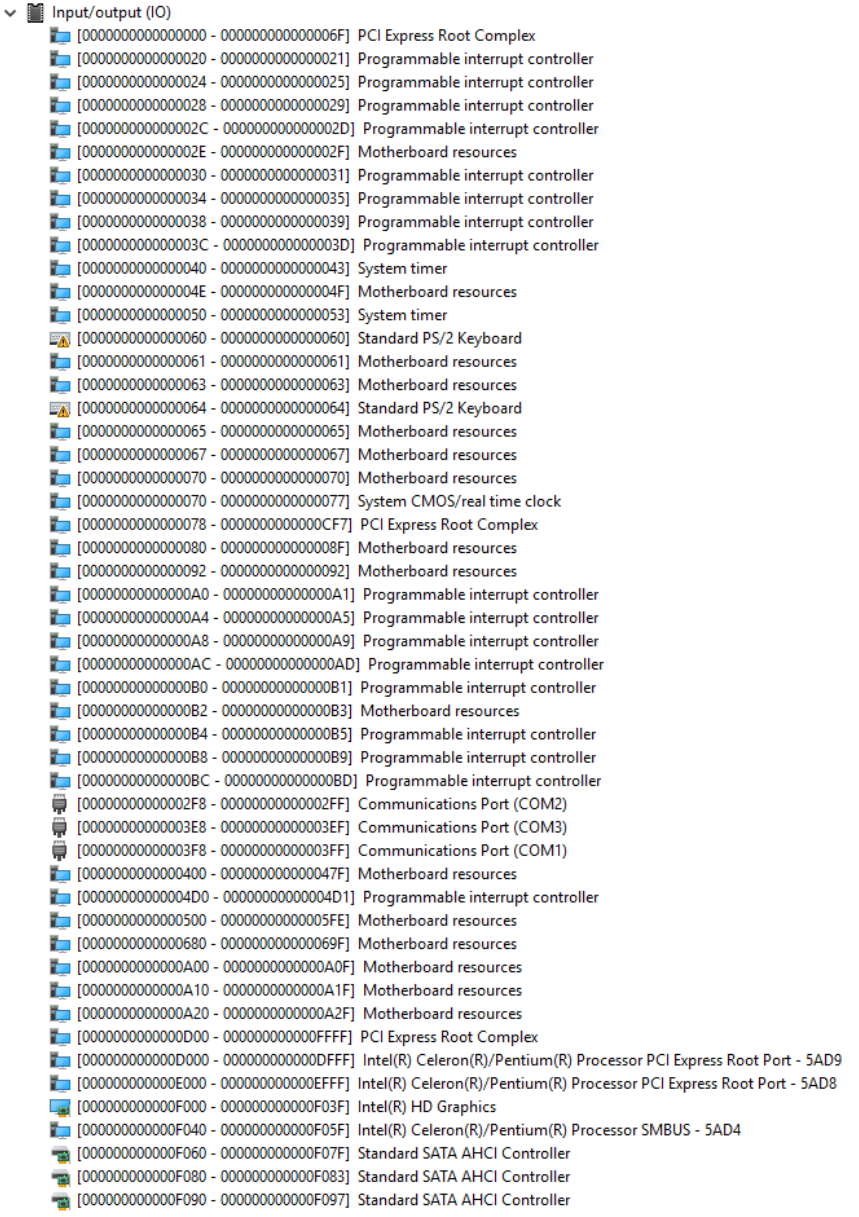
```
*********************************************************************************
// Procedure : AaeonWDTEnable
VOID    AaeonWDTEnable (){
        WDTEnableDisable(EnableLDN, EnableReg, EnableBit, 1);
}

// Procedure : AaeonWDTConfig
VOID    AaeonWDTConfig (){
         // Disable WDT counting
         WDTEnableDisable(EnableLDN, EnableReg, EnableBit, 0);
         // Clear Watchdog Timeout Status
         WDTClearTimeoutStatus();
         // WDT relative parameter setting
         WDTParameterSetting();
}

VOID    WDTEnableDisable(byte LDN, byte Register, byte BitNum, byte Value){
       SIOBitSet(LDN, Register, BitNum, Value);
}

VOID    WDTParameterSetting(){
         // Watchdog Timer counter setting
        SIOByteSet(TimerLDN, TimerReg, TimerVal);
       // WDT counting unit setting
       SIOBitSet(UnitLDN, UnitReg, UnitBit, UnitVal);
       // WDT output mode setting, level / pulse
       SIOBitSet(ModeLDN, ModeReg, ModeBit, ModeVal);
        // Watchdog timeout output via WDTRST#
        SIOBitSet(WDTRstLDN, WDTRstReg, WDTRstBit, WDTRstVal);
}

VOID    WDTClearTimeoutStatus(){
       SIOBitSet(StatusLDN, StatusReg, StatusBit, 1);
}
*********************************************************************************
```

```
*****************************************************************************
VOID   SIOEnterMBPnPMode(){
       IOWriteByte(SIOIndex, 0x87);
       IOWriteByte(SIOIndex, 0x87);
}

VOID   SIOExitMBPnPMode(){
       IOWriteByte(SIOIndex, 0xAA);
}

VOID   SIOSelectLDN(byte LDN){
        IOWriteByte(SIOIndex, 0x07); // SIO LDN Register Offset = 0x07
       IOWriteByte(SIOData, LDN);
}

VOID   SIOBitSet(byte LDN, byte Register, byte BitNum, byte Value){
       Byte TmpValue;

       SIOEnterMBPnPMode();
        SIOSelectLDN(byte LDN);
       IOWriteByte(SIOIndex, Register);
       TmpValue = IOReadByte(SIOData);
       TmpValue &= ~(1 << BitNum);
       TmpValue |= (Value << BitNum);
       IOWriteByte(SIOData, TmpValue);
       SIOExitMBPnPMode();
}

VOID   SIOByteSet(byte LDN, byte Register, byte Value){
       SIOEnterMBPnPMode();
        SIOSelectLDN(LDN);
       IOWriteByte(SIOIndex, Register);
        IOWriteByte(SIOData, Value);
        SIOExitMBPnPMode();
}
*****************************************************************************
```

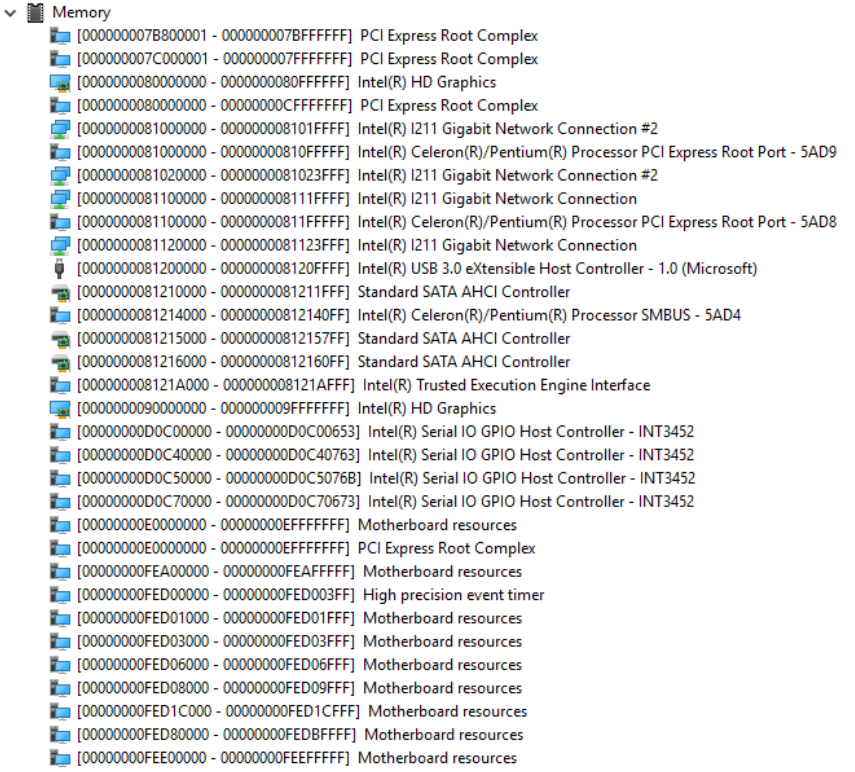
Appendix B

I/O Information

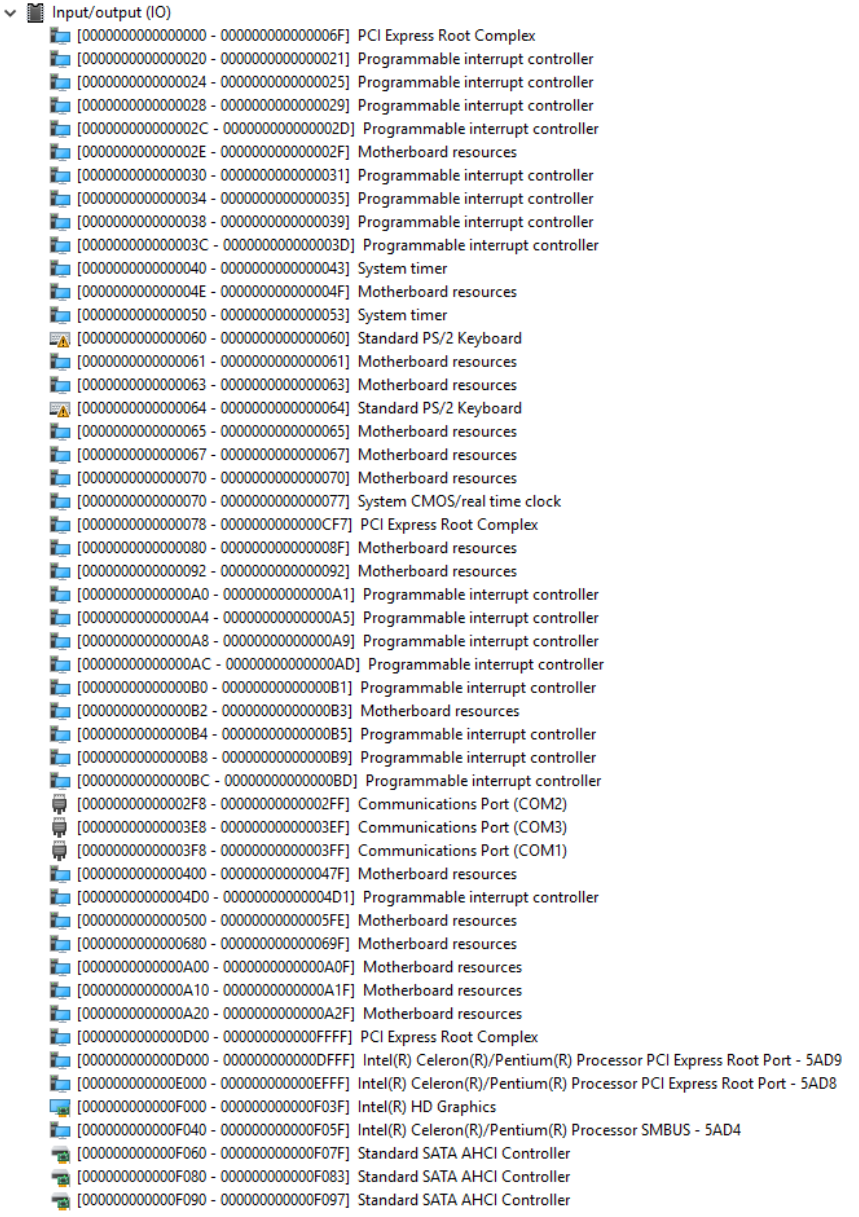
B.1 I/O Address Map



B.2 Memory Address Map



B.3 IRQ Mapping Chart



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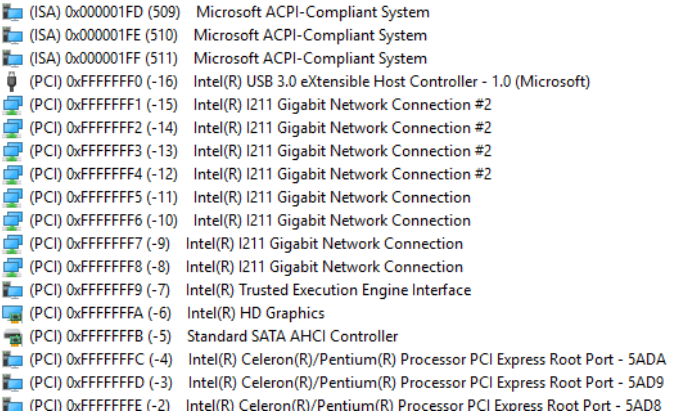
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(PCI) 0xFFFFFFF1 (-15) Intel(R) I211 Gigabit Network Connection #2
(PCI) 0xFFFFFFF2 (-14) Intel(R) I211 Gigabit Network Connection #2
(PCI) 0xFFFFFFF3 (-13) Intel(R) I211 Gigabit Network Connection #2
(PCI) 0xFFFFFFF4 (-12) Intel(R) I211 Gigabit Network Connection #2
(PCI) 0xFFFFFFF5 (-11) Intel(R) I211 Gigabit Network Connection
(PCI) 0xFFFFFFF6 (-10) Intel(R) I211 Gigabit Network Connection
(PCI) 0xFFFFFFF7 (-9) Intel(R) I211 Gigabit Network Connection
(PCI) 0xFFFFFFF8 (-8) Intel(R) I211 Gigabit Network Connection
(PCI) 0xFFFFFFF9 (-7) Intel(R) Trusted Execution Engine Interface
(PCI) 0xFFFFFFFA (-6) Intel(R) HD Graphics
(PCI) 0xFFFFFFFB (-5) Standard SATA AHCI Controller
(PCI) 0xFFFFFFFC (-4) Intel(R) Celeron(R)/Pentium(R) Processor PCI Express Root Port - 5ADA
(PCI) 0xFFFFFFFD (-3) Intel(R) Celeron(R)/Pentium(R) Processor PCI Express Root Port - 5AD9
(PCI) 0xFFFFFFFE (-2) Intel(R) Celeron(R)/Pentium(R) Processor PCI Express Root Port - 5AD8