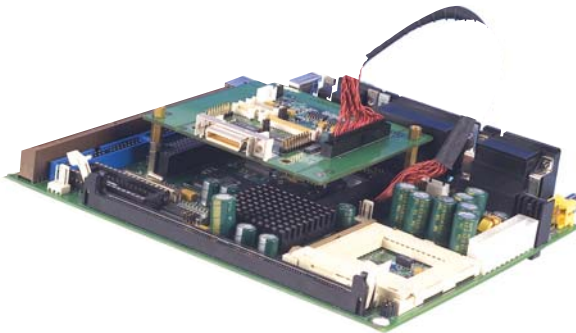
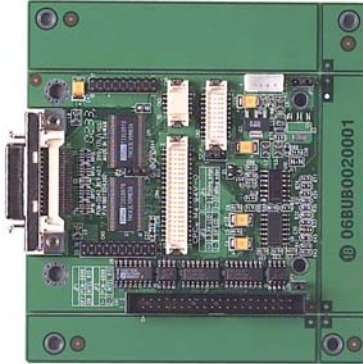


PCM-3542

DVO/TTL to LVDS Transmitter Module



PCM-3542 on the PCM-6898

Features

- Direct interface with major chipset, Intel® 815E, VIA 8604, etc., via 12-bit DVO port
- Onboard scaling function
- Panel type defined by onboard EDID data. Attach utility file provides the flexibility of user programming
- LCD power sequence is included. Backlight control signal interface is also provided
- Dual channel LVDS connector (D-sub) onboard
- Pre-programmed EDID as default data for unique LCD panel and one additional utility to provide different LCD panel type with on-Board programmed flexibility

Specifications

- TTL panel controller : SP1015E
Resolution : 18/24/36/48-bit, up to XGA (1024x768)
- LVDS Transmitter : THC63LVDM83A (optional)
Resolution : 18/24/36/48-bit, up to SXGA (1280x1024)
- Connector Interface:
 - Input : 40-pin fool-proof header, same DVO port with PCM-6896/6898, SBC-659/P
 - Output : standard dual channel LVDS connector (D-sub)
Support 18/24/36/48-bit TTL panel (DF-13 connector)(For DVO in TTL out, or TTL in LVDS out)
- Board size : 90.17mm X 95.89mm (3.55" X 3.775") PC/104 size
- Operating environment : 0~60°C (32~140°F)

PCM-3538T/R

DVO LVDS Transmitter/Receiver Module

PCM-3538T

- Using transmitter device module THC63LVDM83A chipset, transmitter converts 28 bit CMOS/TTL data into LVDS data stream.
- Scalable Bandwidth: VGA (640 x 480) to SXGA (1280 x 1024)
- Supports LVDS channel (3 data pairs & 1 clock pair) for 18/24 bit LVDS panel or up to two channel for 18/24/36/48 bit LVDS panel
- Cable Distance support: 5M or above (depend on cable quality)
- Wide frequency range: 1. One channel (20~ 85 MHz)
2. Two channel (20~170 MHz)
- 28:4 Data channel compression at up to 298 MB per second throughput.
- Input port using TTL signal with DF13 connector (2x20, 2x10, 2x5).
- Output LVDS differential signal for LVDS panel

PCM-3538R

- Using THC63LVDF84A chipset (receiver device module)
- Scalable Bandwidth: VGA (640 x 480) to SXGA (1280 x 1024)
- Supports LVDS channel (3 data pairs & 1 clock pair) for 18/24 bit LVDS panel or up to two channel for 18/24/36/48 bit LVDS panel
- Cable Distance support: 5M or above (depends on cable quality)
- Wide frequency range : 1. One channel (20~ 85 M Hz)
2. Two channel (20~170 M Hz)
- 28:4 Data channel compression at up to 298 MB per second throughput.
- Input LVDS differential signal from THC 63LCDM83A chipset (PCM-3538T, transmitter devices module)

