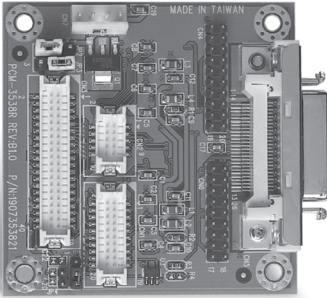


# PCM-3538R

## The Power That Solves DVO/TTL to LVDS Transmitter Module

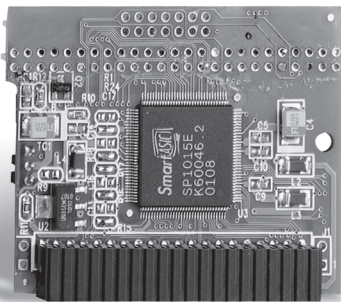


### Features

- Using **THC63LVDF84A** chipset (receiver device module)
- Scalable Bandwidth: **VGA (640 x 480) to SXGA (1280 x 1024)**
- Supports **LVDS channel (3 data pairs & 1 clock pair) for 18/24 bit LVDS panel or up to two channel for 18/24/36/48 bit LVDS panel**
- Cable Distance support: **5M or above (depends on cable quality)**
- Wide frequency range: **1. One channel (20 ~ 85 M Hz)**  
**2. Two channel (20 ~ 170 M Hz)**
- 28:4 Data channel compression at up to 298 MB per second throughput.**
- Input **LVDS differential signal from THC 63LCDM83A chipset (PCM-3538T, transmitter devices module)**

# PCM-3536

## DVO TTL Module



### Features

- Direct interface with major chipset, **Intel® 815E (PCM-6896, SBC-659/P), VIA 8604 (PCM-6898), etc., via 12-bit DVO port.**
- Unique scaling capability to support wide range resolution from **VGA to XGA including non-standard mode**
- For **TTL signal outputs, support 18/24/36/48-bit TFT LCD panel**
- Panel type defined by onboard **EDID data. Included utility file provides the flexibility of user programmability**
- LCD power sequence is included. Backlight control signal interface is also provided**
- Support **3.3V or 5V LCD panel**

### Specifications

	Two DF-13 connectors for 18/24/36/48-bit TFT LCD Panel
	3.3V/5V LCD power is provided
	Pre-programmed EDID as default data for unique LCD panel and one additional utility to provide different LCD panel type with on-board programmed flexibility
	LCD panel can support from VGA to XGA resolution including non-standard mode