

PCM-3538T

DVO LVDS Transmitter/Receiver Module



Features

- Using transmitter device module *THC63LVDM83A* chipset, transmitter converts 28 bit of CMOS/TTL data into LVDS data stream
- Scalable Bandwidth: VGA (640 x 480) to SXGA (1280 x 1024)
- Supports LVDS channel (3 data pairs & 1 clock pair) for 18/24 bit LVDS panel or up to two channel for 18/24/36/48 bit LVDS panel
- Cable Distance support: 5M or above (depend on cable quality)
- Wide frequency range:
 1. One channel (20 ~ 85 M Hz)
 2. Two channel (20 ~ 170 M Hz)
- 28:4 Data channel compression at up to 298 MB per second throughput.
- Input port using TTL signal with DF13 connector (2x20, 2x10, 2x5).
- Output LVDS differential signal for LVDS panel

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Compact Board

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Sub Compact Board

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Full-Size CPU Card

4

Half-Size CPU Card

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Media PC

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Embedded ATX Board

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PC/104 Module

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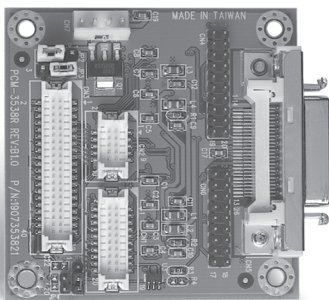
Mini PCI Module

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Accessory

PCM-3538R

DVO/TTL to LVDS Transmitter Module



Features

- Using *THC63LVDF84A* chipset (receiver device module)
- Scalable Bandwidth: VGA (640 x 480) to SXGA (1280 x 1024)
- Supports LVDS channel (3 data pairs & 1 clock pair) for 18/24 bit LVDS panel or up to two channel for 18/24/36/48 bit LVDS panel
- Cable Distance support: 5M or above (depends on cable quality)
- Wide frequency range:
 1. One channel (20 ~ 85 M Hz)
 2. Two channel (20 ~ 170 M Hz)
- 28:4 Data channel compression at up to 298 MB per second throughput.
- Input LVDS differential signal from *THC 63LCDM83A* chipset (PCM-3538T, transmitter devices module)